



## PCIe x16 connector PCle<sub>x</sub>4{PCle}**◇** +3/3 +12V +12V J2B 10146070-113Y0LF C36 10u 10u 10u B1 B1 B2 B2 B2 B2 B3 B3 B4 B4 B5 B6 B6 B6 B6 B6 B7 B7 B7 B7 B8 B8 B8 B8 B8 B8 B8 B8 B8 B1 B10 B10 B10 B11 B11 B12 B13 B13 B14 B14 B-1 B15 B16 B16 B16 B17 B18 B19 B19 B19 B19 B19 B20 B20 B21 B21 B22 B23 B23 B24 B24 B25 B26 B26 B27 B27 B28 B28 B29 B30 B30 B31 B31 B31 B31 B32 B32 B33 B33 B34 B35 B36 B37 B38 B38 B39 B40 B40 B41 B41 10146070-113Y0LF SZSMF4L12AT3G GND +3V3 I2C0{I2C}**◇**— I2CO.SCL I2CO.SDA ± C37 → PCIereFo{PCIereF} PCIeREFO.CK+ PCIeREFO.CK-PCIex4.TX0+ PCIex4.TX0-PCIex4.RX0+ PCIex4.TX1+ PCIex4.TX1-PCIe<sub>x</sub>4.RX1+ PCIe<sub>x</sub>4.RX1-PCIex4.TX2+ PCIex4.TX2-PClex4.RX2+ PCIex4.RX2-PCIex4.TX3+ PCIex4.TX3-PCIex4.RX3+ PCIex4.RX3www.antmicro.com Antmicro Ltd Sheet: /PCle-connector/ File: pcie-connector.kicad\_sch Title: OCuLink to PCIe adapter Size: A3 KiCad E.D.A. 9.0.2 Date: 2025-06-18

## +3V3 DC/DC converter Power supply ORing Ideal-diode-0 U1 TPS56C230RJER C1 100n +12V BST 1 3V3\_BST | L1 1u8/6.8A File: ideal-diode.kicad\_sch 3V3\_EN + C29 + C30 150u + 150u C2 C15 C4 22u 22u 22u 25V 25V 25V PWR PWR\_FLAG Ideal-diode-1 F1 3413.0326.11 VCC\_DC\_CONN\_1D VOUTE File: ideal-diode.kicad\_sch → PWR\_FLAG GND GND GND GND F2 70 3413.0326.11 VCC\_DC\_CONN\_2D == D8 SZSMF4L12AT3G File: ideal-diode.kicad\_sch PERST and CPRSNT control Power indicator LEDs and testpoints R15 C12 100n GND D PERST R7 100k GND PERST\_CONN R41 www.antmicro.com Antmicro Ltd Sheet: /Power/ File: power.kicad\_sch Powers need to be OK and host needs to want device out of reset for $\overline{\text{PERST}}$ to be deasserted Title: OCuLink to PCIe adapter









