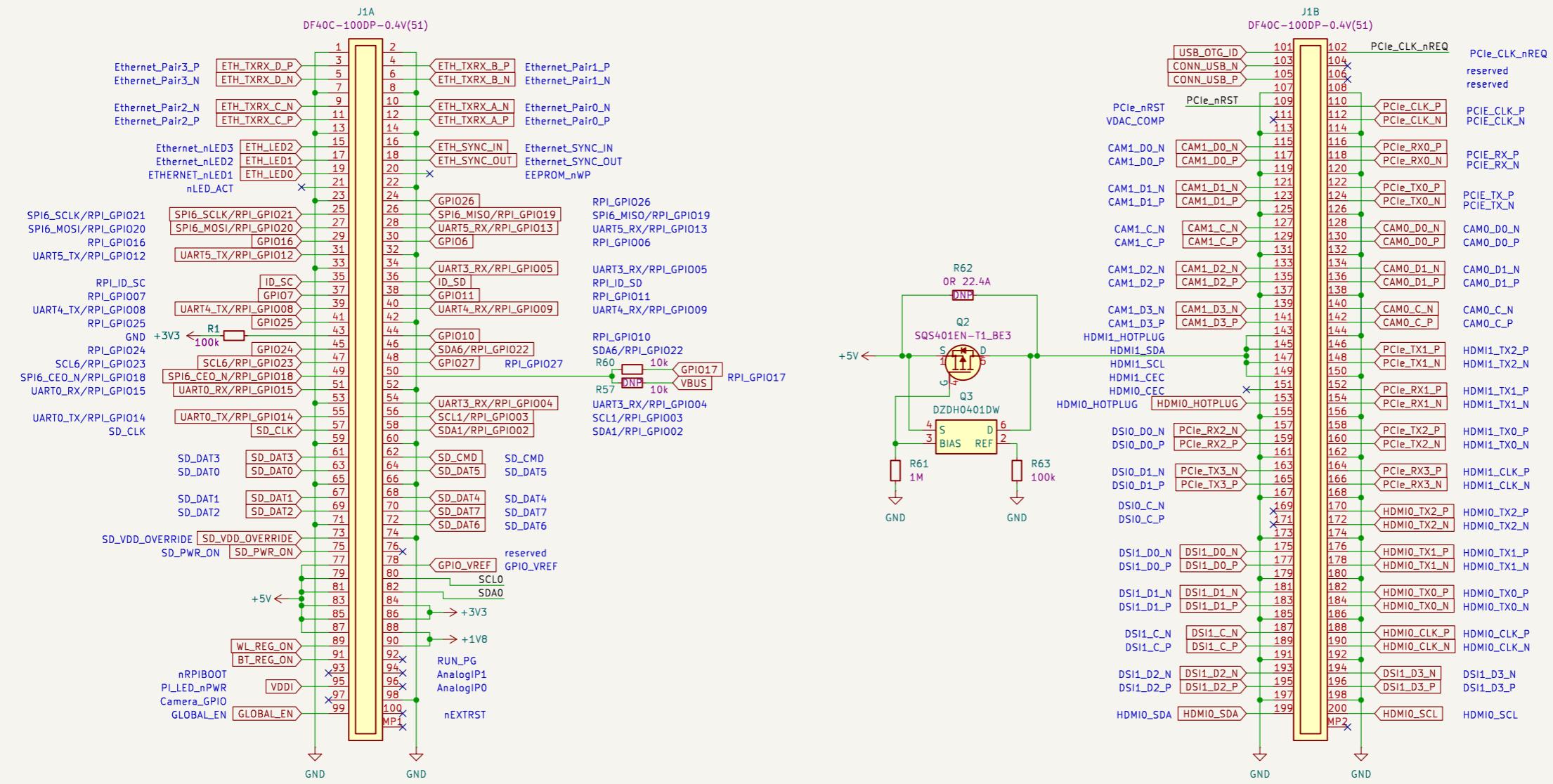
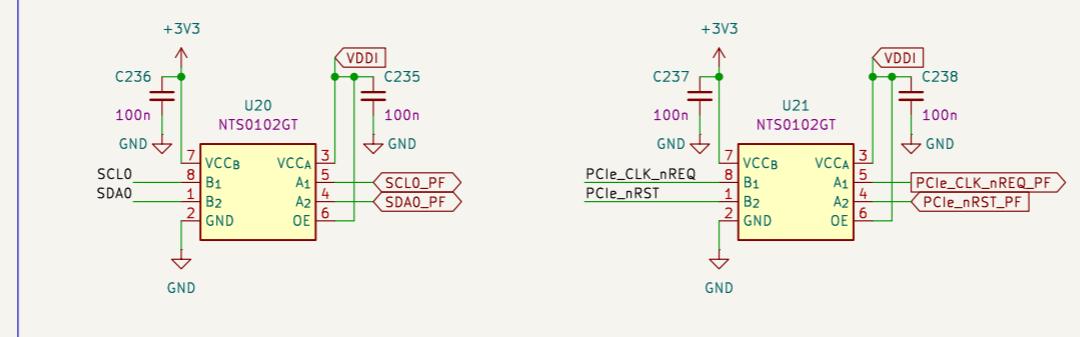


- | | |
|------|-------------------------------|
| Logo | N1
antmicro_logo |
| Logo | N2
oshw_logo |
| x1 | MP1
MP_Pad5.4mm_Drill2.7mm |
| x1 | MP2
MP_Pad5.4mm_Drill2.7mm |
| x1 | MP3
MP_Pad5.4mm_Drill2.7mm |
| x1 | MP4
MP_Pad5.4mm_Drill2.7mm |

CM4 compatible connectors

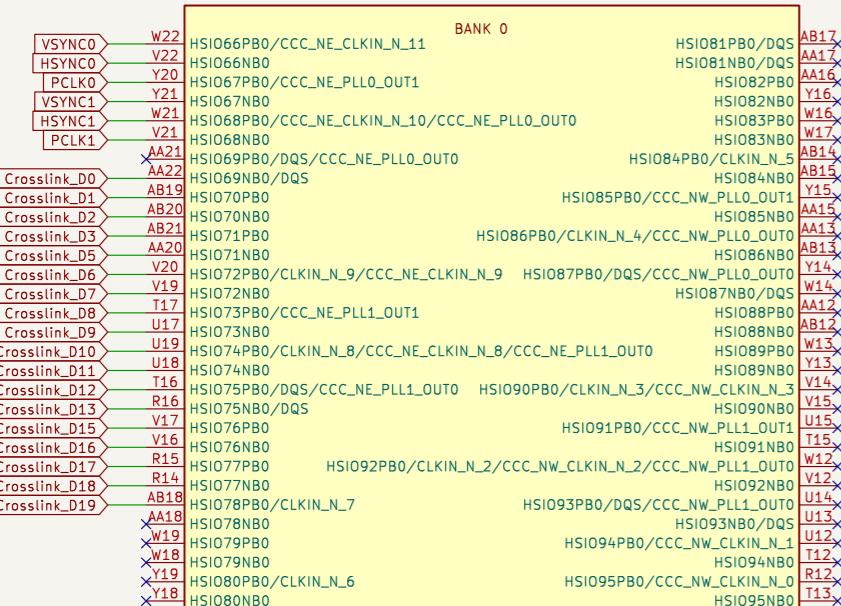


LEVEL SHIFTERS

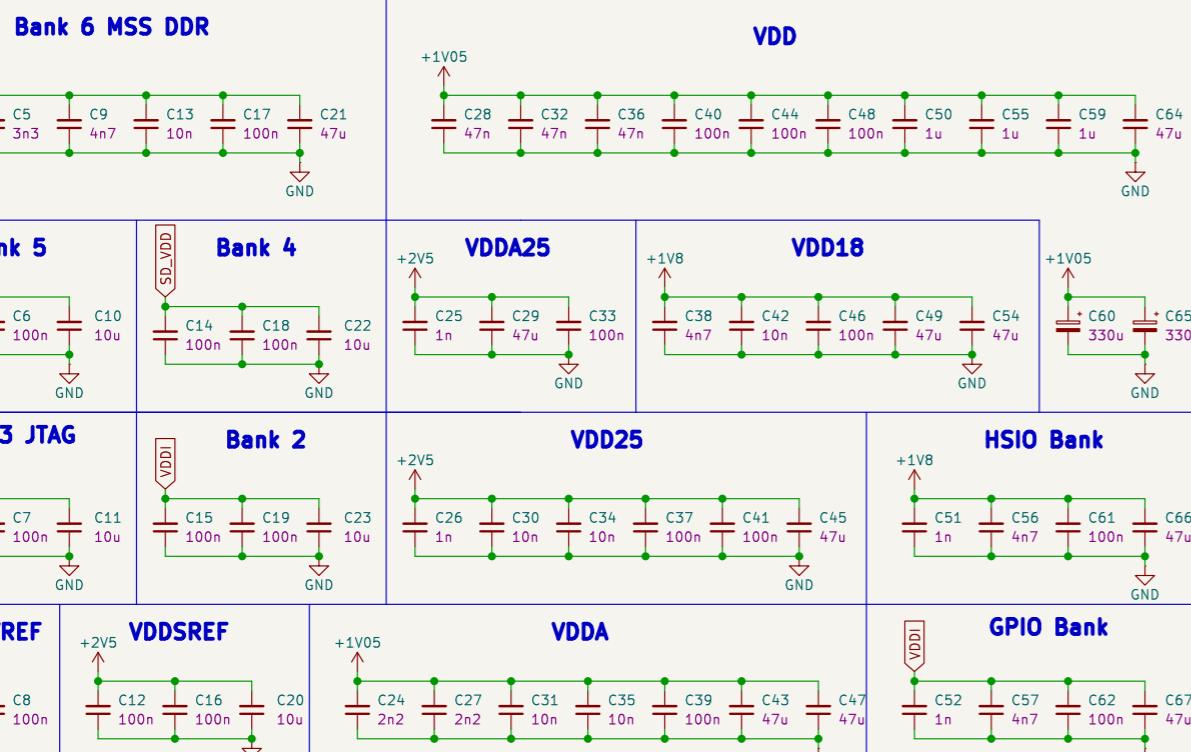




U1A
MPFS250T-FCVG484

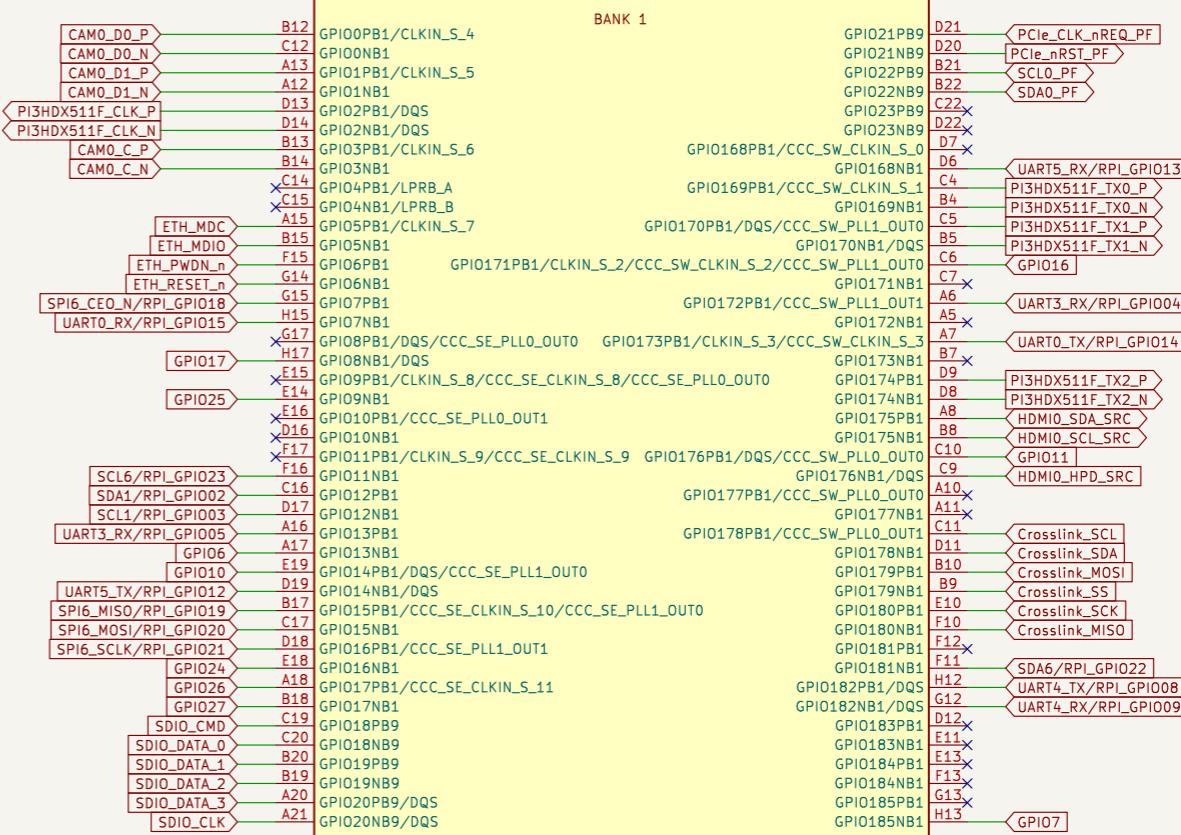


Decoupling capacitors

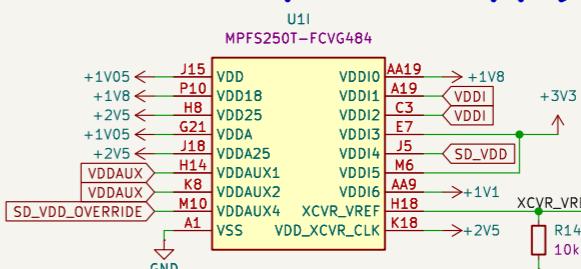


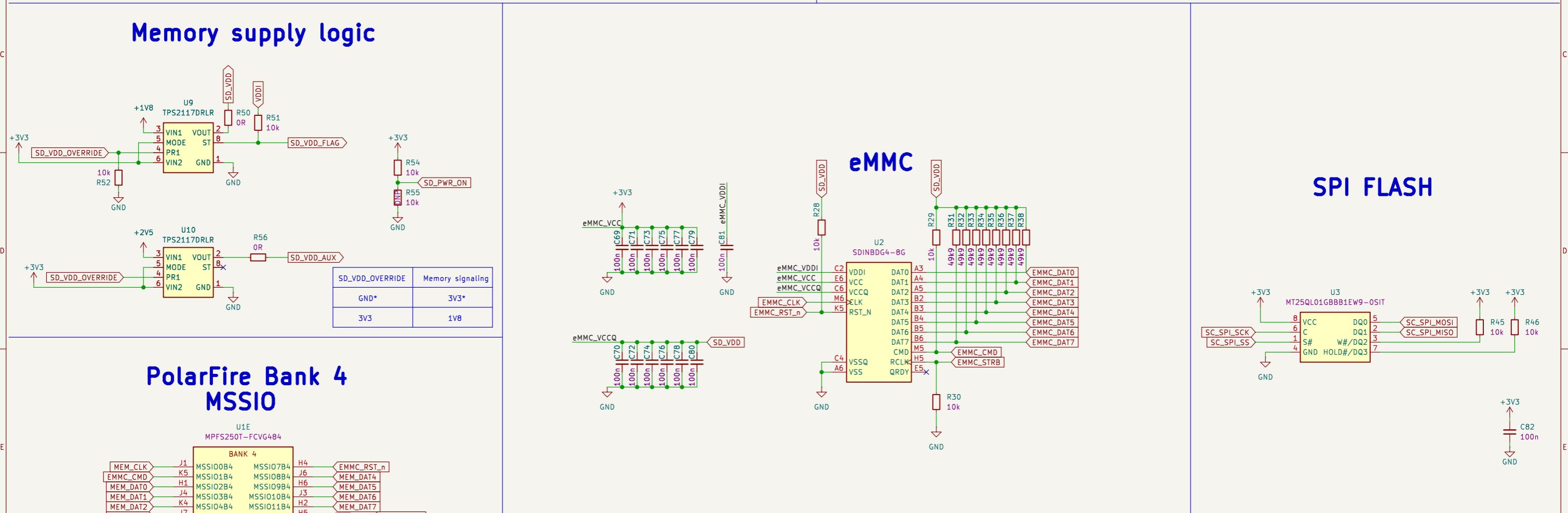
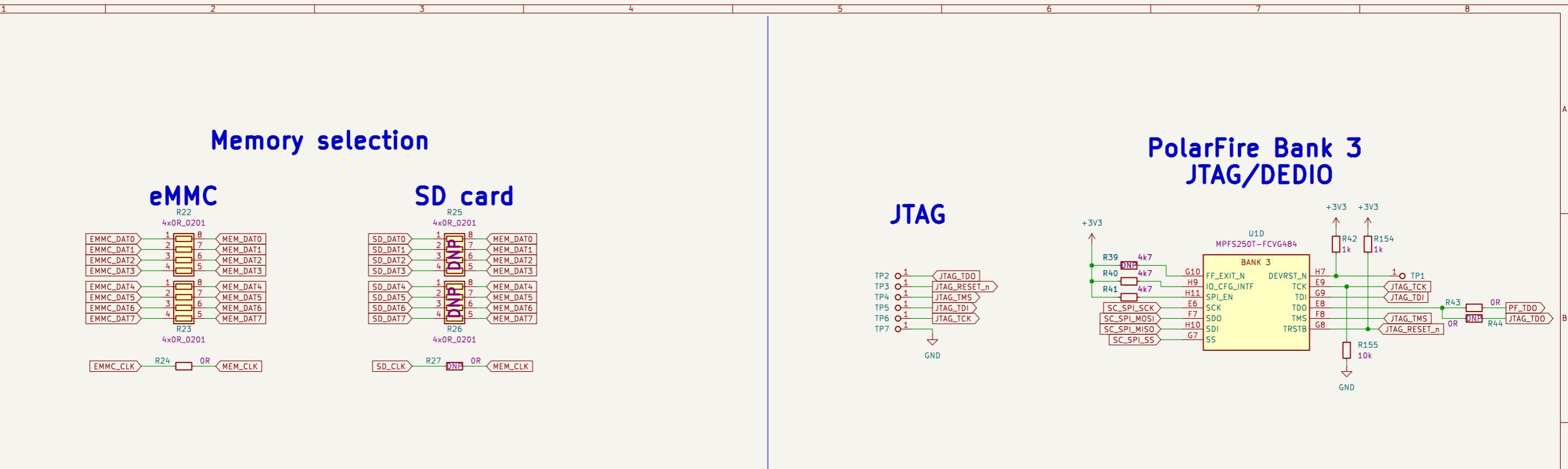
PolarFire Bank 1 GPIO

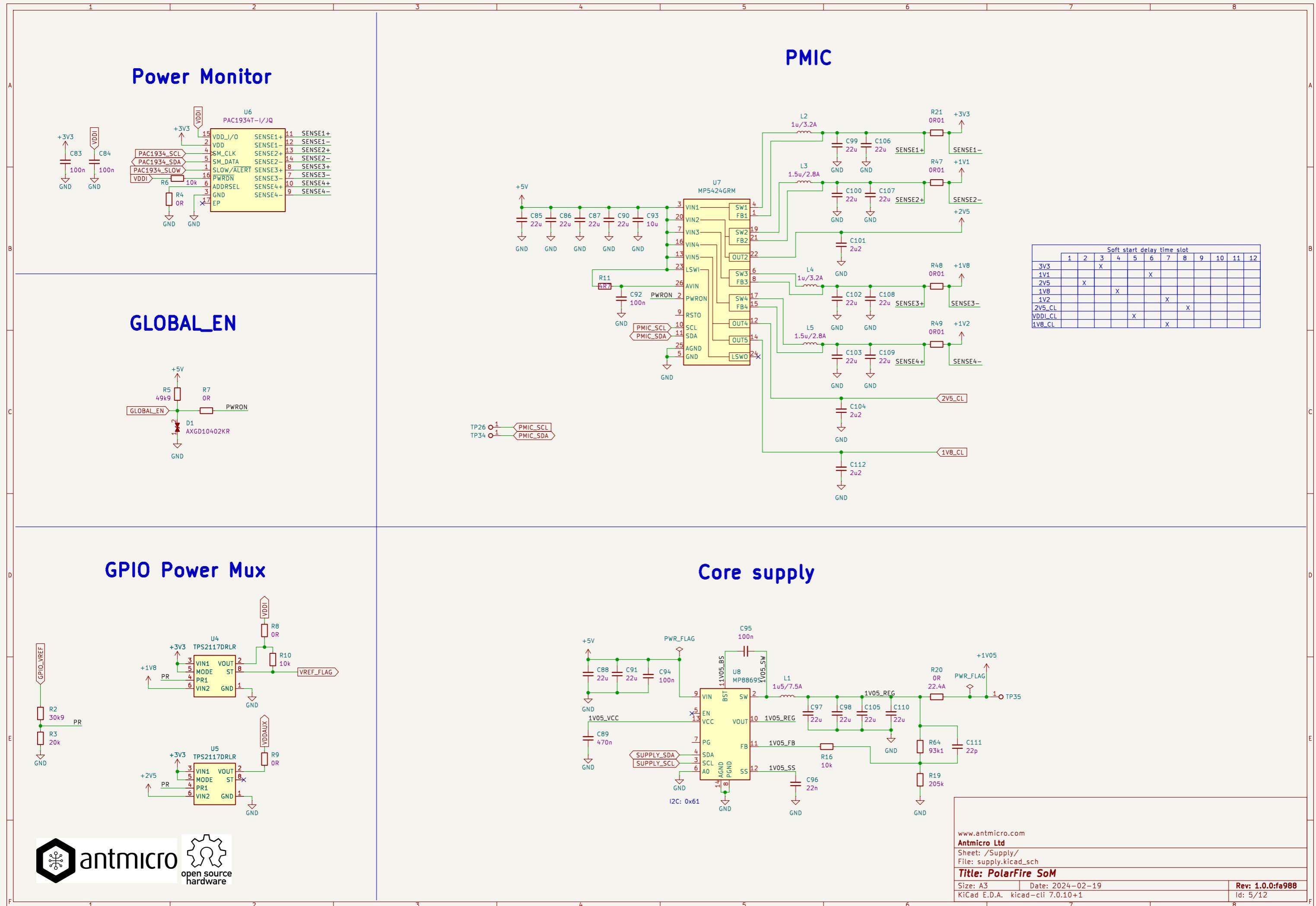
MPFS250T-FCVG484



PolarFire power supply

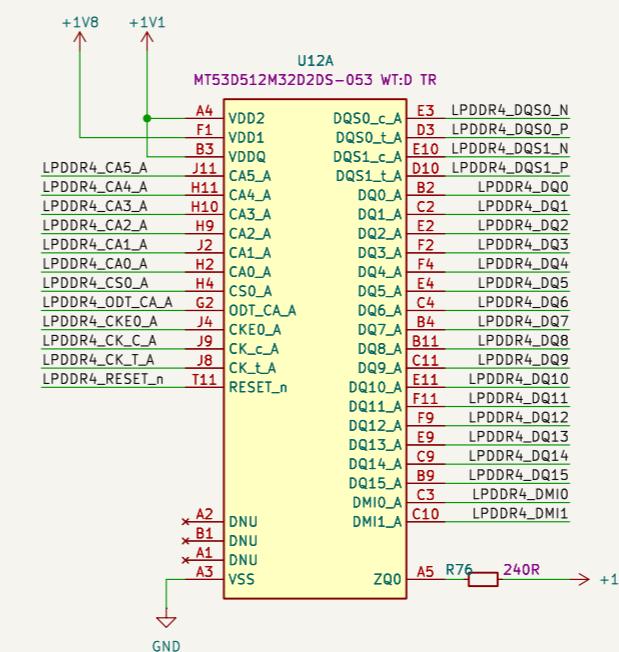
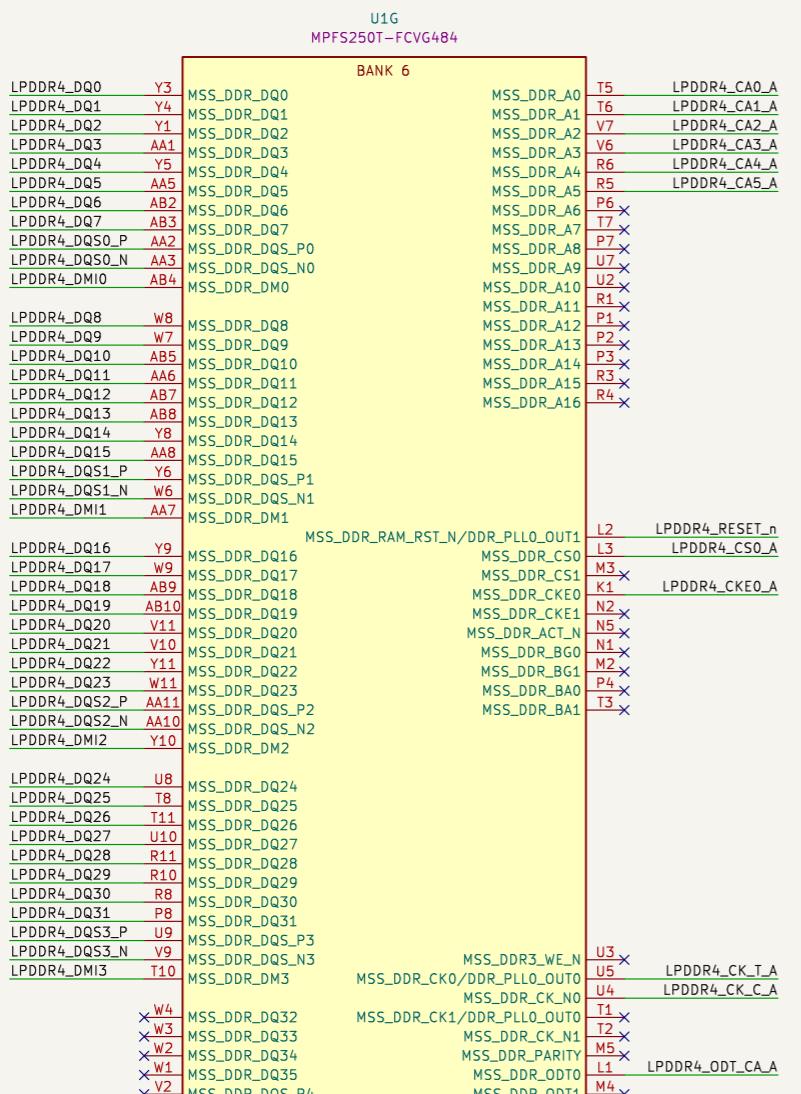




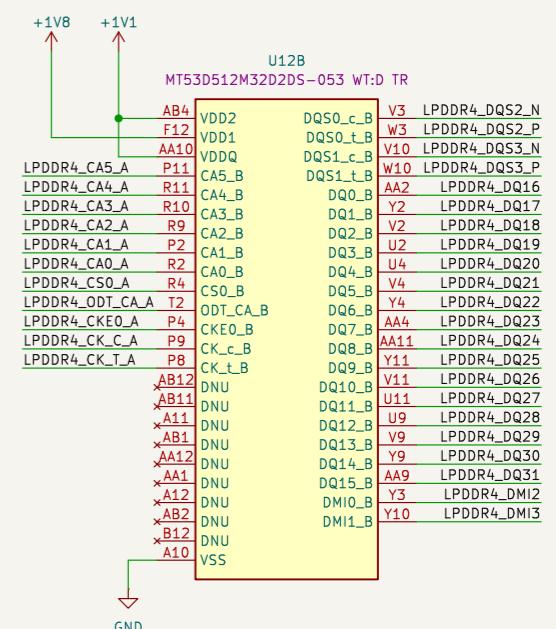


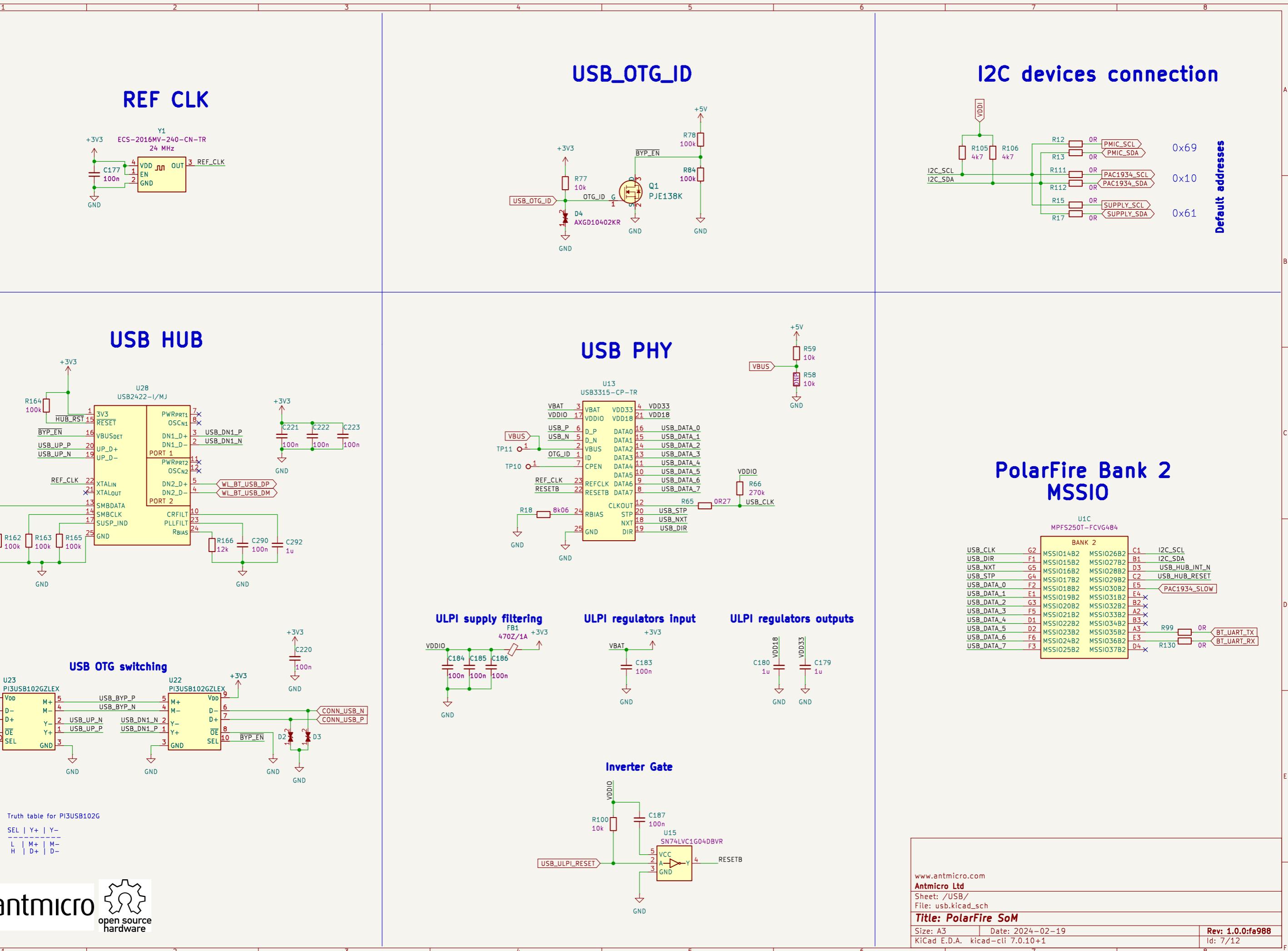
A

PolarFire Bank 6 MSS_DDR



LPDDR4





A

A

B

B

C

C

D

D

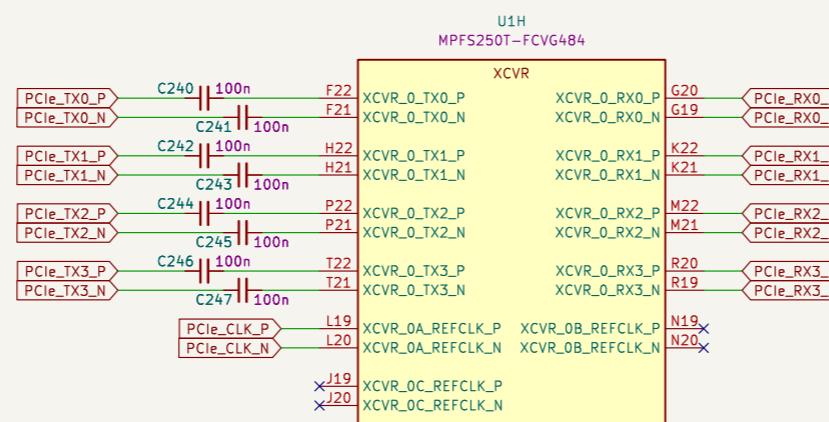
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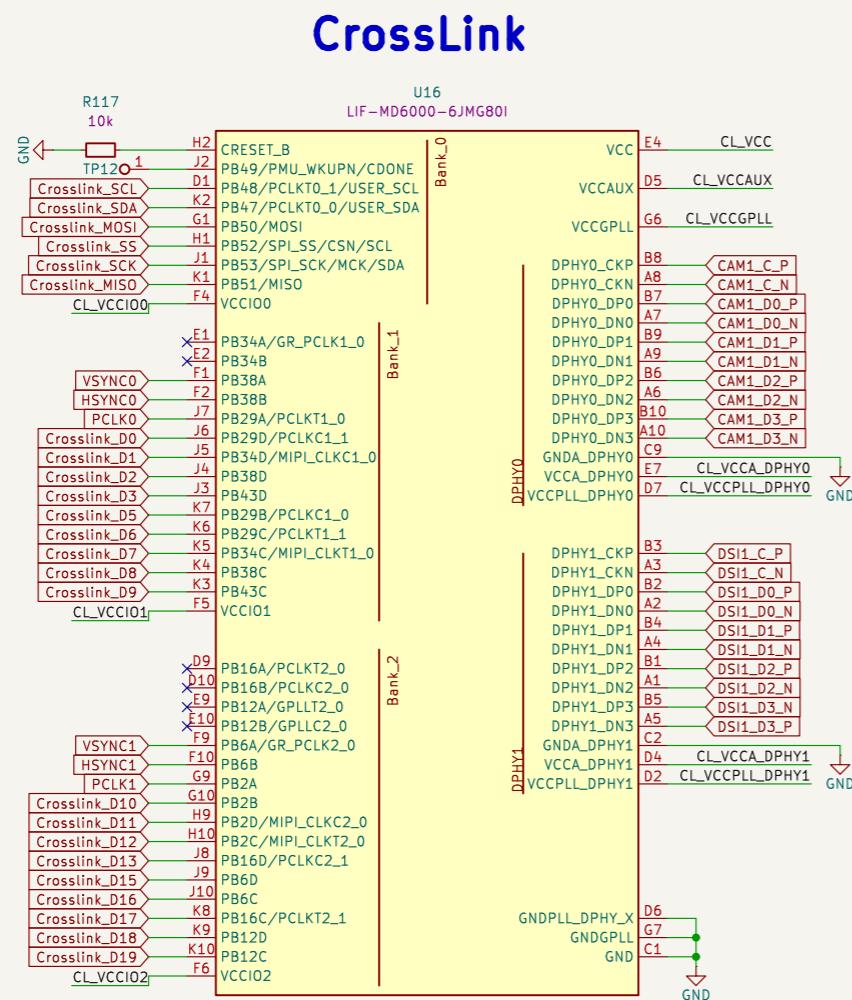
E

F

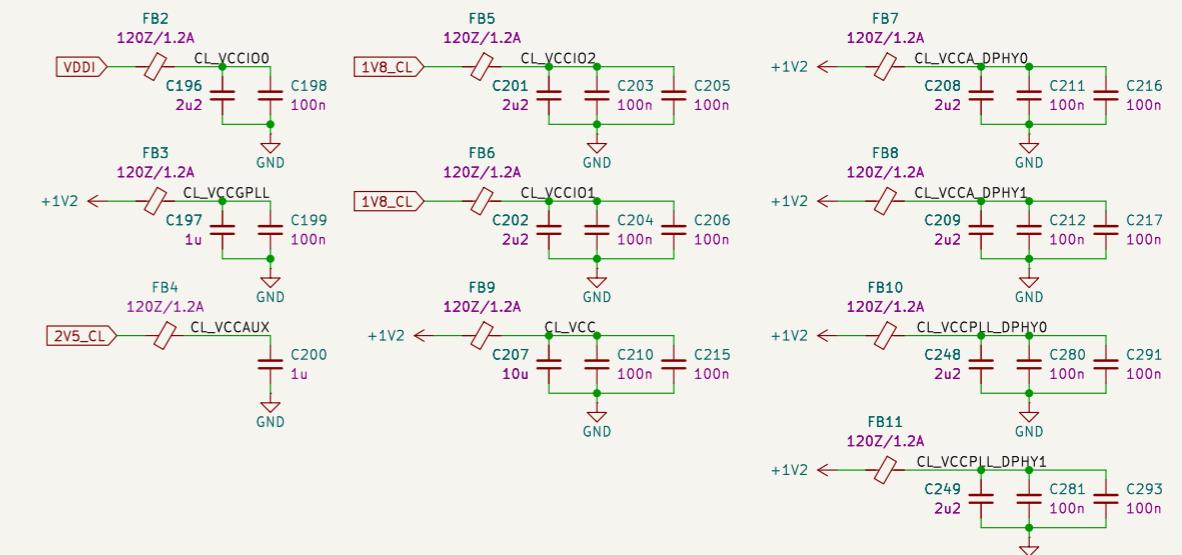
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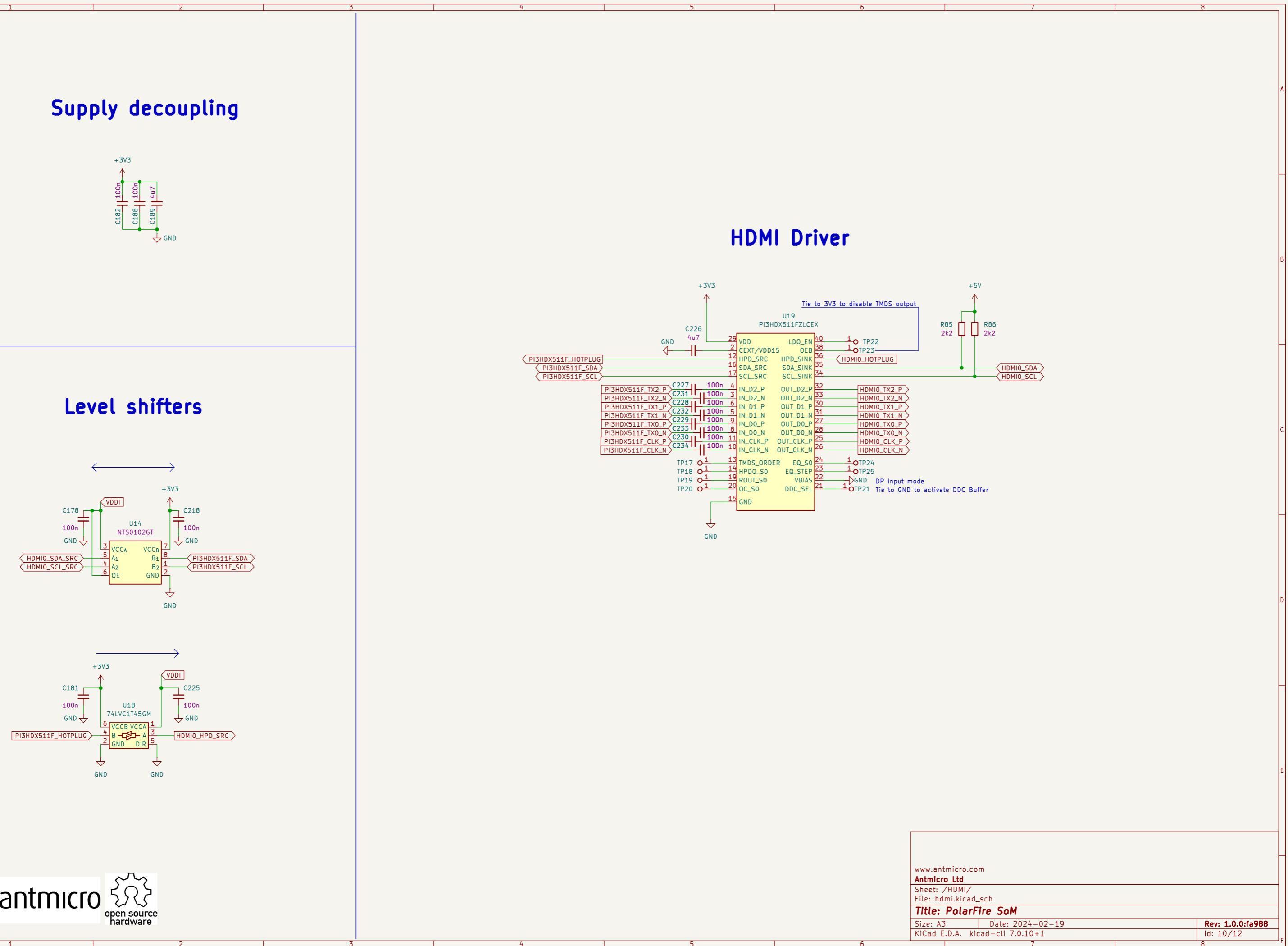
PolarFire Transceivers



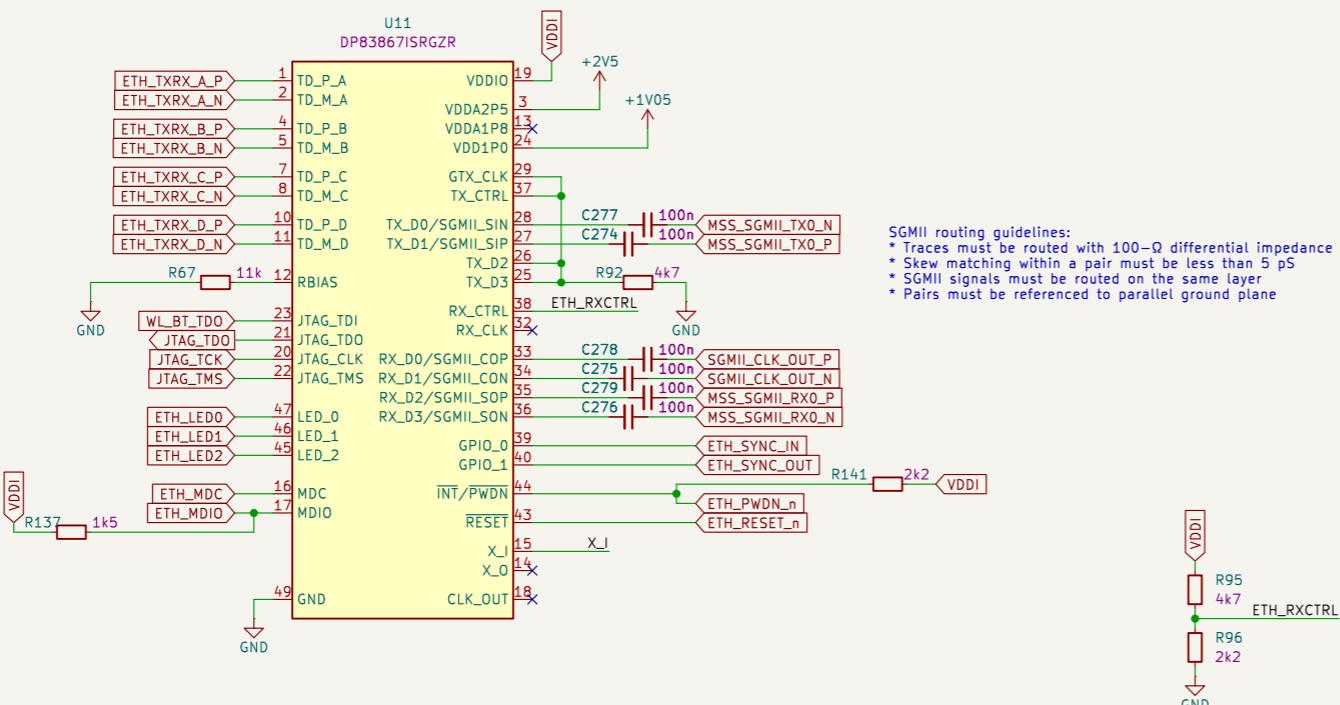


Power filtering

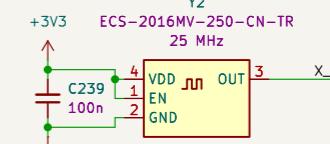




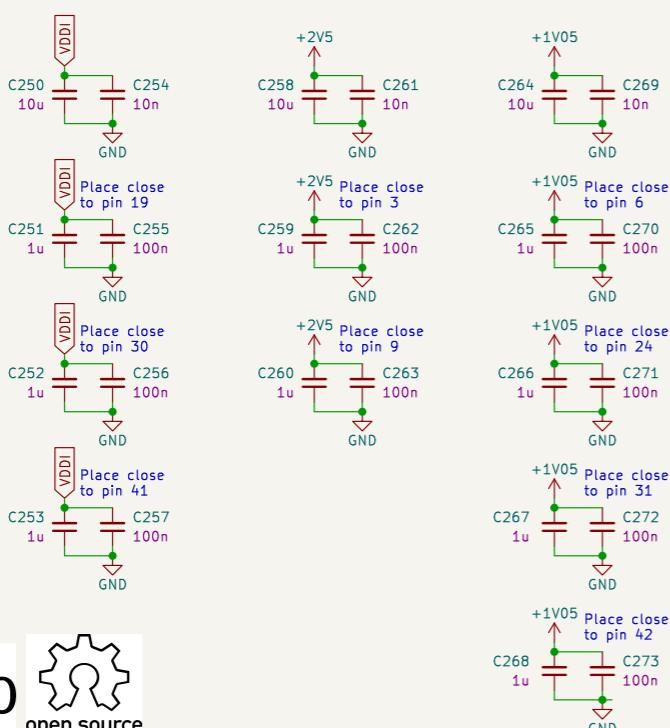
Gigabit Ethernet PHY transceiver with SGMII



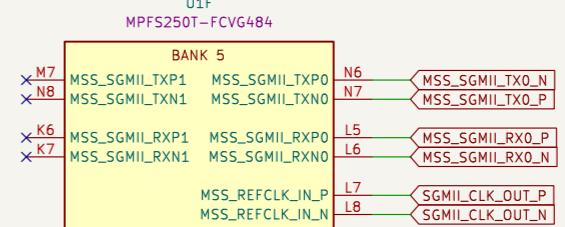
Clock



Decoupling capacitors

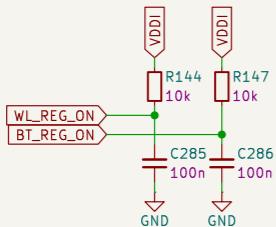


PolarFire Bank 5 MSS_SGMII

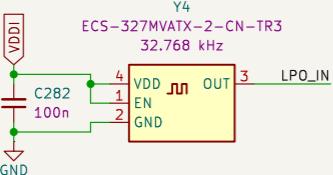


1 2 3 4 5 6 7 8

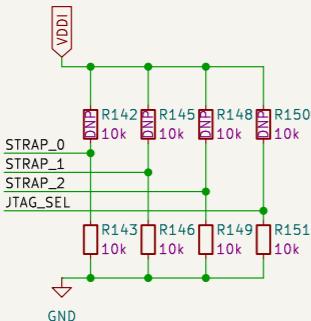
WL and BT Enable



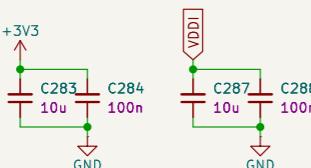
Sleep clock



Config



Decoupling



WiFi and Bluetooth Module

