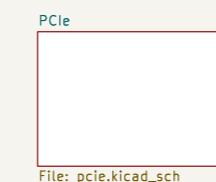


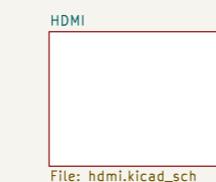
File: usb.kicad_sch



File: pcie.kicad_sch



File: crosslink.kicad_sch



File: hdmi.kicad_sch



File: ethernet.kicad_sch



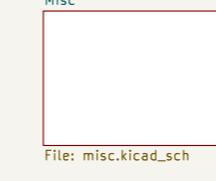
File: wifi_bt.kicad_sch



File: connectors.kicad_sch



File: soc.kicad_sch



File: misc.kicad_sch

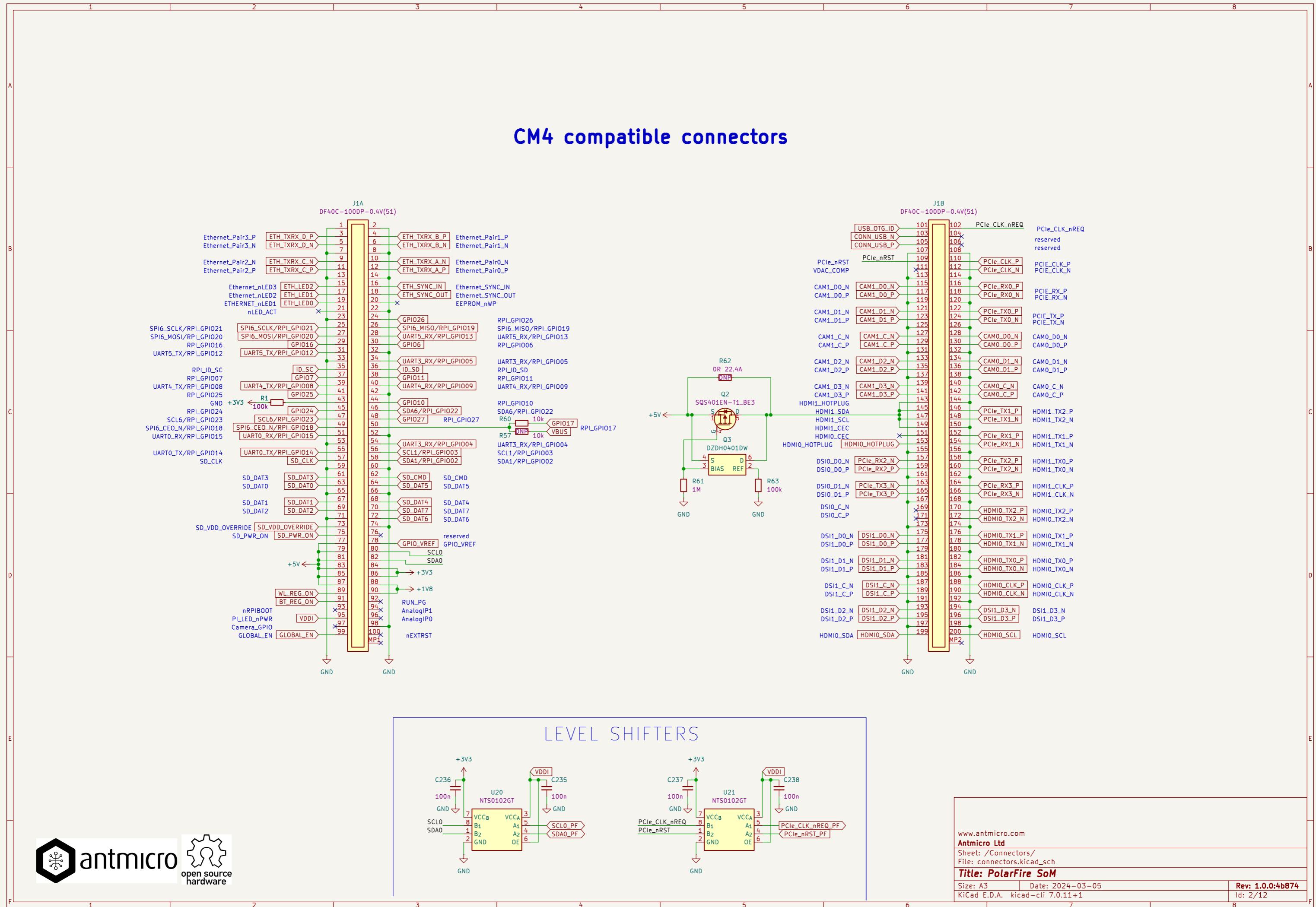


File: supply.kicad_sch



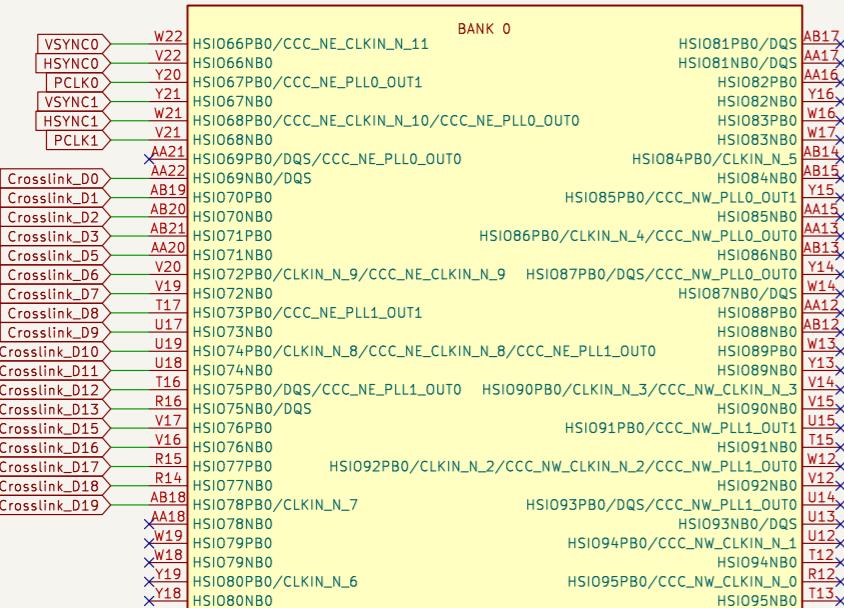
File: lpddr.kicad_sch

- N1 Logo antmicro_logo
- N2 Logo oshw_logo
- MP1 MP_NPTH_Drill2.7mm
- MP2 MP_NPTH_Drill2.7mm
- MP3 MP_NPTH_Drill2.7mm
- MP4 MP_NPTH_Drill2.7mm

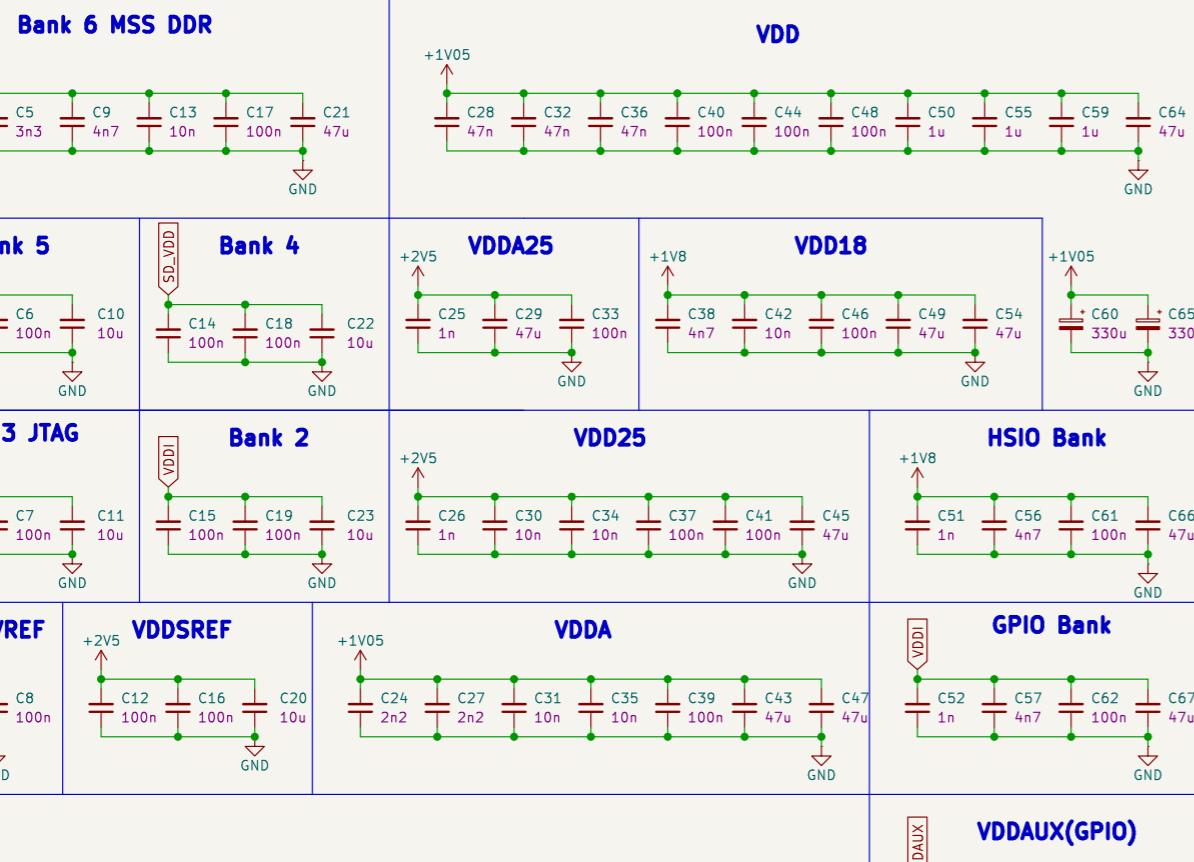




U1A
MPFS250T-FCVG484

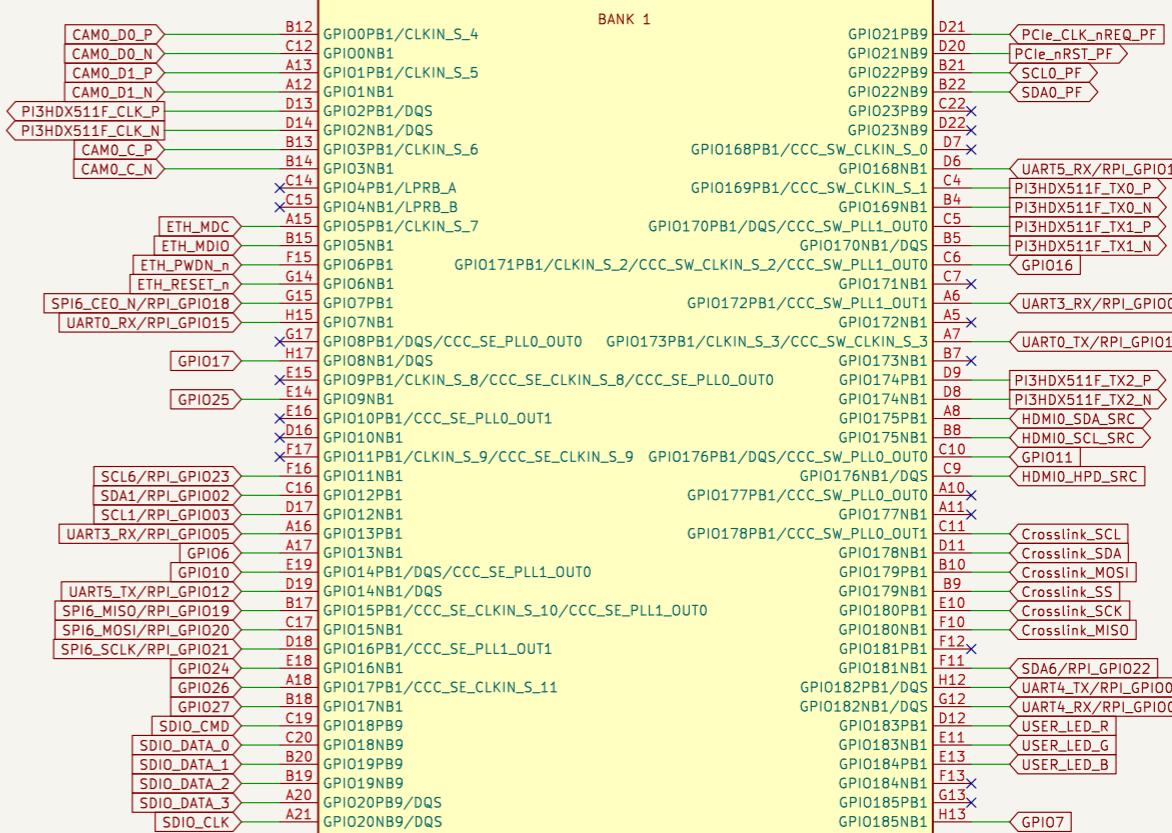


Decoupling capacitors

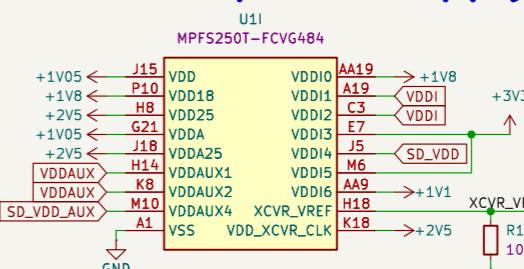


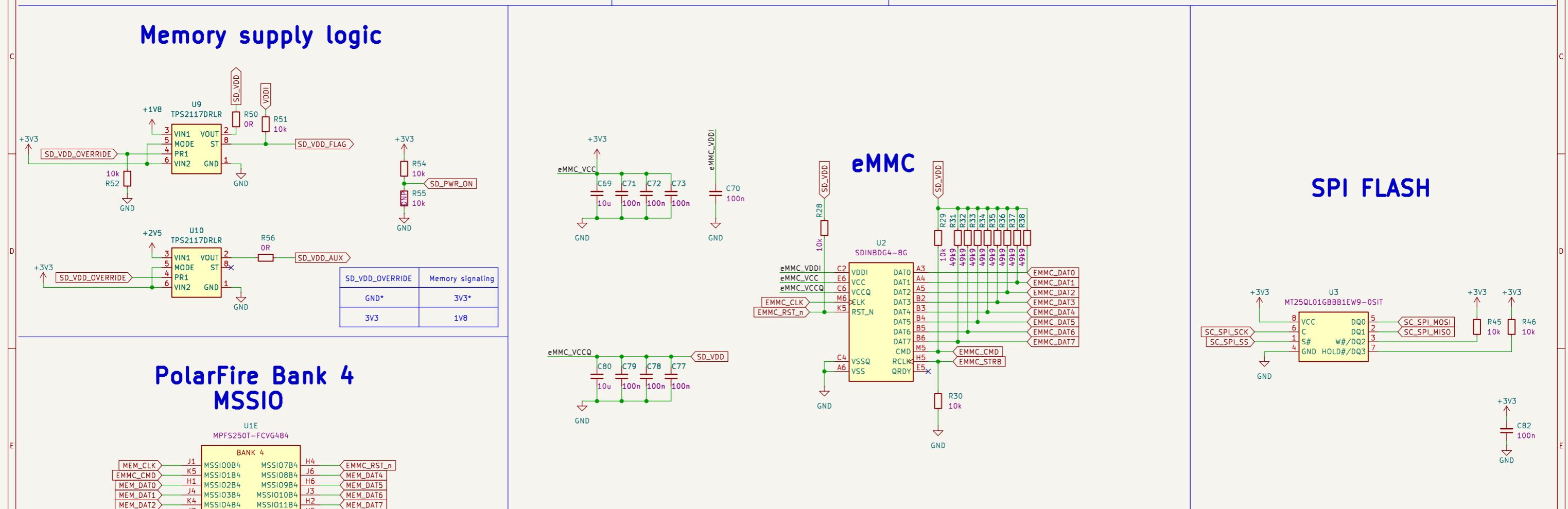
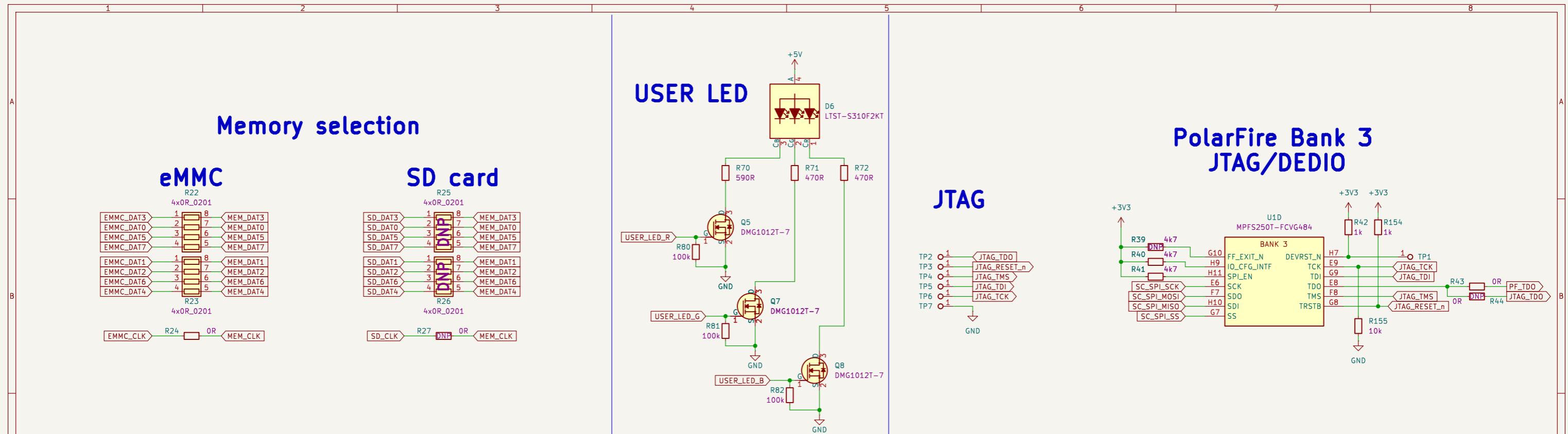
PolarFire Bank 1 GPIO

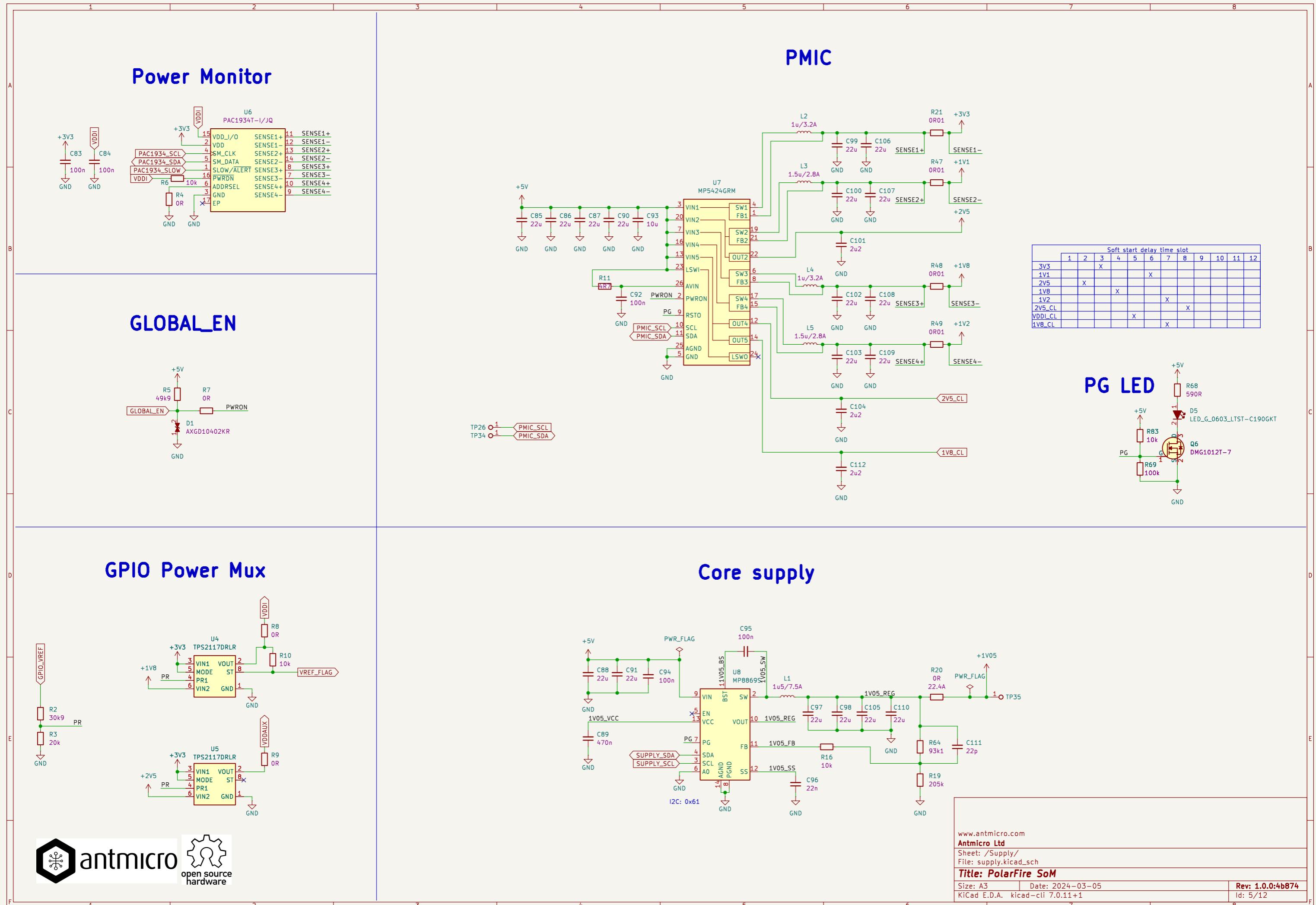
MPFS250T-FCVG484

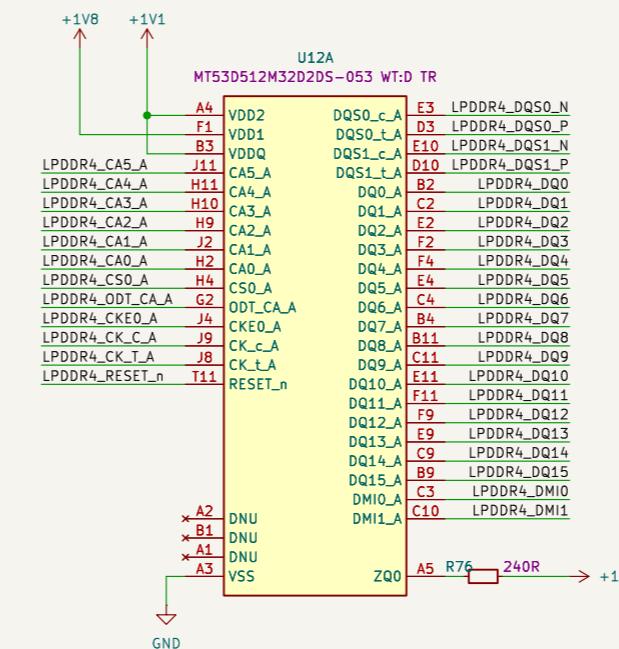
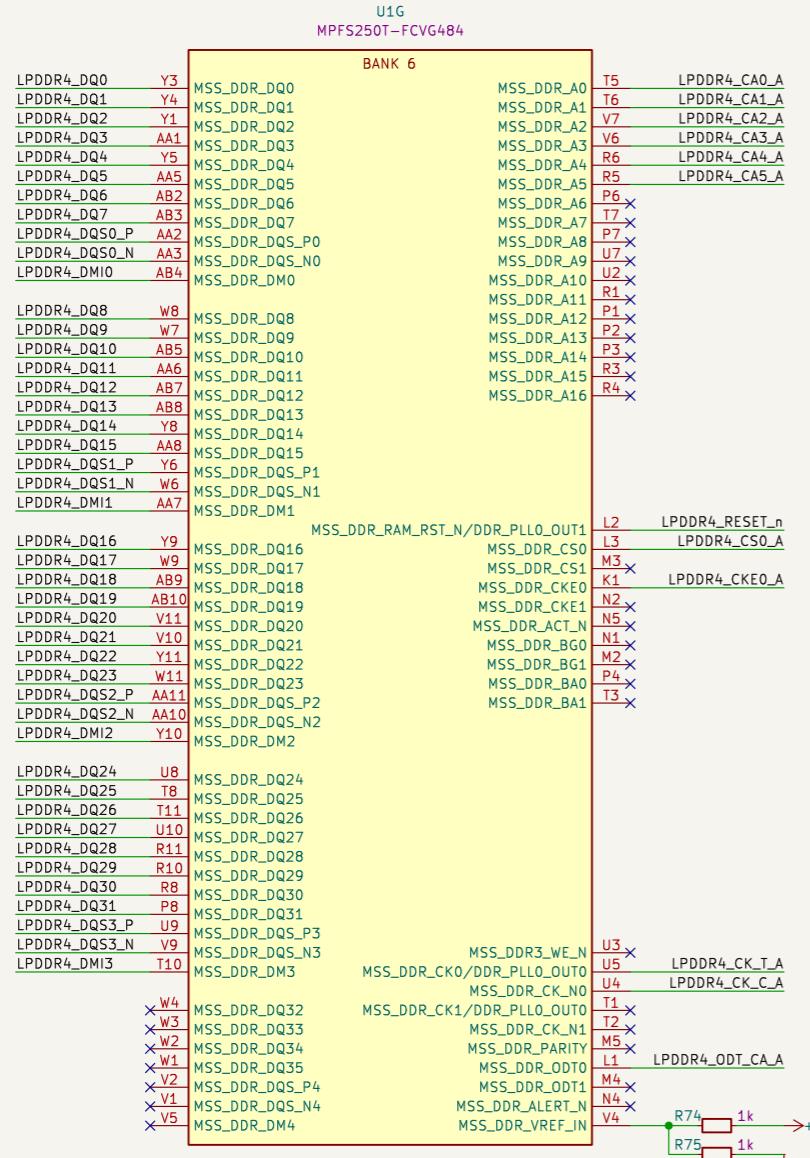


PolarFire power supply

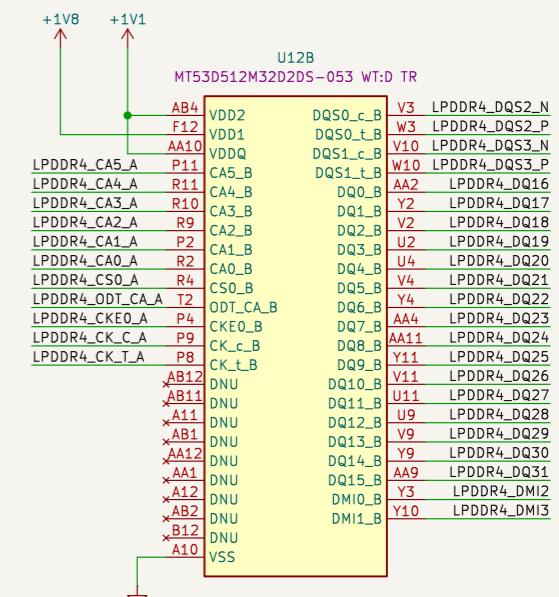




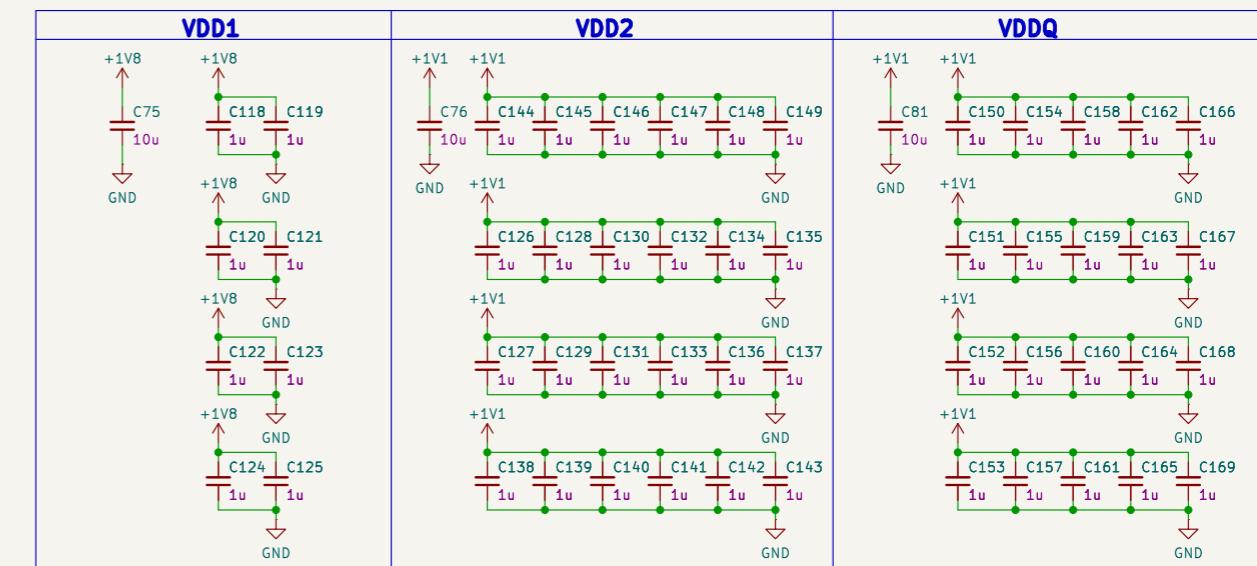


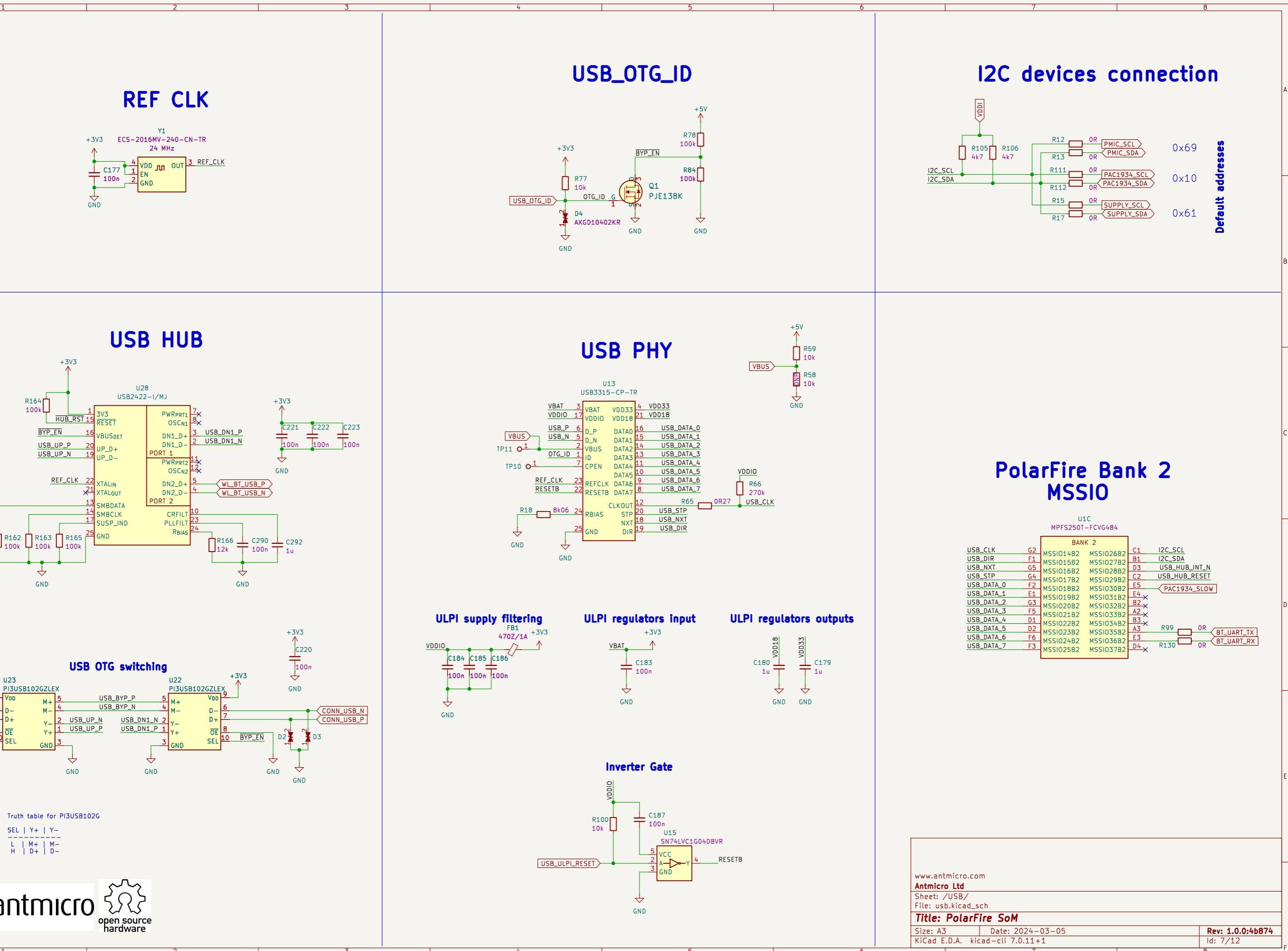


LPDDR4



Decoupling capacitors





A

A

B

B

C

C

D

D

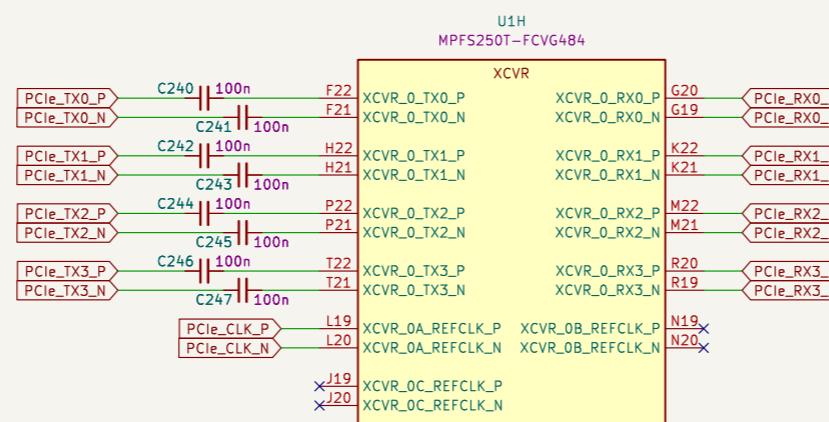
E

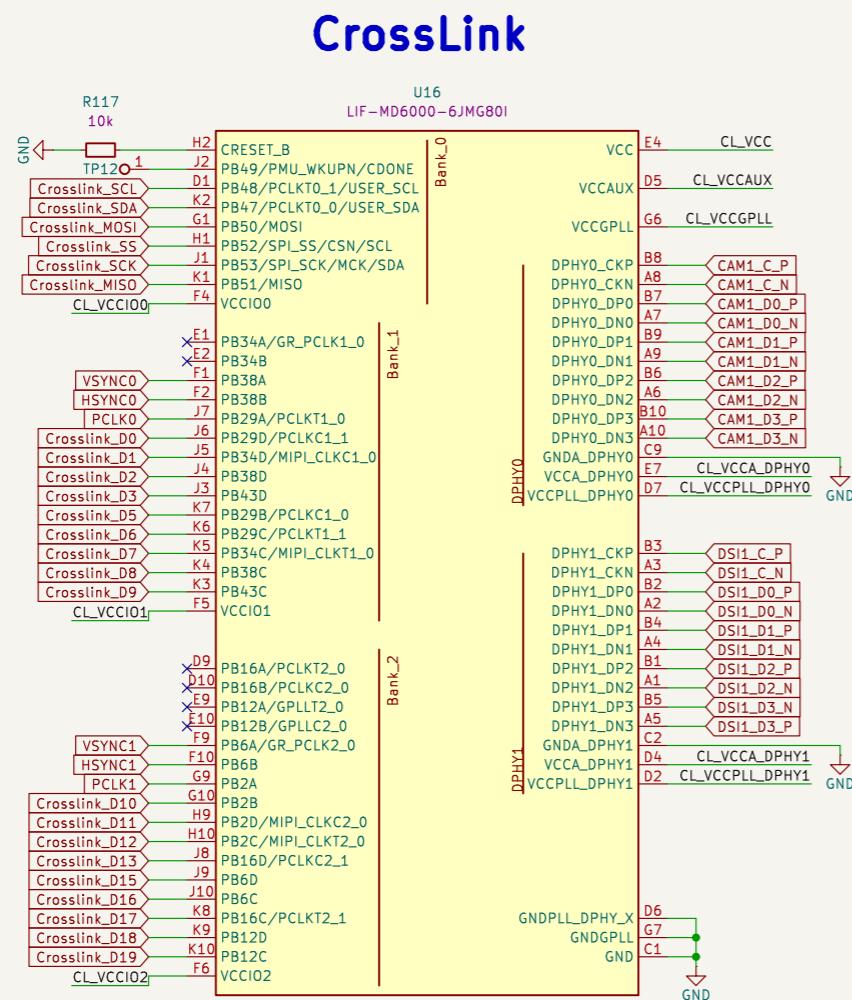
E

F

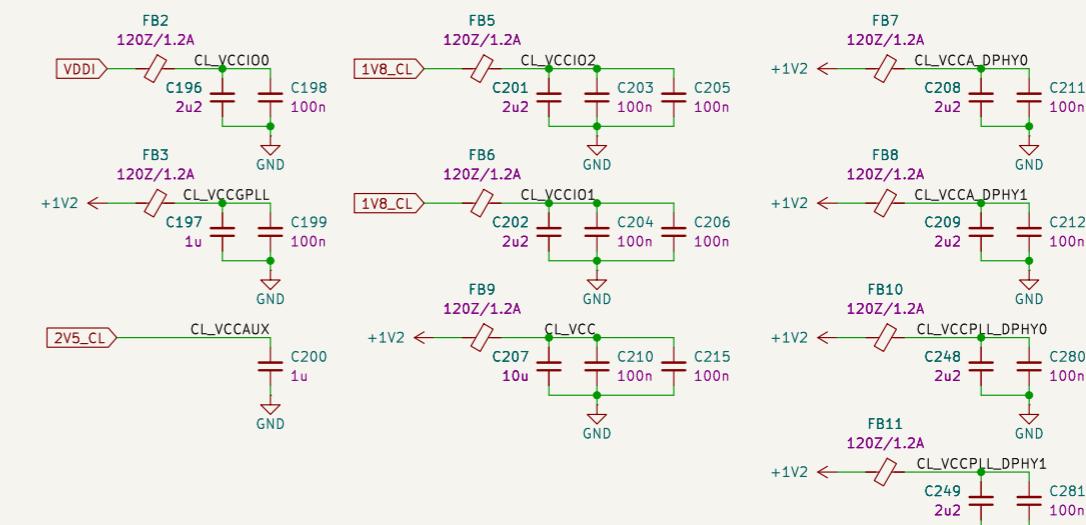
F

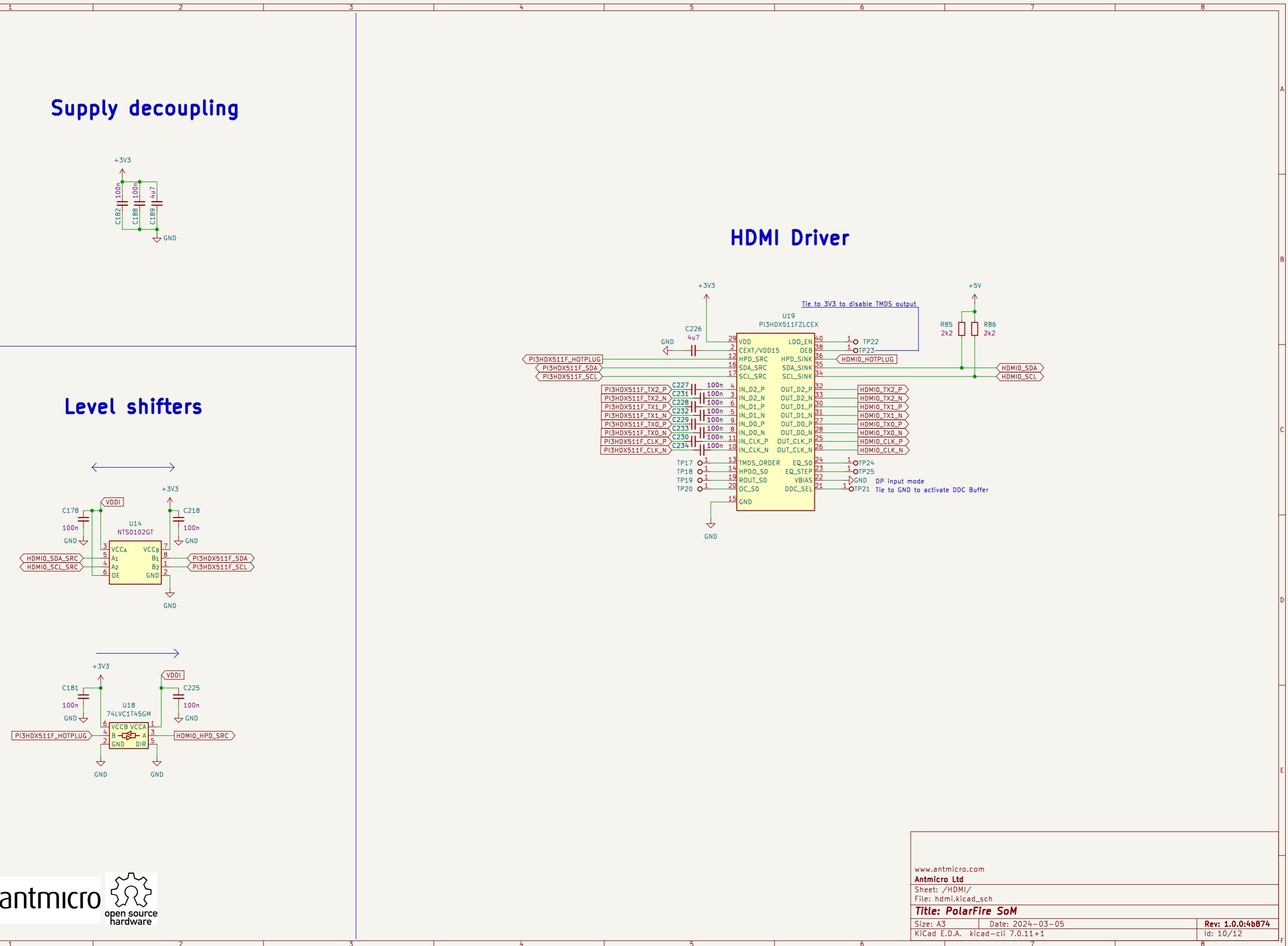
PolarFire Transceivers



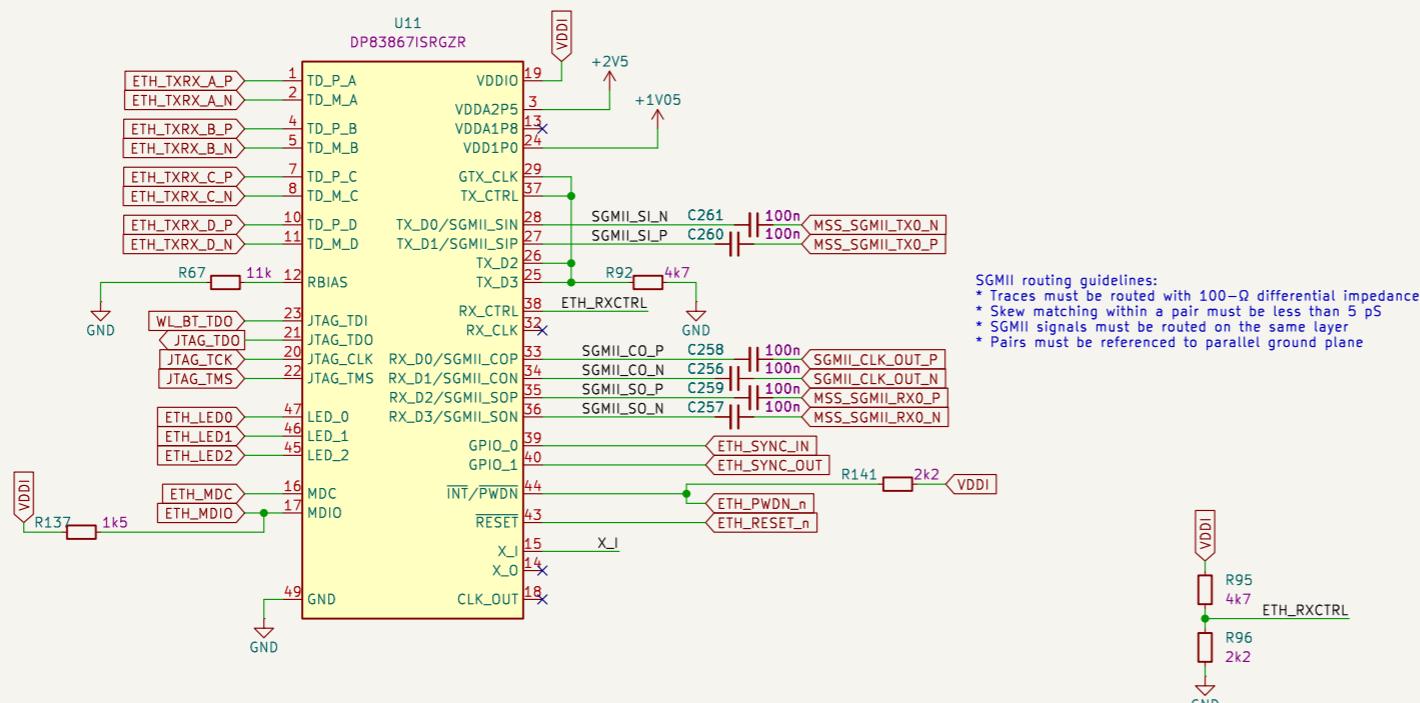


Power filtering

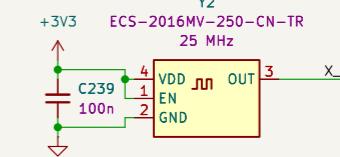




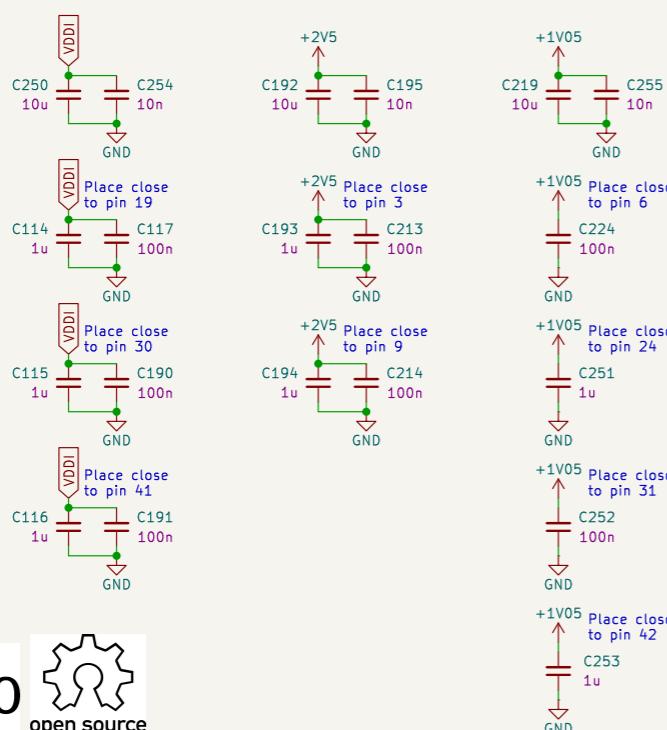
Gigabit Ethernet PHY transceiver with SGMII



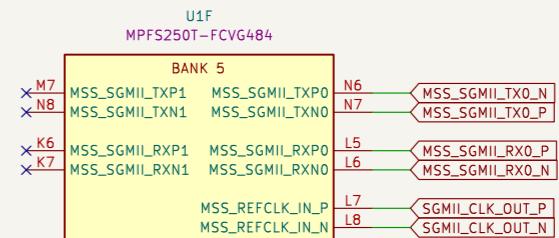
Clock



Decoupling capacitors

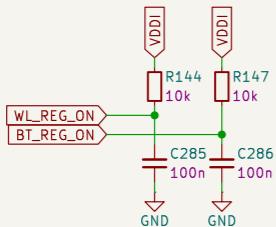


PolarFire Bank 5 MSS_SGMII

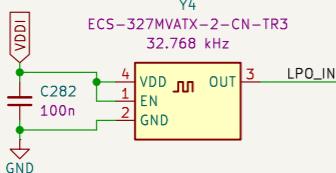


1 2 3 4 5 6 7 8

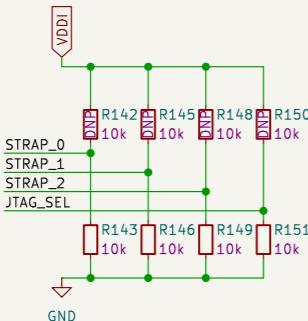
WL and BT Enable



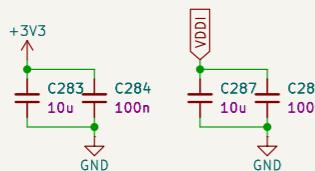
Sleep clock



Config



Decoupling



WiFi and Bluetooth Module

