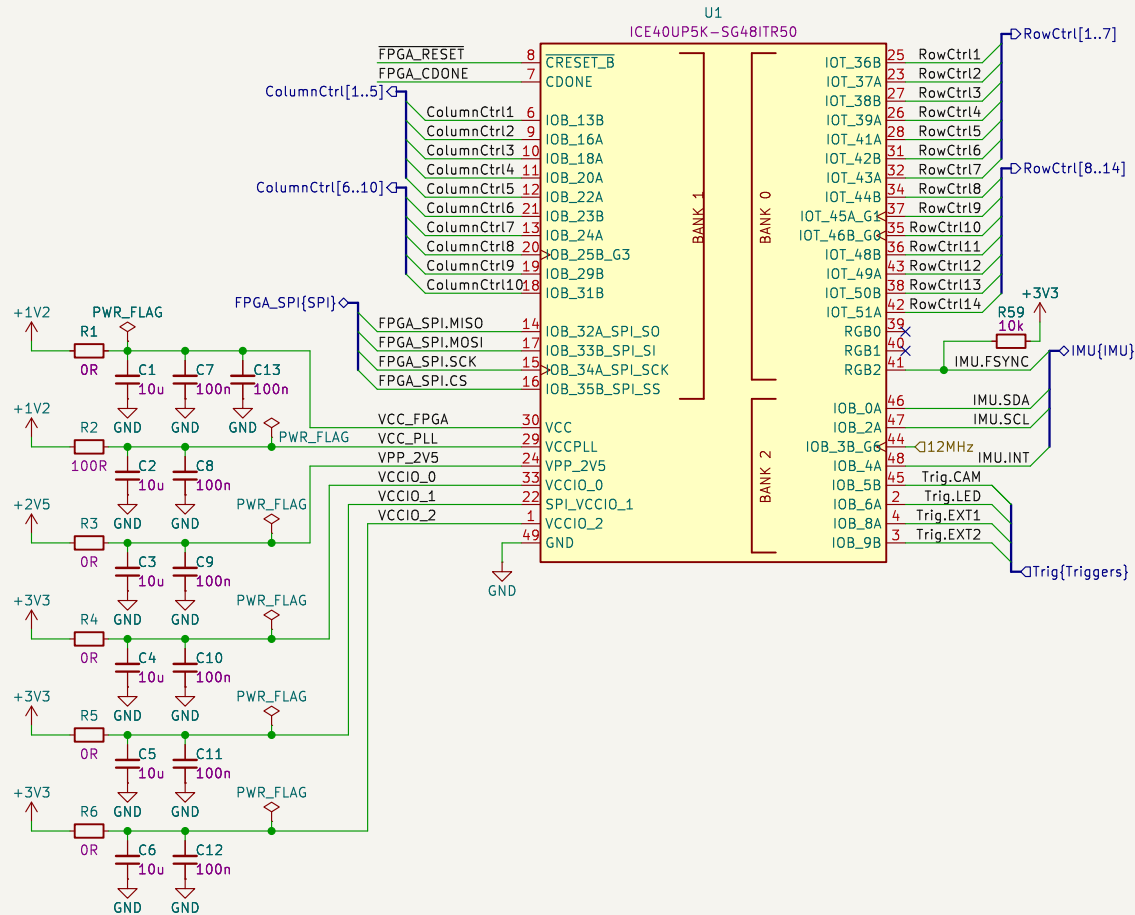
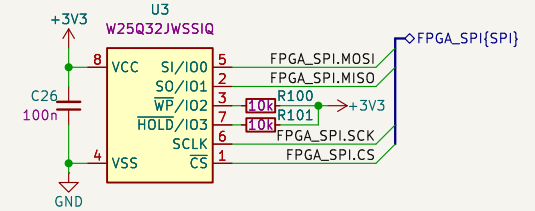


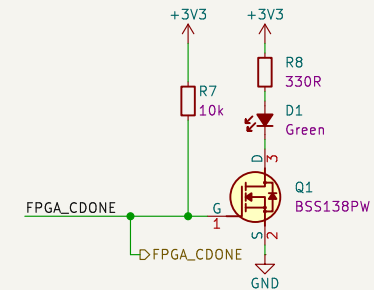
FPGA



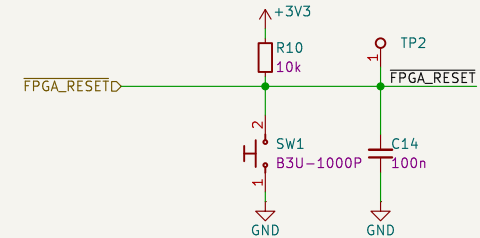
FLASH



CDONE STATUS



RESET



Sheet: /FPGA/
File: fpga.kicad_sch

Title: LED Panel Board

Size: A4

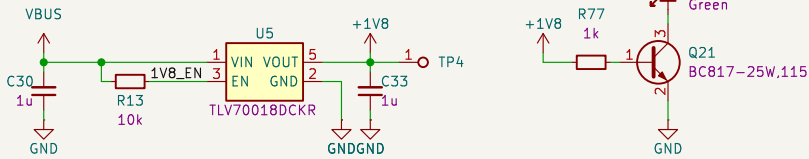
Date: 2025-01-31

Rev: 1.0.0:ec4ac

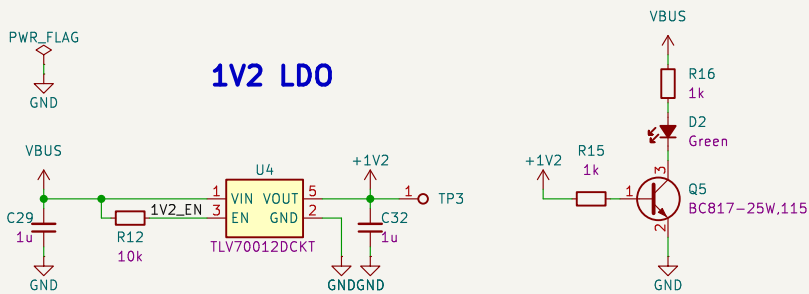
KiCad E.D.A. 8.0.7

Id: 2/12

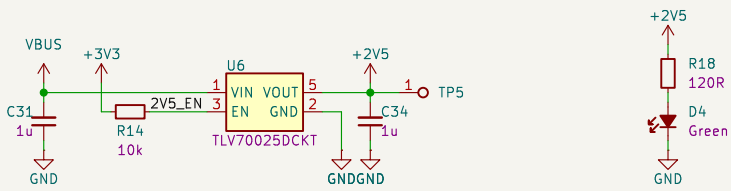
1V8 LDO



1V2 LDO

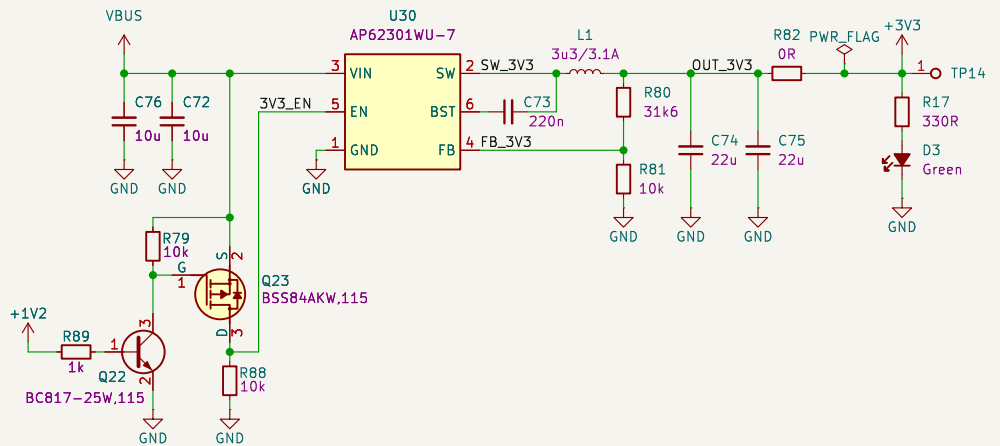


2V5 LDO

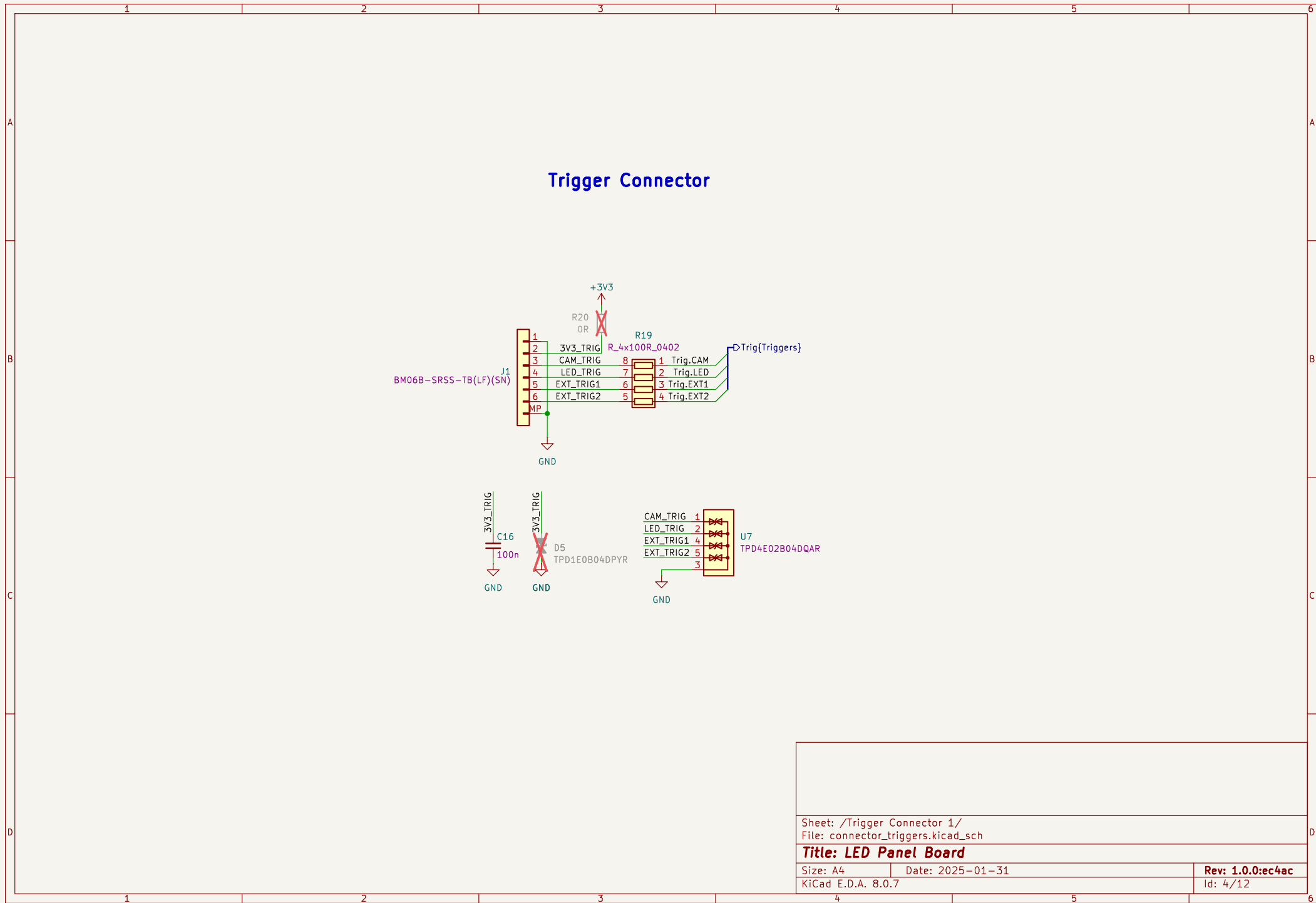


Power-up sequence: +5V => +1V2 => +3V3 => +2V5

3V3 DC-DC



Sheet: /Supply/ File: supply.kicad_sch		
Title: LED Panel Board		
Size: A4	Date: 2025-01-31	Rev: 1.0.0:ec4ac
KiCad E.D.A. 8.0.7		Id: 3/12



Sheet: /Trigger Connector 1/
File: connector_triggers.kicad_sch

Title: LED Panel Board

Size: A4

Date: 2025-01-31

Rev: 1.0.0:ec4ac

KiCad E.D.A. 8.0.7

Id: 4/12

The schematic diagram illustrates the trigger circuit for the BM06B-SRSS-TB(LF)(SN) camera module. The circuit includes a 3V3 TRIG input, a 100nF capacitor (C17), a 100k resistor (R29), a 4x100R_0402 resistor (R28), and a 4-pin connector (J2). The output is connected to a 4-pin connector (U10) which is labeled 'Trig(Triggers)'.

Title: LED Panel Board

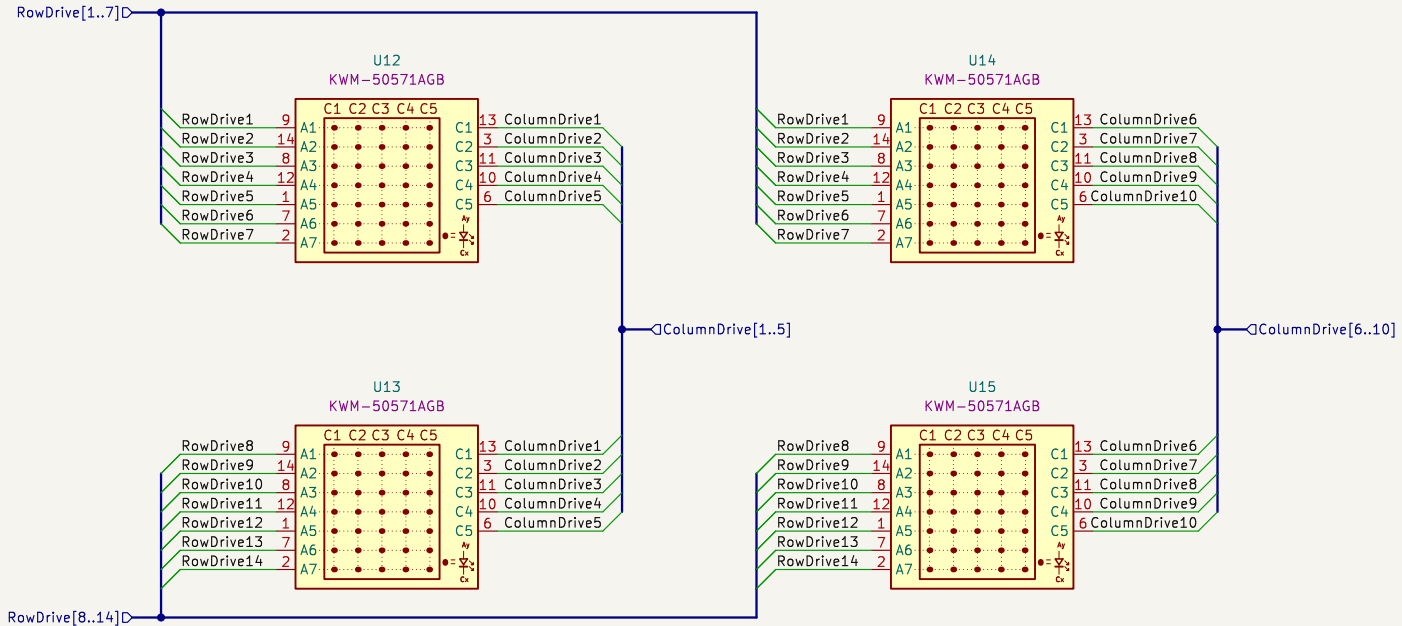
Rev: 1.0.0:ec4ac

Id: 6/12

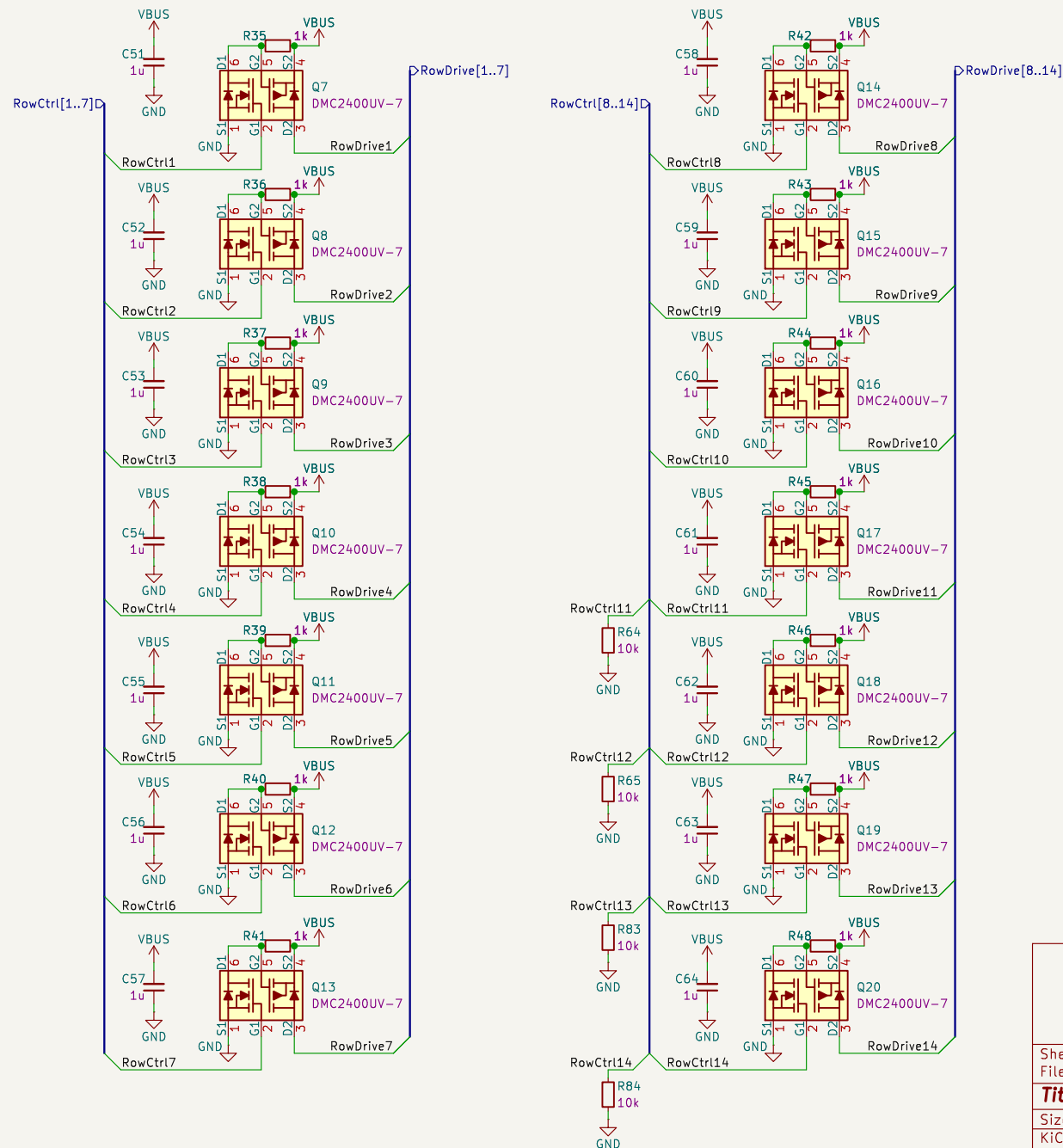
[illegible]

Size: A4	Date: 2025-01-31	Rev: 1.0.0:ec4ac
KiCad E.D.A. 8.0.7		Id: 7/12

LED Matrices



High Side LED Drivers



Sheet: /Matrix High Side Driver/
File: matrix_high_side_drive.kicad_sch

Title: LED Panel Board

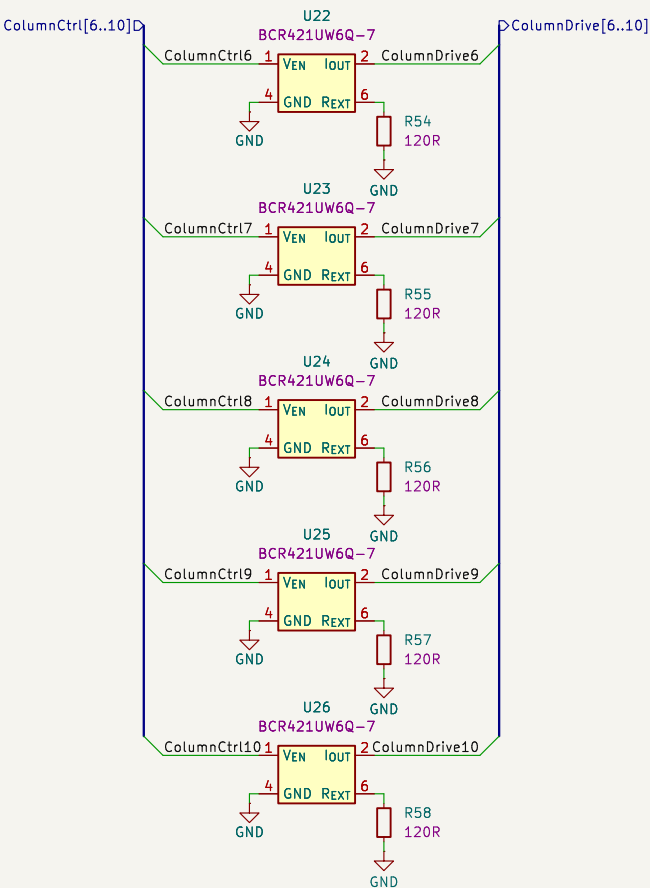
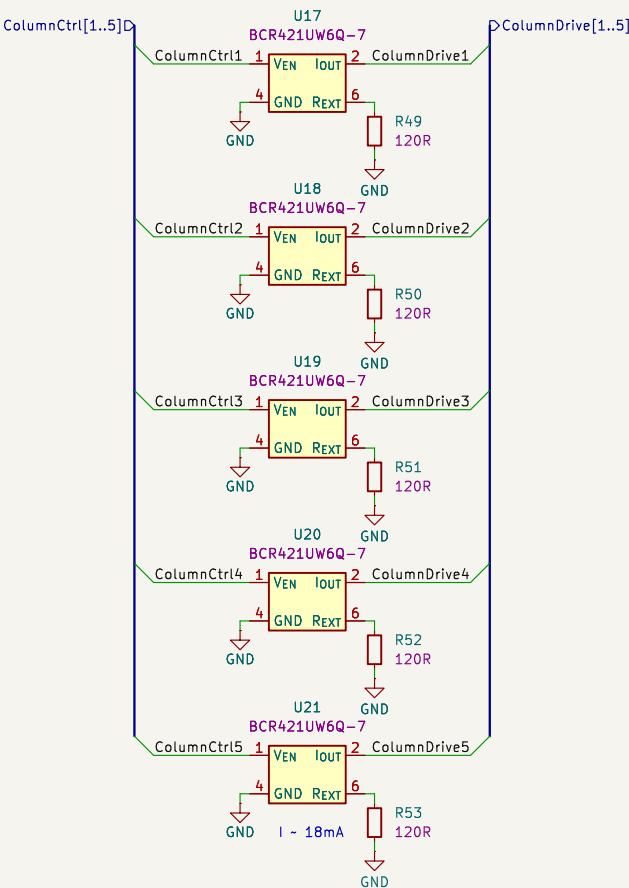
Size: A4 Date: 2025-01-31

KiCad E.D.A. 8.0.7

Rev: 1.0.0:ec4ac

Id: 10/12

Low Side LED Drivers



Sheet: /Matrix Low Side Driver/
File: matrix_low_side_drive.kicad_sch

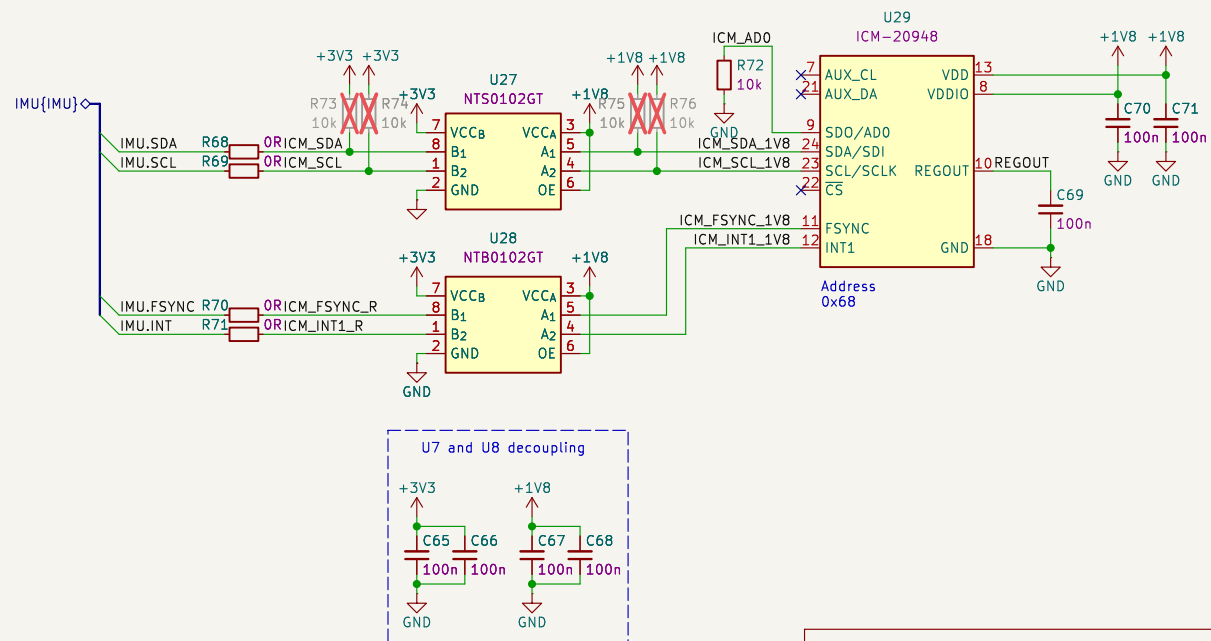
Title: LED Panel Board

Size: A4 Date: 2025-01-31

KiCad E.D.A. 8.0.7

Rev: 1.0.0:ec4ac

Id: 11/12



Sheet: /IMU/
File: IMU.kicad_sch

Title: LED Panel Board

Size: A4

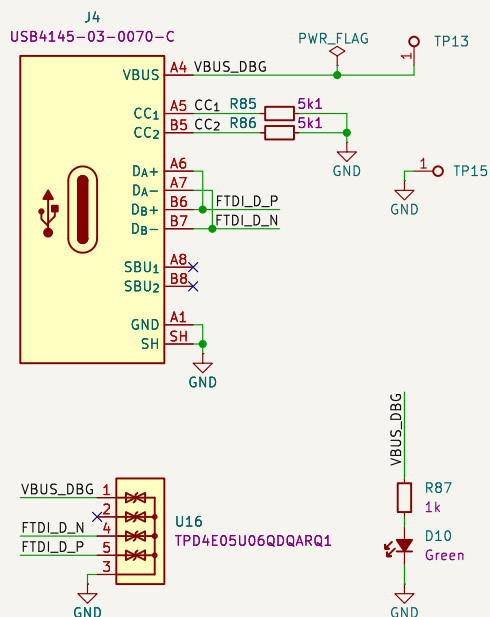
Date: 2025-01-31

Rev: 1.0.0:ec4ac

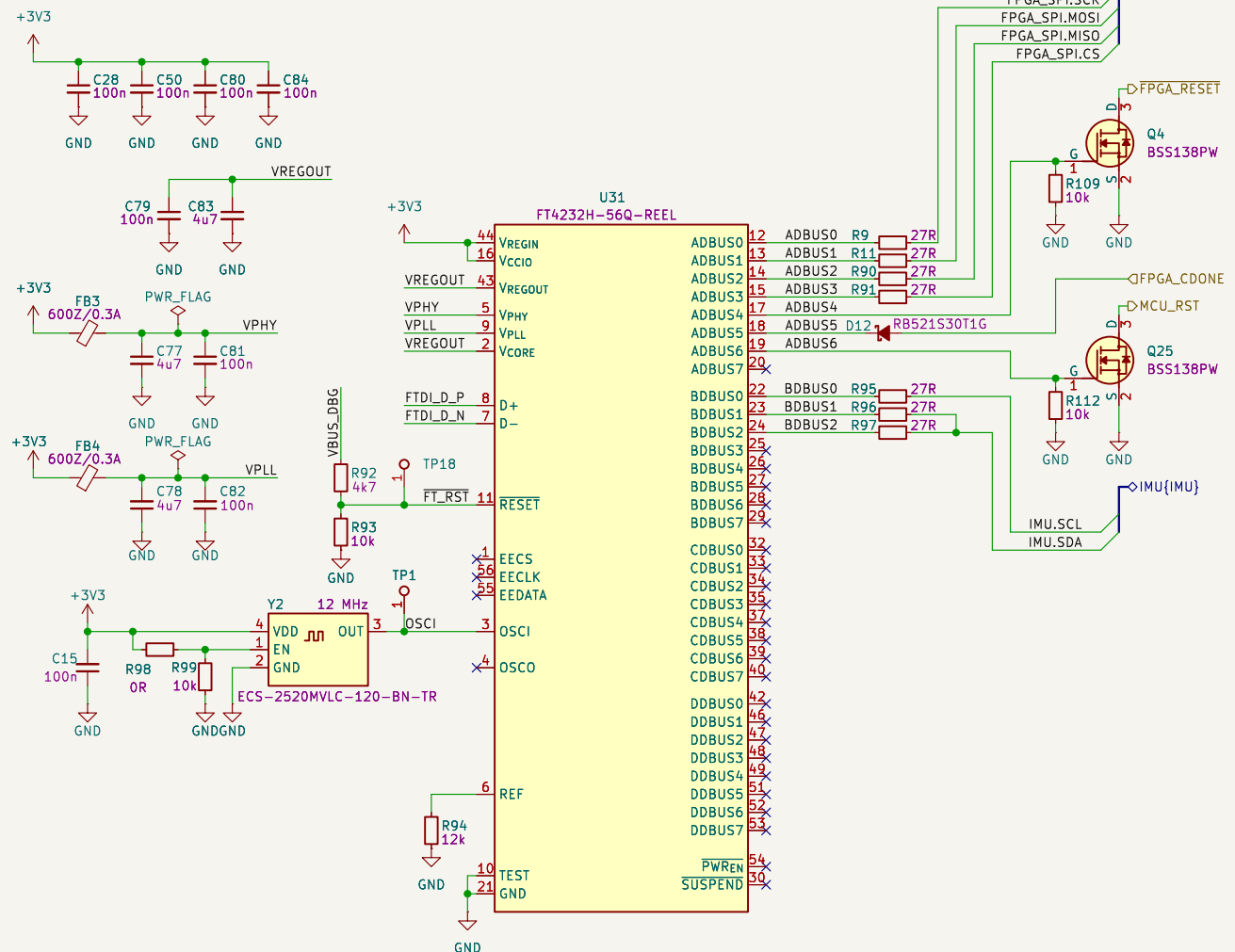
KiCad E.D.A. 8.0.7

Id: 12/12

Debug USB-C Connector



FTDI



Sheet: /USB_FTDI/
File: USB-debug.kicad_sch

Title: LED Panel Board

Size: A4 Date: 2025-01-31

KiCad E.D.A. 8.0.7

Rev: 1.0.0:ec4ac

Id: 13/12