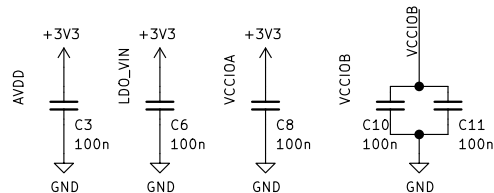


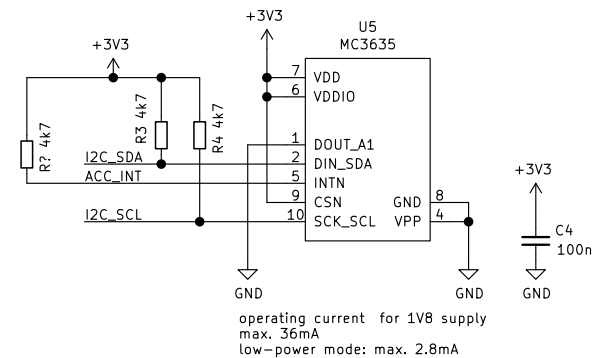
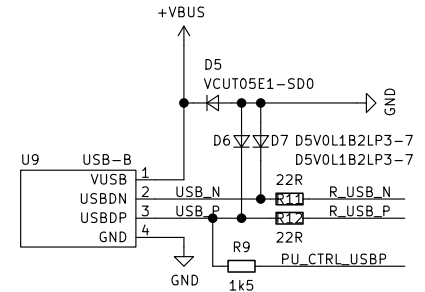
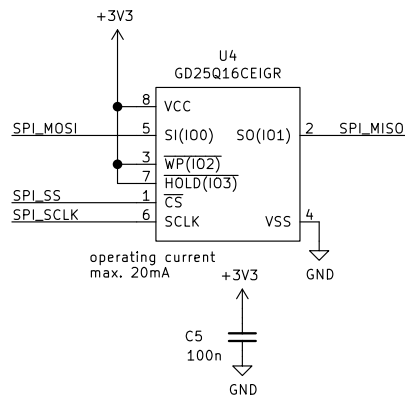
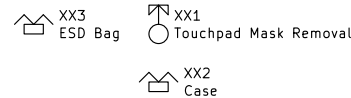
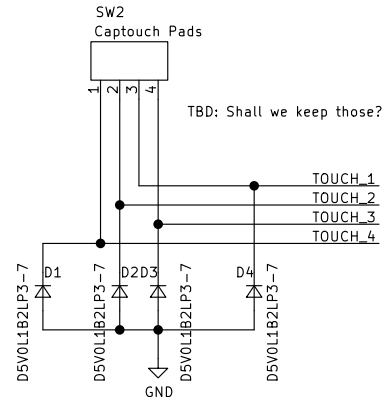
Maximum power consumption for A and B rails:
LDO mode: 65mA (VCCIOA) 114mA (VCCIOB)
LDO bypass mode: 77mA (VCCIOA) 269mA (VCCIOB)

The multi-functional IOs have four programmable drive strength states: 2 mA, 4 mA, 8 mA, 12 mA (refer to datasheet page 92)

FBIO_19 and FBIO_20 are bootstrap pins for SPI flash boot. They must be pulled low



E0S S3



Antmicro Ltd. (www.antmicro.com)

Sheet: /
File: qomu-board.sch

Title: Qomu Board

Size: A4 Date:
KiCad E.D.A. kicad 5.1.4+dfsg1-1bpo10+1

Rev:
Id: 1/1