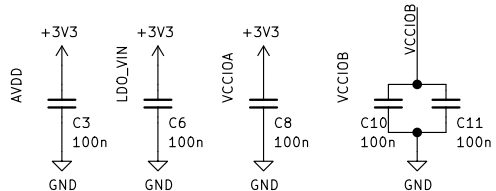
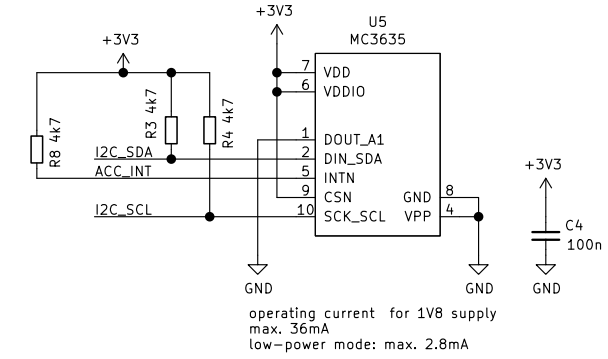
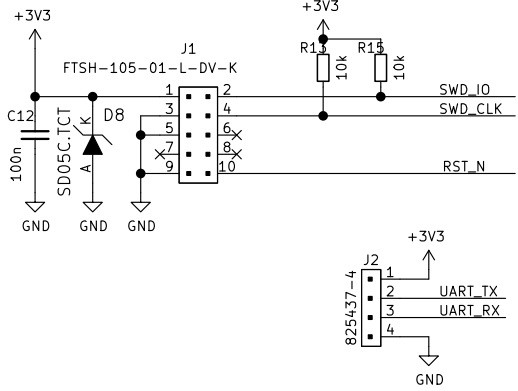


Maximum power consumption for A and B rails:
 LDO mode: 65mA (VCCIOA) 114mA (VCCIOB)
 LDO bypass mode: 77mA (VCCIOA) 269mA (VCCIOB)
 The multi-functional IOs have four programmable drive strength states: 2 mA, 4 mA, 8 mA, 12 mA (refer to datasheet page 92)

FBIO_19 and FBIO_20 are bootstrap pins for SPI flash boot. They must be pulled low



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Sheet: /
 File: qomu-board.sch

Title: Qomu Board

Size: A4 Date:
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Rev:
 Id: 1/1