

31

2625

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left. The least significant bit is labeled '0' at the top right. A vertical line of tick marks runs along the top and bottom edges of the register. The register is divided into two main sections: a shaded section on the left and an unshaded section on the right. The unshaded section is labeled 'controller_last_addr_26[25:0]' in large text. The shaded section is labeled 'controller_last_addr_25[0]' in smaller text.

controller_last_addr_26[25:0]