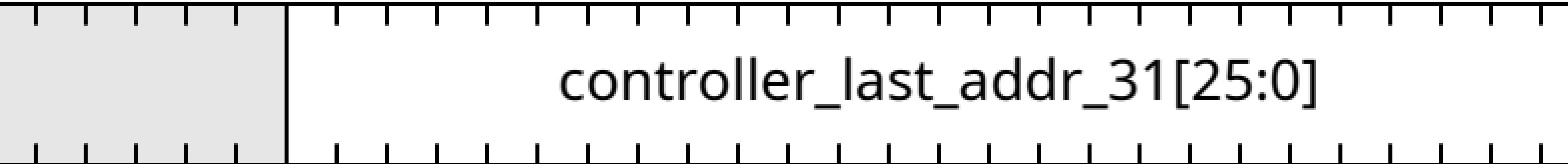


31

2625

0



A 32-bit register visualization. The most significant bit (MSB) is shaded gray, while the remaining 31 bits are white. The register is labeled "controller_last_addr_31[25:0]" below it.

controller_last_addr_31[25:0]