

31

220

0

controller_last_addr_7[20:0]

The diagram illustrates a 32-bit register structure. It features two main fields: a 21-bit field labeled "controller_last_addr_7[20:0]" and a 2-bit field labeled "controller_last_addr_1[1:0]". The "controller_last_addr_7[20:0]" field is represented by a light gray rectangle, while the "controller_last_addr_1[1:0]" field is represented by a white rectangle. Both fields are preceded by 1-bit width indicators (31, 220, 0) and followed by 1-bit width indicators (1, 0). The entire register is enclosed in a black border.

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