

31

2625

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left. The least significant bit is labeled '0' at the top right. The register is divided into two main fields: 'controller_last_addr_19[25:0]' and 'controller_last_addr_1[19:0]'. The 'controller_last_addr_19[25:0]' field is represented by a large gray rectangle spanning bits 25 to 0, with its label centered below it. The 'controller_last_addr_1[19:0]' field is represented by a smaller white rectangle spanning bits 19 to 0, located below the gray rectangle. Both fields have black outlines. Vertical tick marks are present along the top and bottom edges of the register.

controller_last_addr_19[25:0]