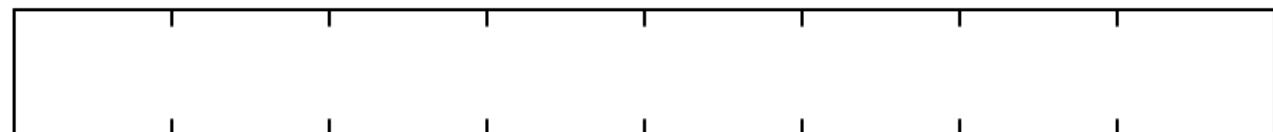
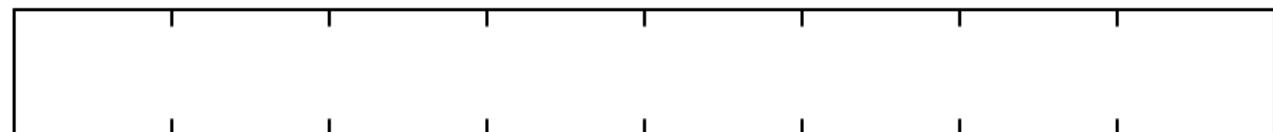
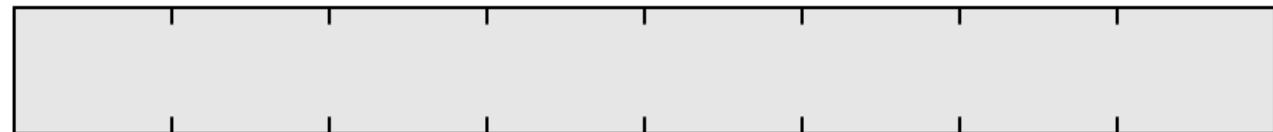


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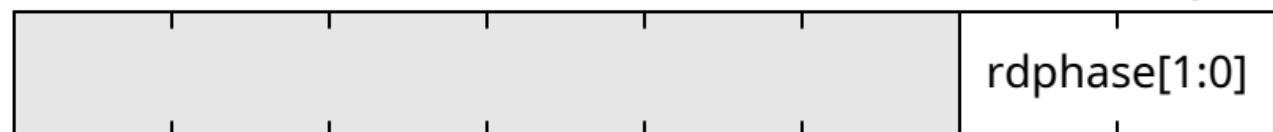
7

2

1

0

rdphase[1:0]



reset: 2