

31

220

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left. The least significant bit is labeled '0' at the top right. The register is divided into two main fields: 'controller\_last\_addr\_3[20:0]' and 'controller\_last\_addr\_4[1:0]'. The 'controller\_last\_addr\_3[20:0]' field is represented by a light gray rectangle spanning from bit 20 down to bit 0. The 'controller\_last\_addr\_4[1:0]' field is represented by a white rectangle spanning from bit 21 up to bit 22. Both fields have black outlines. Each field contains a series of vertical tick marks along its length, corresponding to each bit position from 20 to 0 and 21 to 22 respectively.

controller\_last\_addr\_3[20:0]