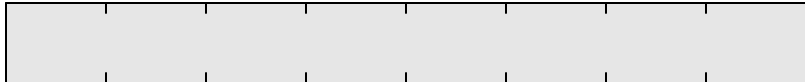


31

24



7

5

4

0

inverter_divisor_mask[4:0]

