

31

2625

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left. The least significant bit is labeled '0' at the top right. The register is divided into two main fields: 'controller_last_addr_27[25:0]' which occupies bits 27 to 0, and 'controller_last_addr_2[1:0]' which occupies bits 2 to 0. The 'controller_last_addr_27[25:0]' field is highlighted with a gray background. The 'controller_last_addr_2[1:0]' field is white with black outlines. Both fields have vertical tick marks along their boundaries.

controller_last_addr_27[25:0]