

31

220

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left. The least significant bit is labeled '0' at the top right. The register is divided into two main fields: 'controller\_last\_addr\_2[20:0]' and 'controller\_last\_addr\_1[1:0]'. The 'controller\_last\_addr\_2[20:0]' field is located in the upper half of the register, spanning from bit 20 down to bit 0. It is highlighted with a light gray background. The 'controller\_last\_addr\_1[1:0]' field is located in the lower half of the register, spanning from bit 1 down to bit 0. It is highlighted with a white background. Both fields have black outlines. The entire register is enclosed in a thick black border.

controller\_last\_addr\_2[20:0]