

31

2322

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left, and the least significant bit is labeled '0' at the top right. The register is divided into two main fields: 'controller_last_addr_22[22:0]' and 'controller_last_addr_1[1:0]'. The 'controller_last_addr_22[22:0]' field is represented by a large gray rectangle covering bits 22 to 0, with vertical tick marks along its top and bottom edges. The 'controller_last_addr_1[1:0]' field is represented by a small white rectangle covering bits 21 to 1, positioned to the right of the gray rectangle. Both fields have vertical tick marks along their top and bottom edges.

controller_last_addr_22[22:0]