

31

220

0

controller_last_addr_0[20:0]

The diagram illustrates a 32-bit register structure. It features two main fields: 'controller_last_addr_0[20:0]' and 'controller_last_addr_1[2:0]'. The first field is represented by a light gray rectangle spanning from bit 20 down to bit 0. The second field is represented by a white rectangle spanning from bit 2 down to bit 0. Both fields are preceded by a black border. Above the register, the bit indices 31, 220, and 0 are labeled at the top left, top center, and top right respectively. The entire register is enclosed within a thick black rectangular frame.

controller_last_addr_1[2:0]