

31

2625

0

controller_last_addr_16[25:0]

controller_last_addr_16[15:0]

The diagram illustrates a 32-bit register structure. The most significant bit is labeled '31' and the least significant bit is labeled '0'. A large gray box highlights the upper 16 bits, labeled 'controller_last_addr_16[25:0]'. Below this, another gray box highlights the lower 16 bits, labeled 'controller_last_addr_16[15:0]'. Both boxes are defined by thick black lines.