

31

0

A 32-bit register diagram. The top row shows bit indices from 31 down to 0. The bottom row shows the corresponding bit values. Bit 31 is high (black), while all other bits (30 to 0) are low (white). A large white rectangular box covers the central 31 bits, labeled `dfiii_pi1_rddata[31:0]`. This indicates that the register contains 31 data bits and 1 enable bit.

dfiii_pi1_rddata[31:0]