

31

2423

0

controller\_last\_addr\_15[23:0]

controller\_last\_addr\_15[15:0]

Detailed description: The diagram shows a 32-bit register. The most significant bit is labeled '31' and the least significant bit is labeled '0'. In the center, there is a large gray box labeled 'controller\_last\_addr\_15[23:0]' which covers bits 23 to 0. To the right of this gray box, another smaller gray box is labeled 'controller\_last\_addr\_15[15:0]' which covers bits 15 to 0. The register is represented by a horizontal line with vertical tick marks at each bit position.