

31

2625

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left. The least significant bit is labeled '0' at the top right. A vertical line of tick marks runs along the top and bottom of the register. The register is divided into two main sections by a thick black vertical line. The left section is shaded gray and contains the text 'controller_last_addr_5[25:0]' below it. The right section is white and contains the text 'controller_last_addr_5[24:0]' below it.

controller_last_addr_5[25:0]

controller_last_addr_5[24:0]