

31

2625

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left. The least significant bit is labeled '0' at the top right. A thick black horizontal bar spans the entire width of the register. On the far left, there is a gray shaded rectangular block representing the `controller_last_addr_8[25:0]` field, which occupies bits 25 to 0. To the right of this gray block, the remaining bits are represented by a white area with vertical tick marks at each bit position, labeled from 25 down to 0. The text `controller_last_addr_8[25:0]` is centered below the gray block.

`controller_last_addr_8[25:0]`