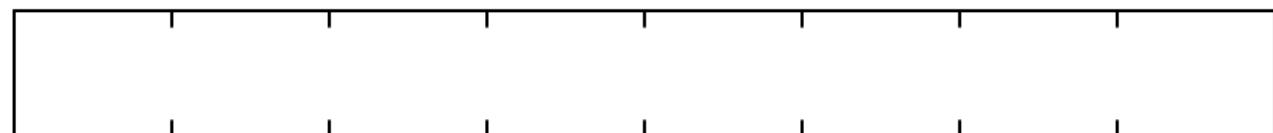
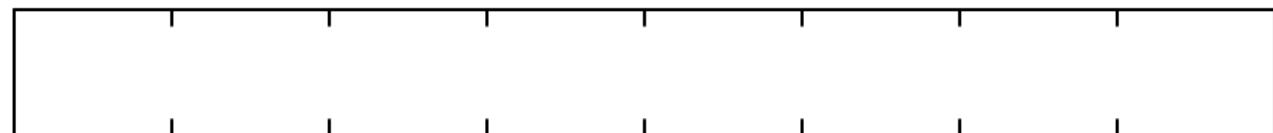


31

24



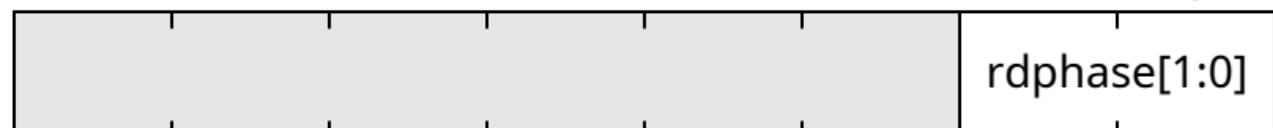
7

2

1

0

rdphase[1:0]



reset: 3