

31

220

0

A 32-bit register diagram. The most significant bit is labeled '31' at the top left. The least significant bit is labeled '0' at the top right. The register is divided into two main fields: 'controller_last_addr_2[20:0]' and 'controller_last_addr_1[1:0]'. The 'controller_last_addr_2[20:0]' field is located in the upper half of the register, spanning from bit 20 down to bit 0. It is represented by a light gray rectangle. The 'controller_last_addr_1[1:0]' field is located in the lower half of the register, spanning from bit 1 down to bit 0. It is represented by a white rectangle. Both fields have black outlines. Each field contains 11 vertical tick marks along its length, corresponding to the bit indices from 20 down to 0 or 1 down to 0.

controller_last_addr_2[20:0]