

31

220

0

controller\_last\_addr\_0[20:0]

This diagram illustrates a 32-bit register structure. The register is divided into two main fields: `controller_last_addr_0[20:0]` and `controller_last_addr_1[2:0]`. The `controller_last_addr_0[20:0]` field occupies the most significant 21 bits (from bit 31 down to bit 0), while the `controller_last_addr_1[2:0]` field occupies the least significant 3 bits (from bit 20 down to bit 18). Both fields are represented by vertical black bars on a white background. The entire register is enclosed in a black rectangular border.