

31

0

b_dq_remapping[31:0]

The diagram illustrates a 32-bit register, labeled `b_dq_remapping[31:0]`. It features a top row of 32 vertical tick marks, each representing a bit position from 31 down to 0. Below this row is a large white rectangular box representing the register's width. A bottom row of 32 vertical tick marks is positioned directly beneath the bottom edge of the white box, mirroring the bit positions above.