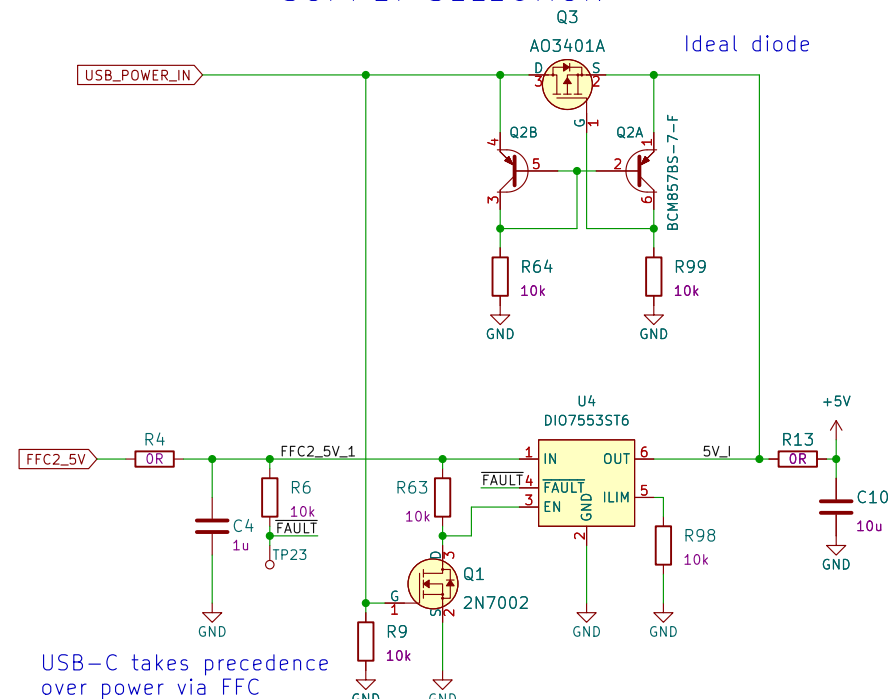
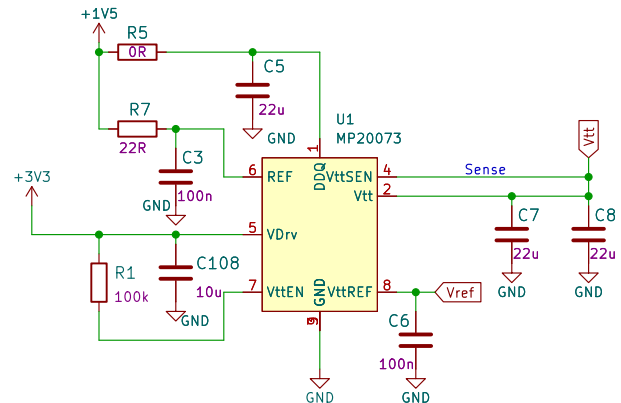


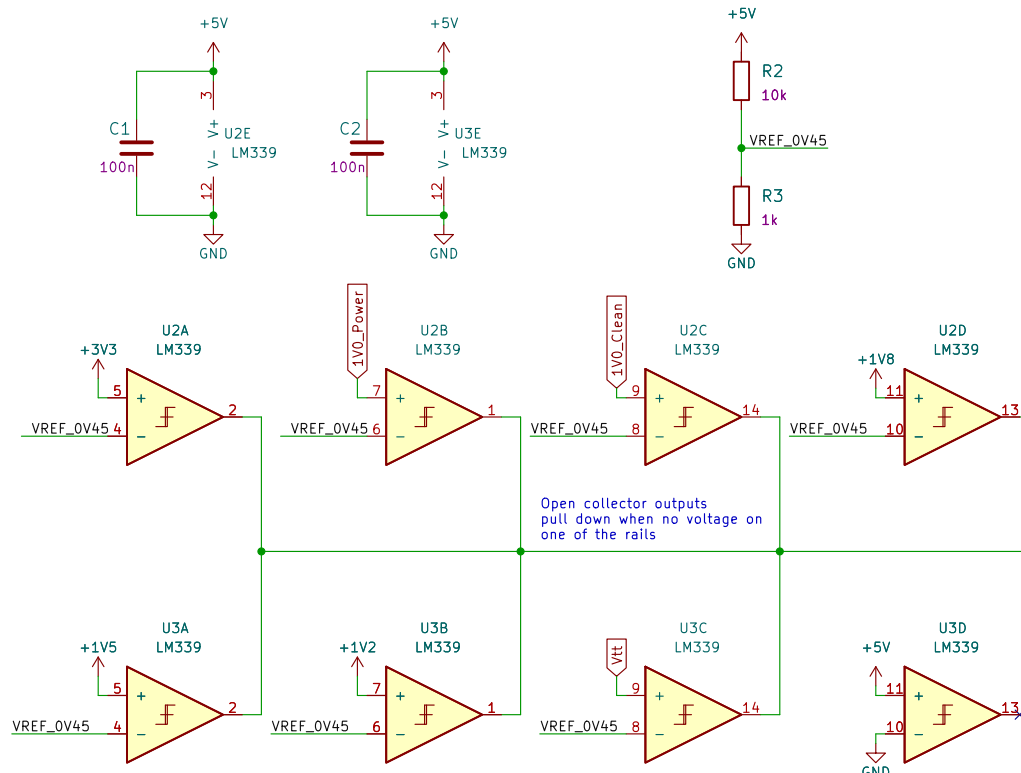
## SUPPLY SELECTION



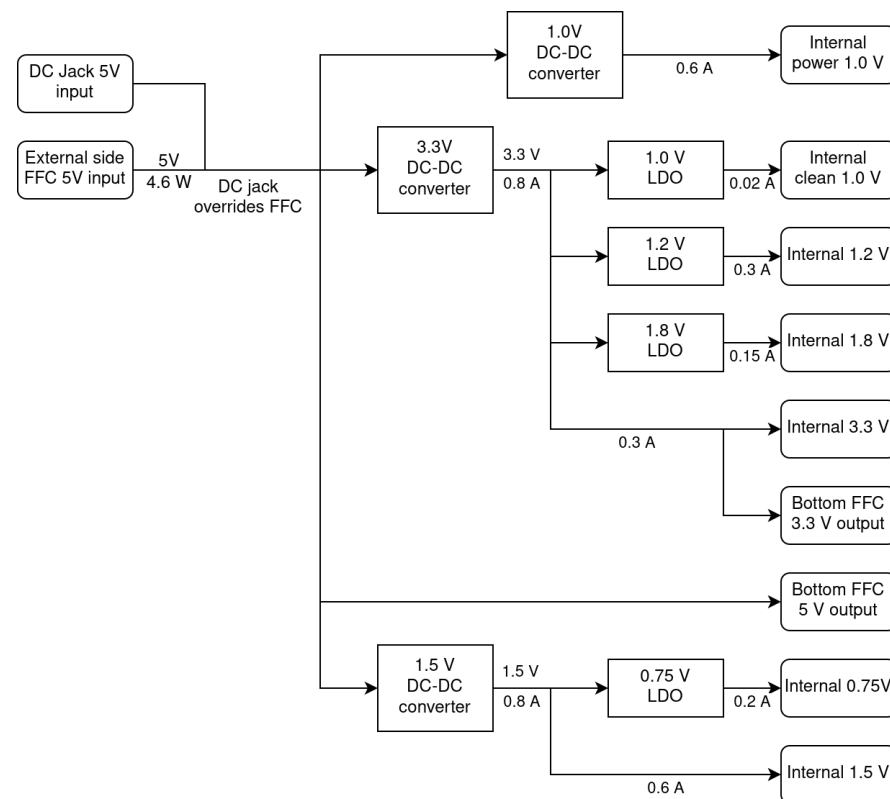
DDR MEMORY TERMINATION SUPPLY



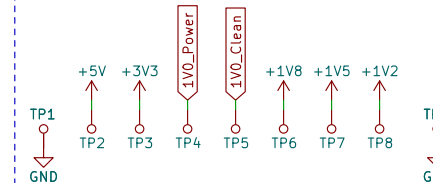
## POWER INDICATORS



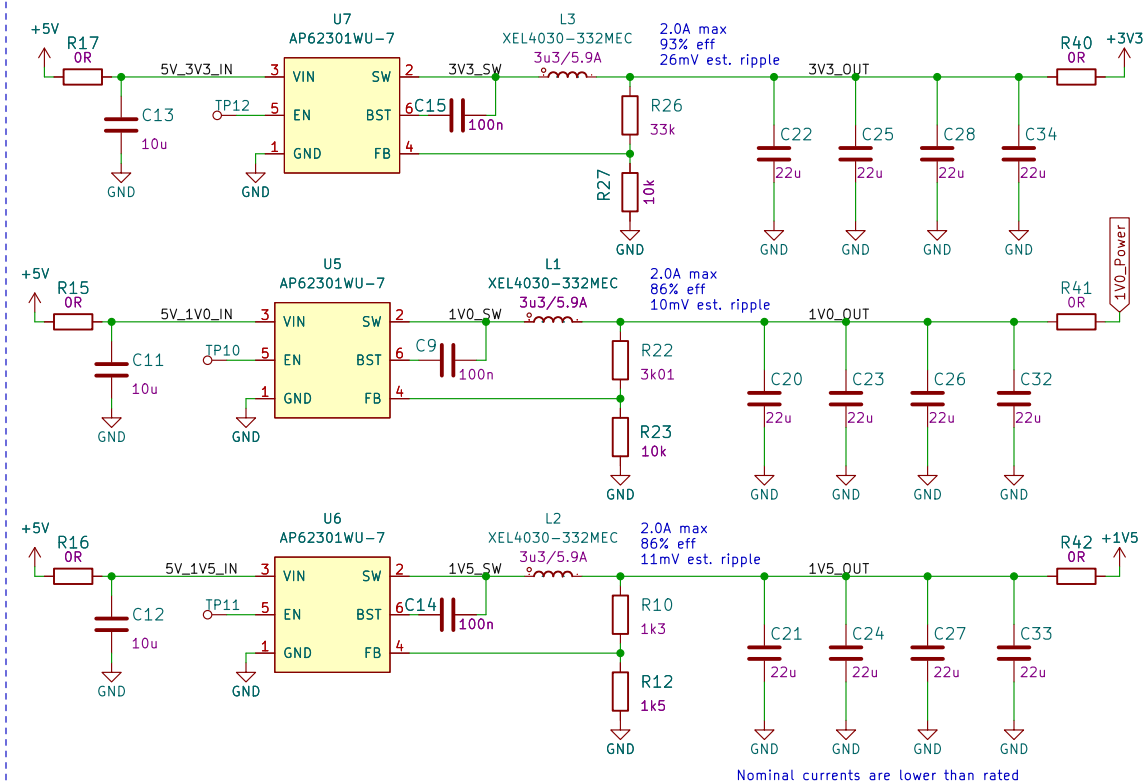
## PS BLOCK DIAGRAM



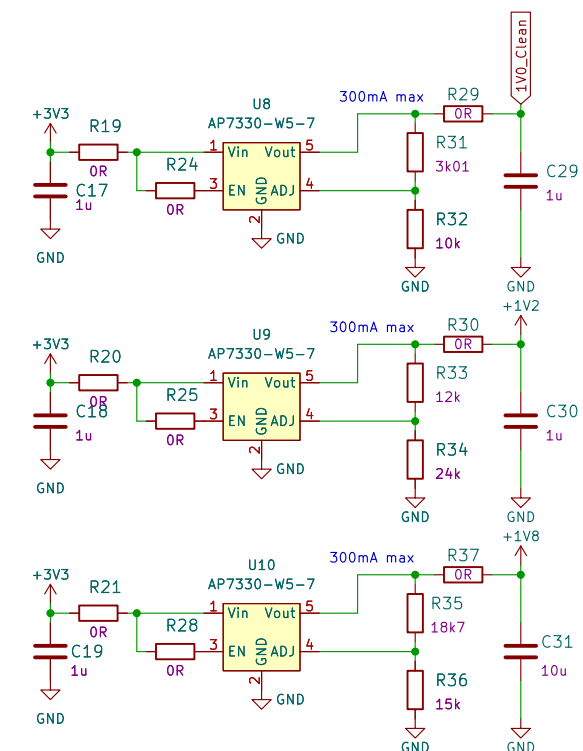
## TEST POINTS



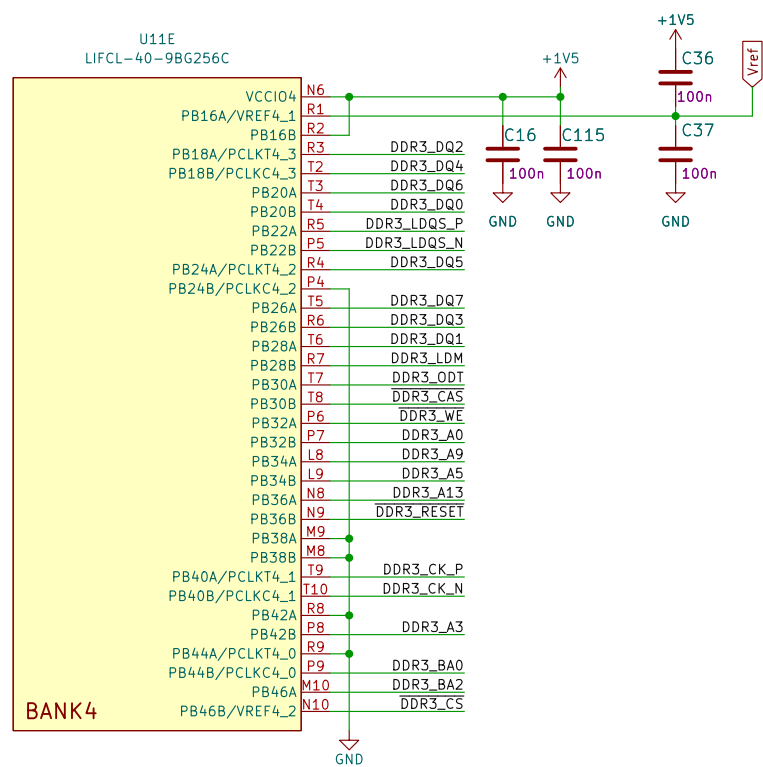
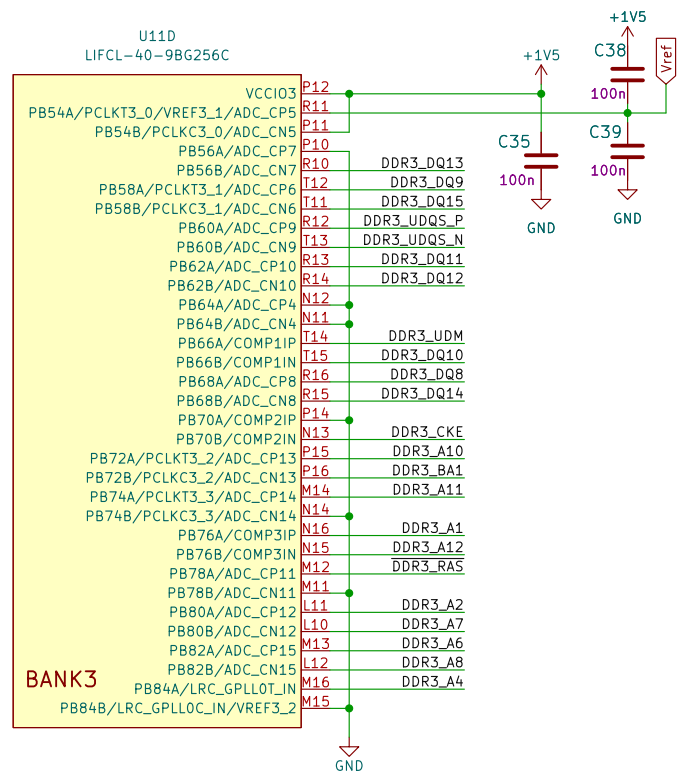
## CONVERTERS



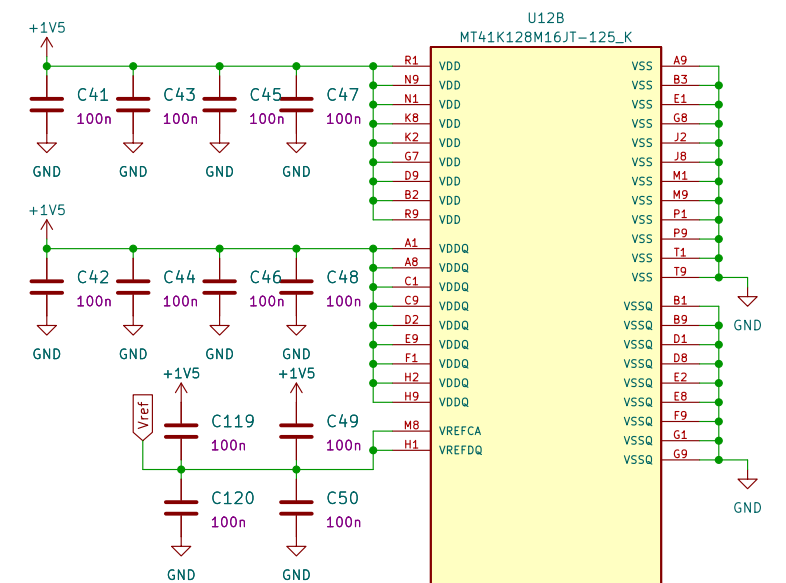
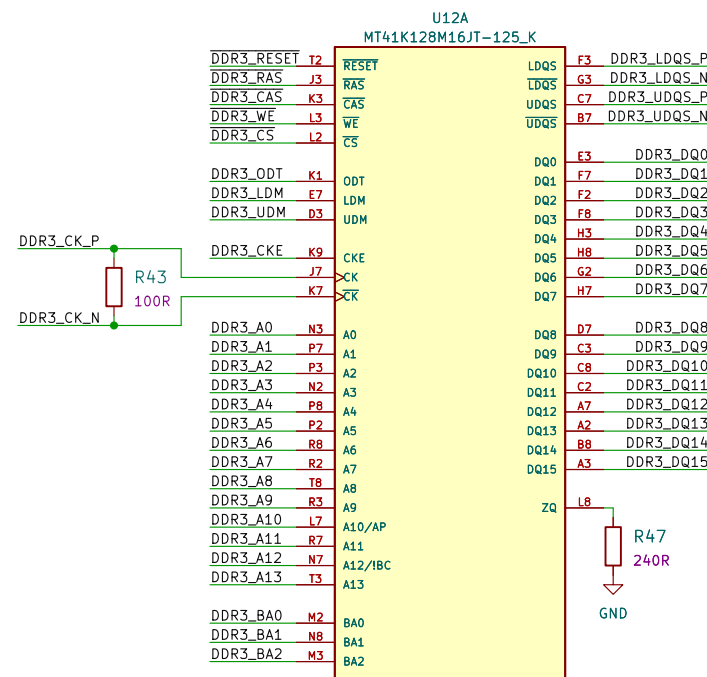
LDOs



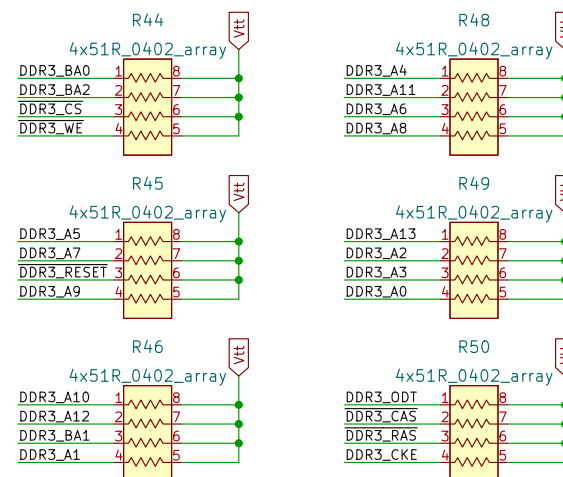
FPGA



RAM

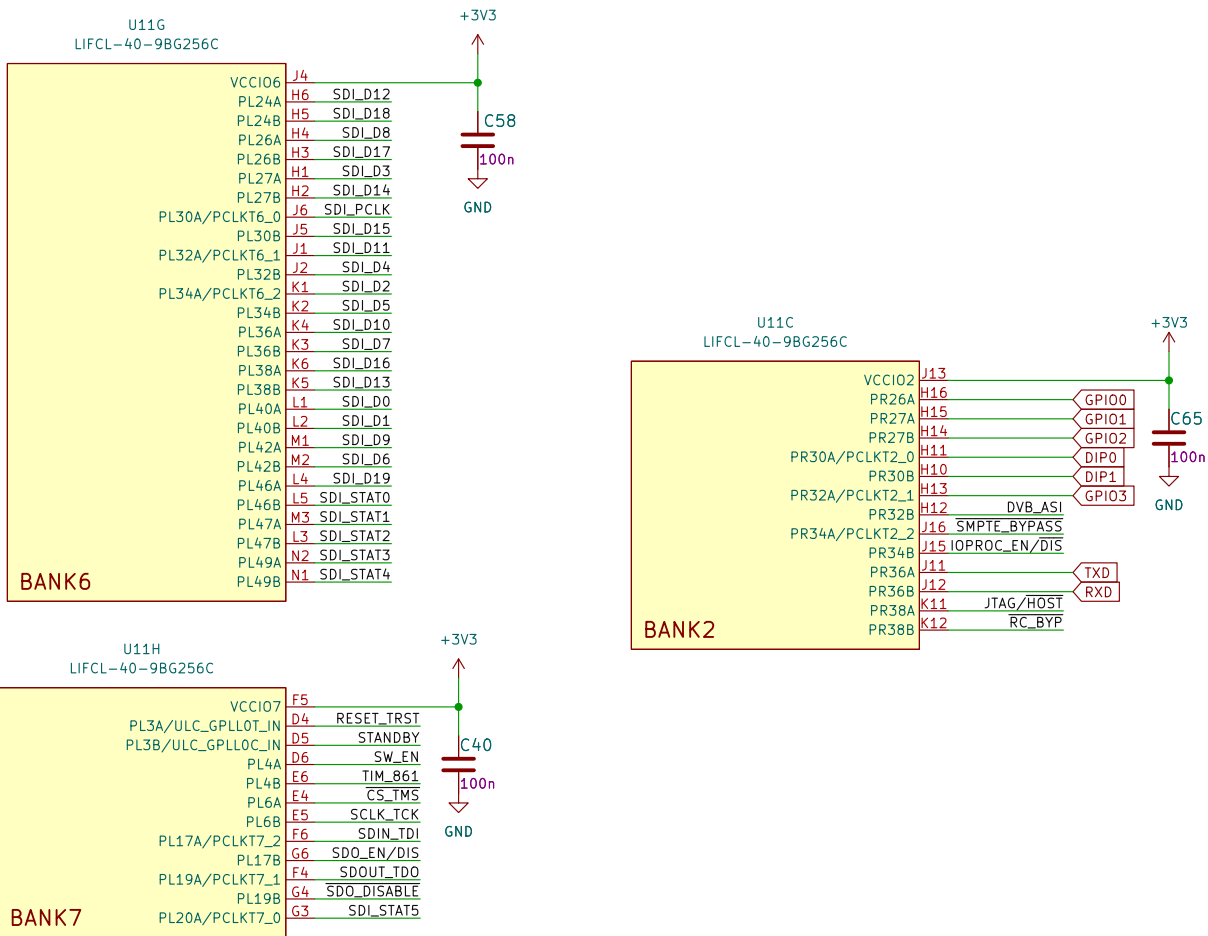


## TERMINATION

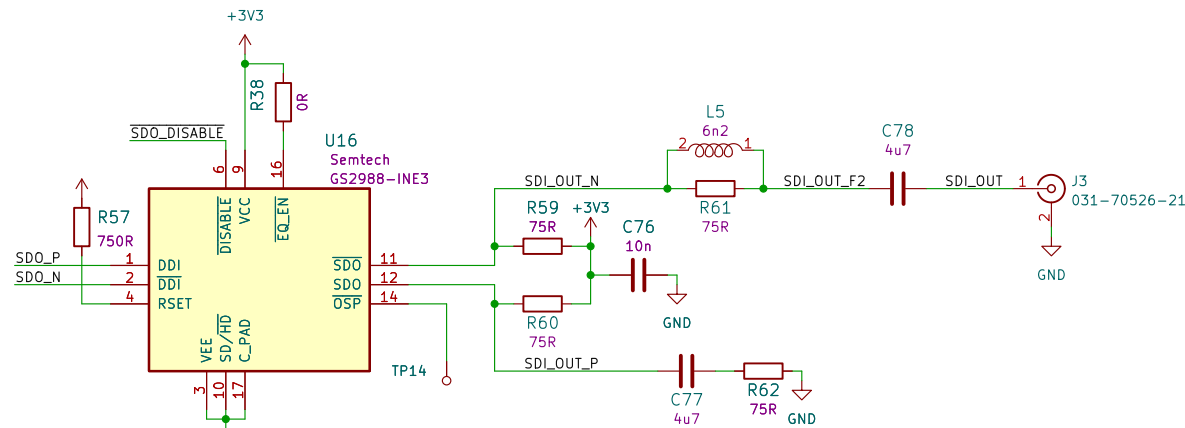


Signal terminations can be rearranged for easier routing

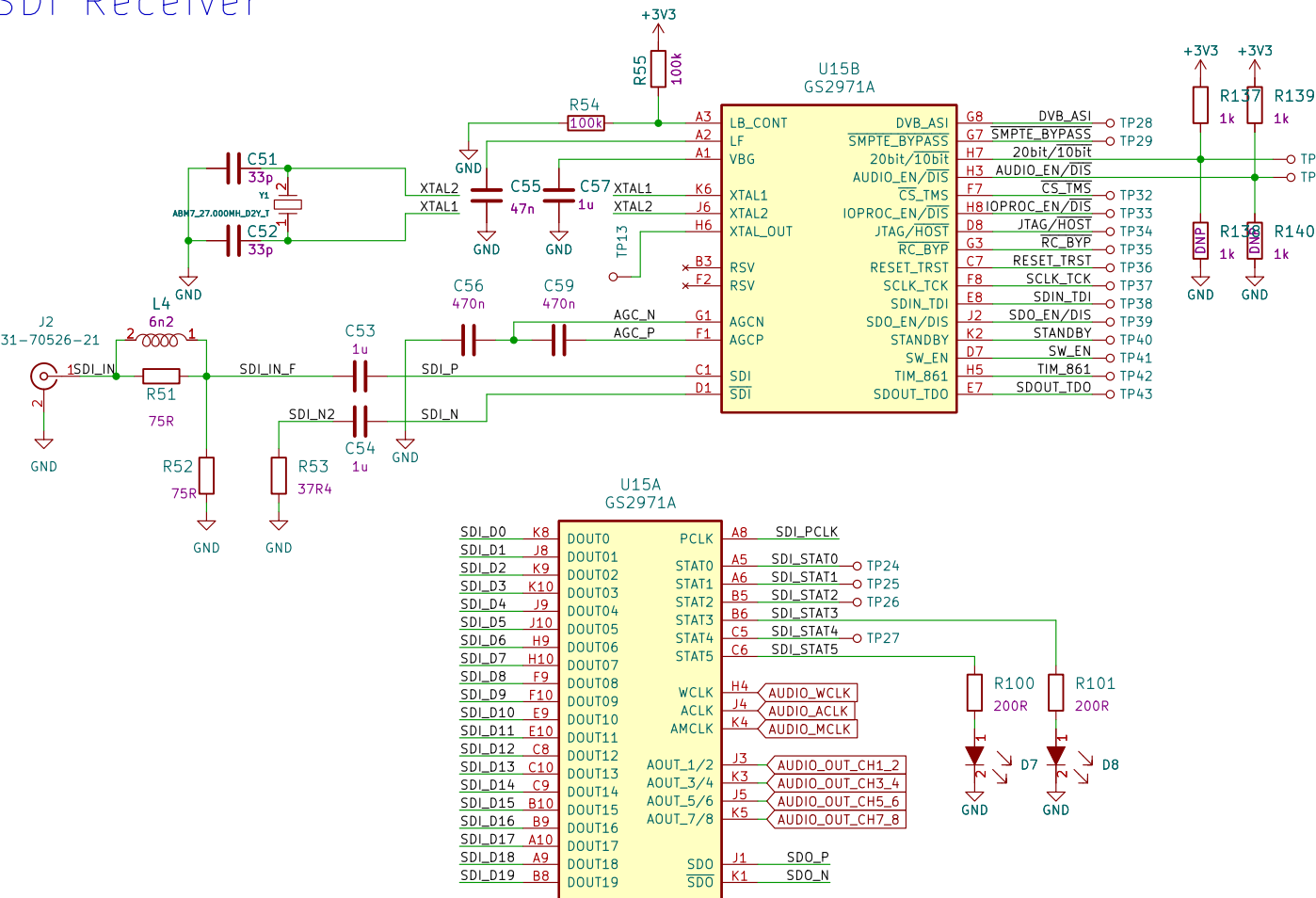
FPGA



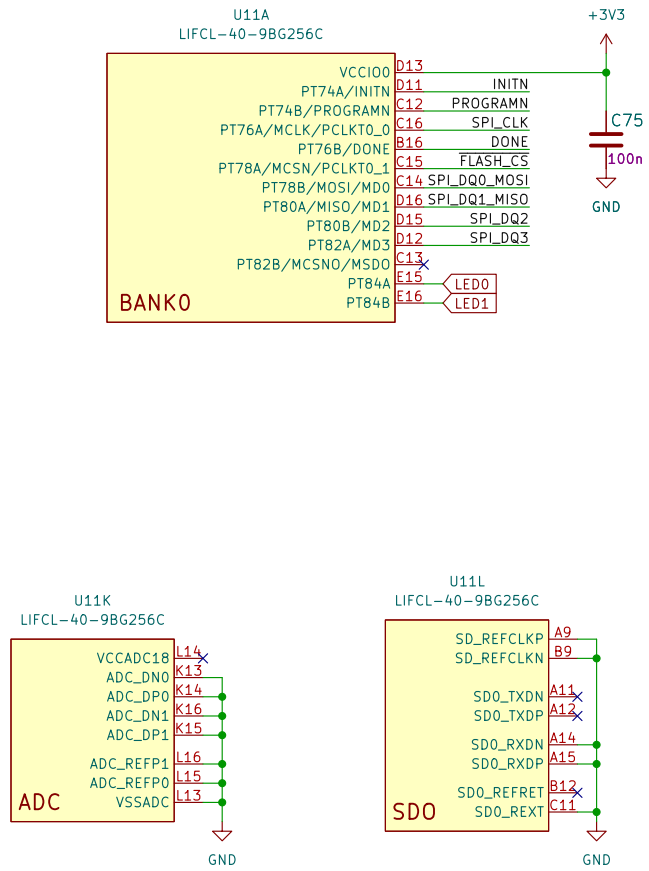
Cable driver



SDI Receiver

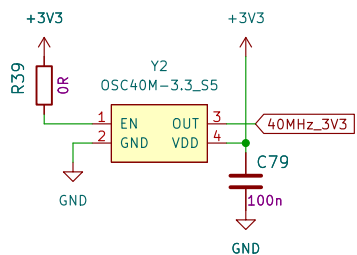


# FPGA



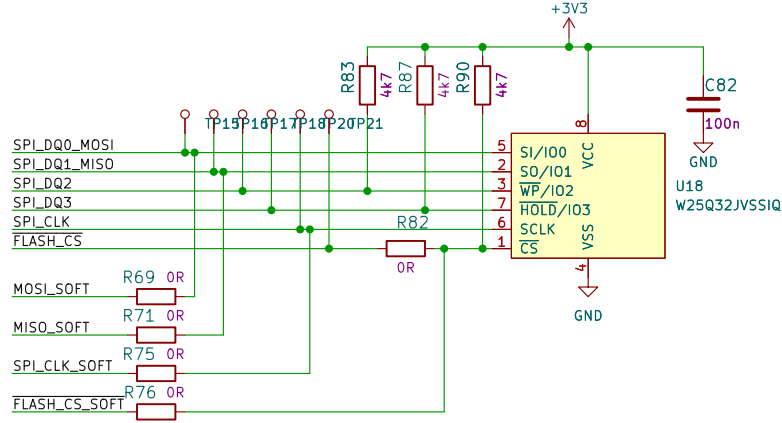
## OSCILLATOR

OPTIONAL, NORMALLY DNP

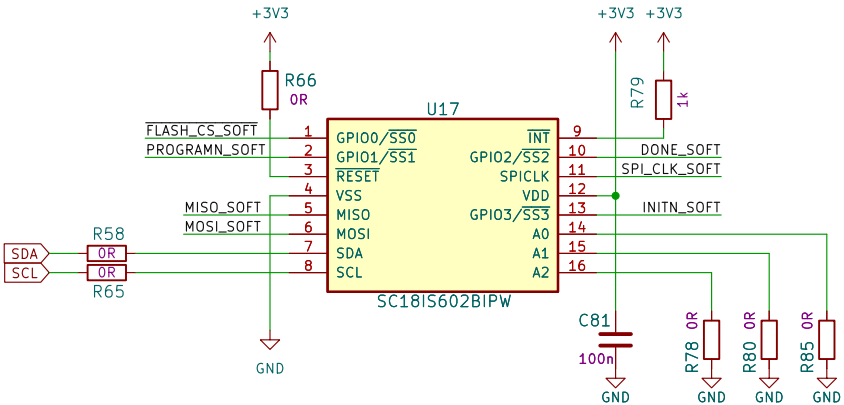


# FPGA CONFIGURATION

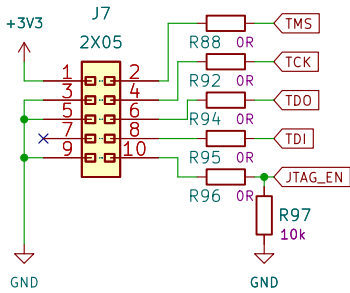
## CONFIGURATION FLASH



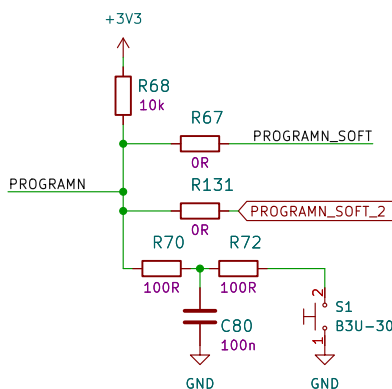
## I2C TO SPI BRIDGE



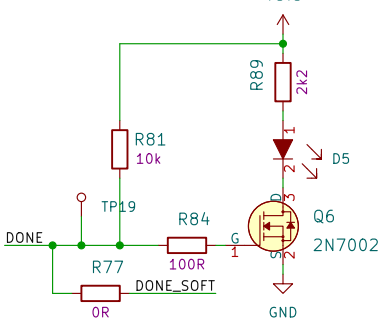
## JTAG CONNECTOR



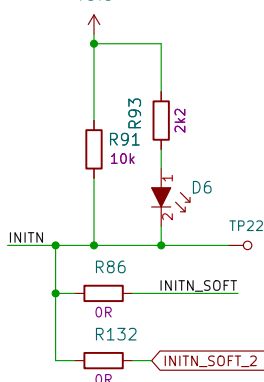
## PROGRAMN



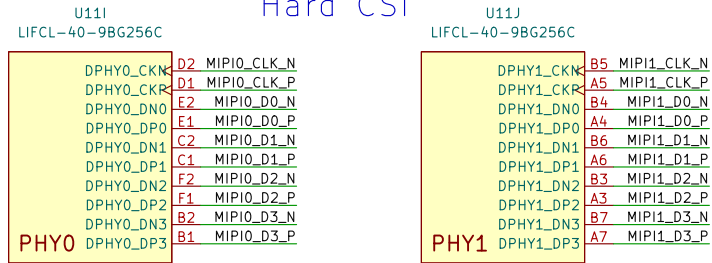
## DONE



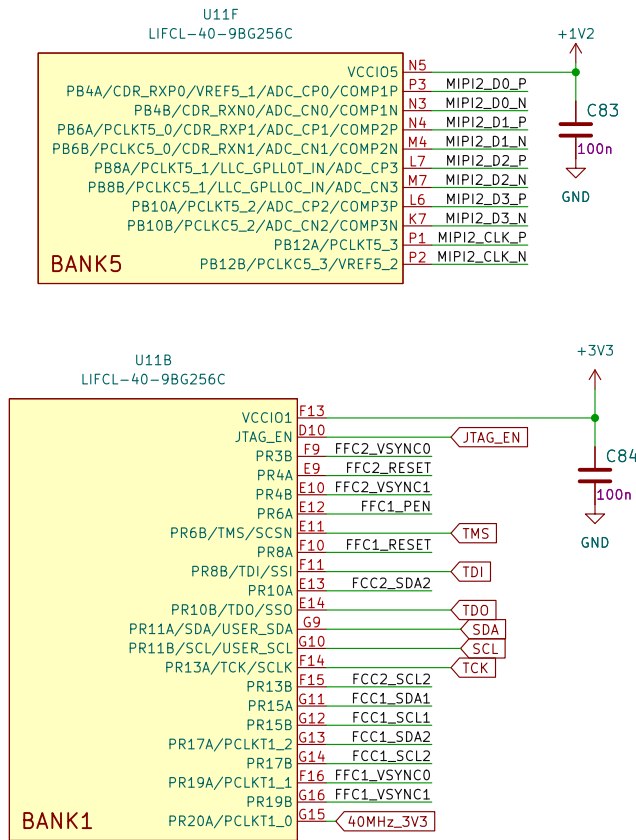
## INITN



FPGA  
Hard CSI

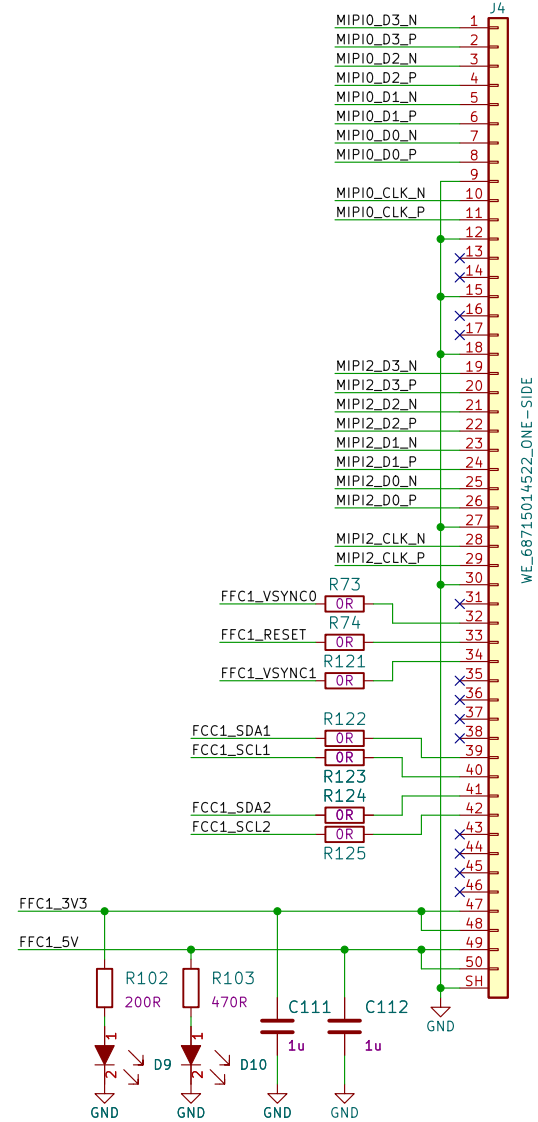


## Soft CSI

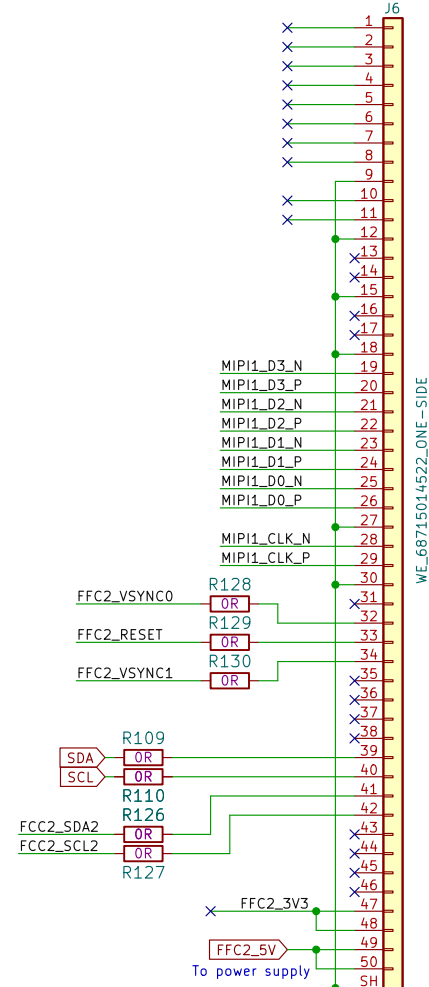


# CSI Connectors

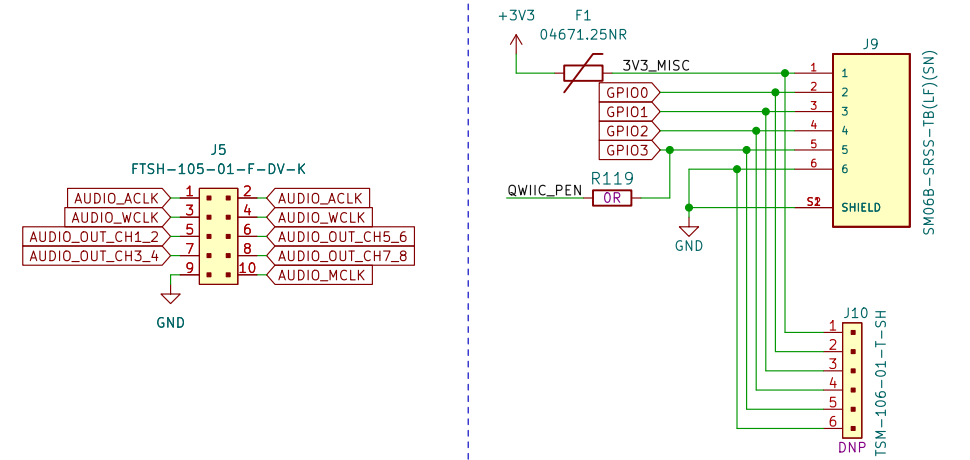
FFC1  
POWER SOURCE



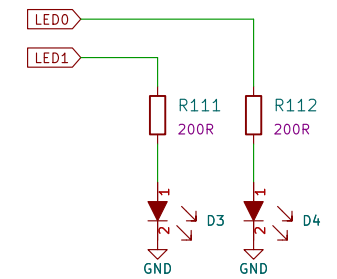
FFC2  
POWER SINK



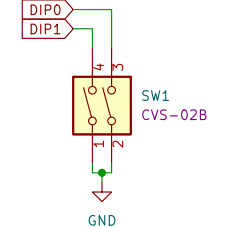
I2S CONNECTOR | GPIO CONNECTOR



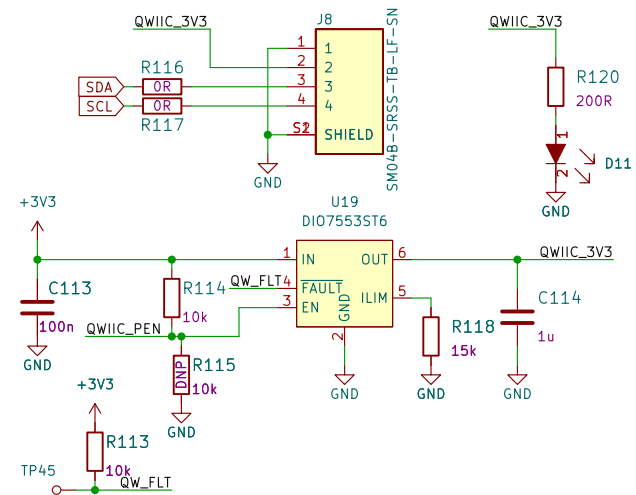
USER LEDs



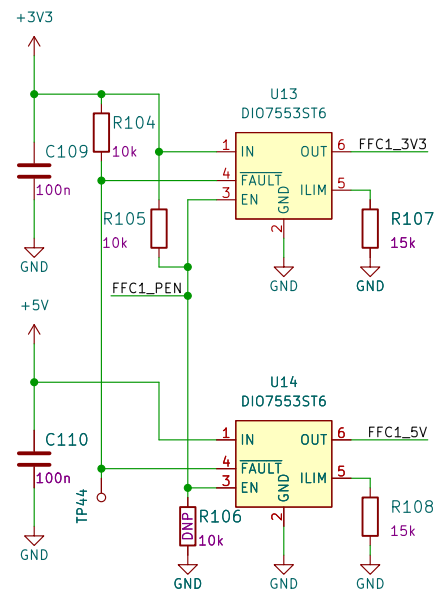
## USER SWITCH



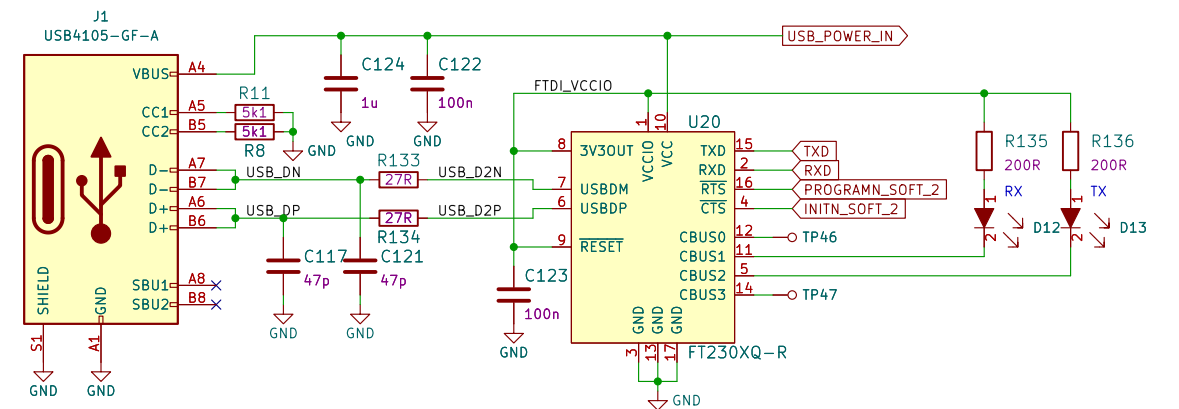
# QWIIC CONNECTOR

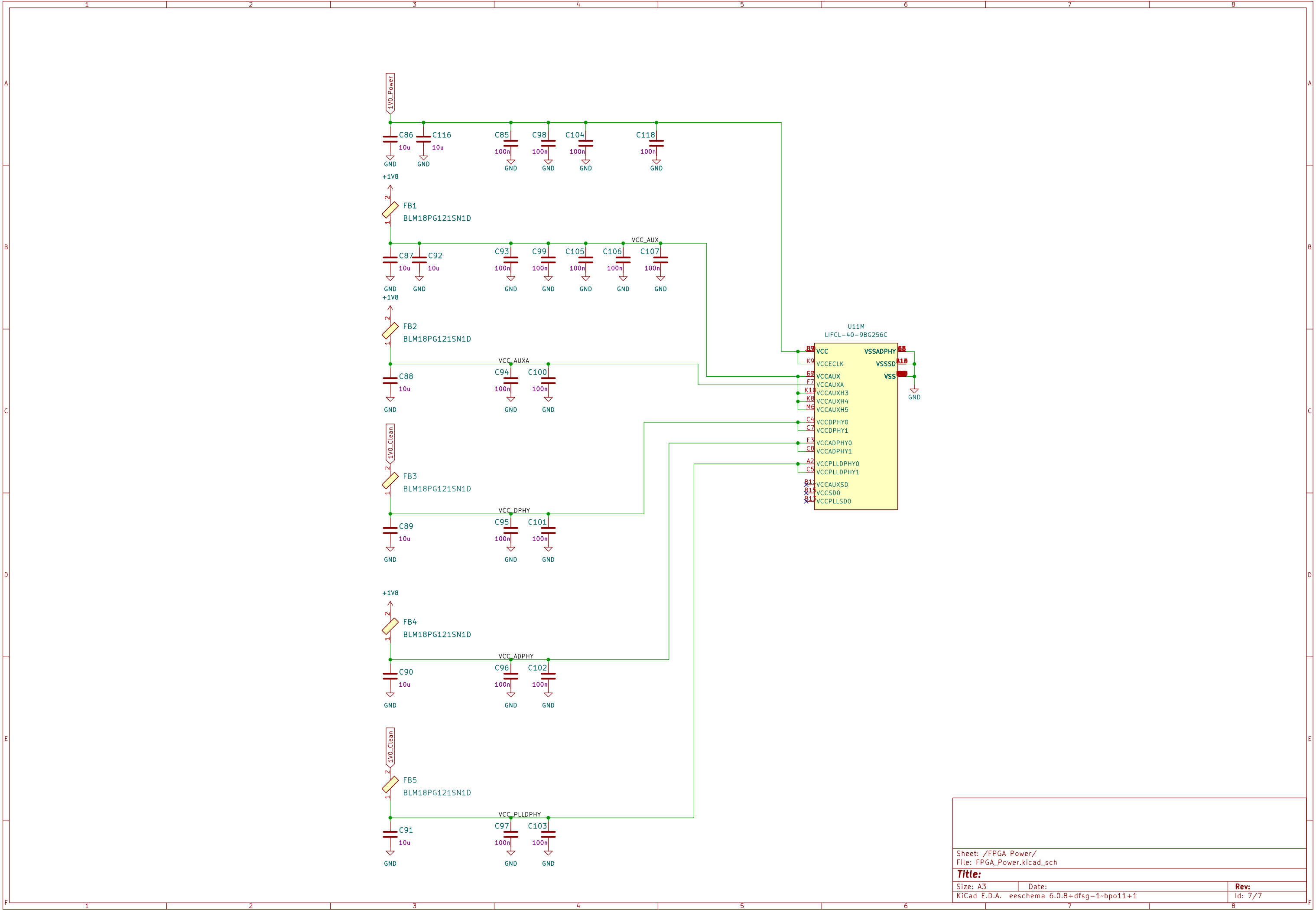


# FFC1 POWER SWITCH



## USB-C CONNECTOR





Sheet: /FPGA Power/ File: FPGA_Power.kicad_sch		
<b>Title:</b>		
Size: A3	Date:	Rev:
KiCad E.D.A. eeschema 6.0.8+dfsg-1-bpo11+1		Id: 7/7