







FPGA FPGA CONFIGURATION CONFIGURATION FLASH U11A LIFCL-40-9BG256C +3V3 P15P16P17P18P20P21 SPI_DQ0_MOSI SPI_DQ1_MISO SPI_DQ2 5 SI/100 S SO/101 S SO/101 S WF/102 HOLD/103 SCLK SO SCL SO PT74A/INITN C12 PROGRAMN C16 SPI CLK PT74B/PROGRAMN PT76A/MCLK/PCLKTO_0 PT76B/DONE PT78B/MOSI/MD0 PT80A/MISO/MD1 PT80B/MD2 PT80B/MD2 PT80B/MD2 PT80B/MD2 PT80B/MD3 PT82B/MCSNO/MSD0 PT80B/MD2 PT80B/MD3 PT82B/MCSNO/MSD0 PT84A PT84A PT84B SPI_DQ3 SPI_CLK FLASH_CS W25Q32JVSSIQ GND BANK0 FLASH_CS_SOFT JTAG CONNECTOR +3V3 12C TO SPI BRIDGE +3V3 +3V3 U11L LIFCL-40-9BG256C U11K LIFCL-40-9BG256C SD_REFCLKP B9 | 10 | DONE_SOFT | 11 | SPI_CLK_SOFT | SDO_TXDN A11X SDO_TXDP A12X SDO_RXDN A14 SDO_RXDP A15 INITN_SOFT MISO_SOFT ADC_REFPO L15 VSSADC L13 6 MOSI 7 SDA SDO_REFRET B12 SDO_REXT C11 R78 OR R80 OR CASE OR PROGRAMN DONE INITN +3V3 PROGRAMN_SOFT OR PROGRAMN R131 PROGRAMN_SOFT_2 R70 R72 100R 100R S1 B3U-3000PM OPTIONAL, NORMALLY DNP C80 R77 DONE_SOFT OR OR INITN_SOFT GND R132 INITN_SOFT_2 +3V3 +3V3 OSC40M-3.3_S5 EN OUT 3 GND VDD 4 Sheet: /FPGA/ File: fpga.kicad_sch Title: Size: A3 Date: KiCad E.D.A. eeschema 6.0.8+dfsg-1~bpo11+1



