

SDI



File: sdi.kicad_sch

FPGA



File: fpga.kicad_sch

FPGA Power



File: FPGA_Power.kicad_sch

Power Supply



File: supply.kicad_sch

DDR3



File: ddr3.kicad_sch

Peripherals



File: peripherals.kicad_sch



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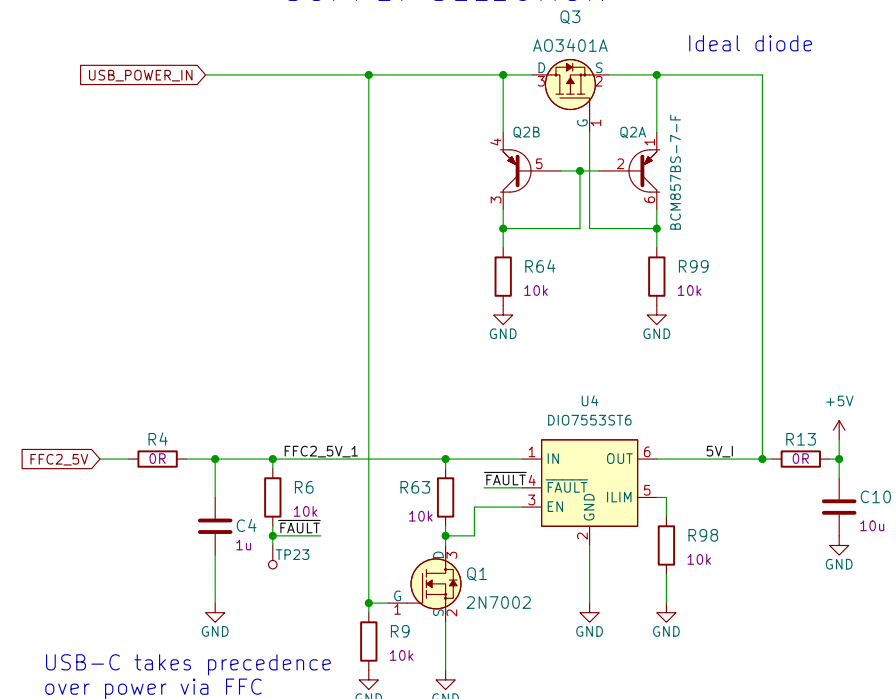
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Title: SDI-MIPI Video Converter

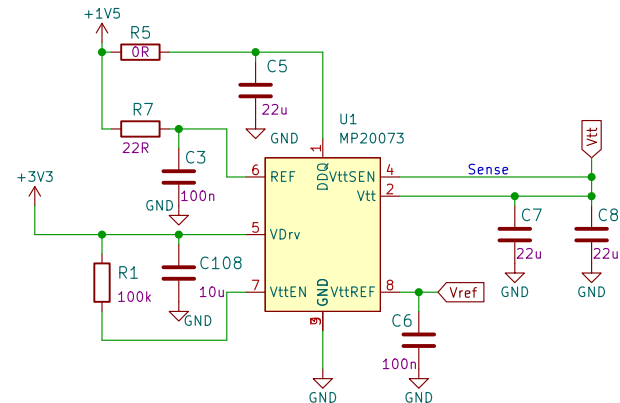
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KiCad E.D.A. eeschema 6.0.5+dfsg-1-bpo11+1

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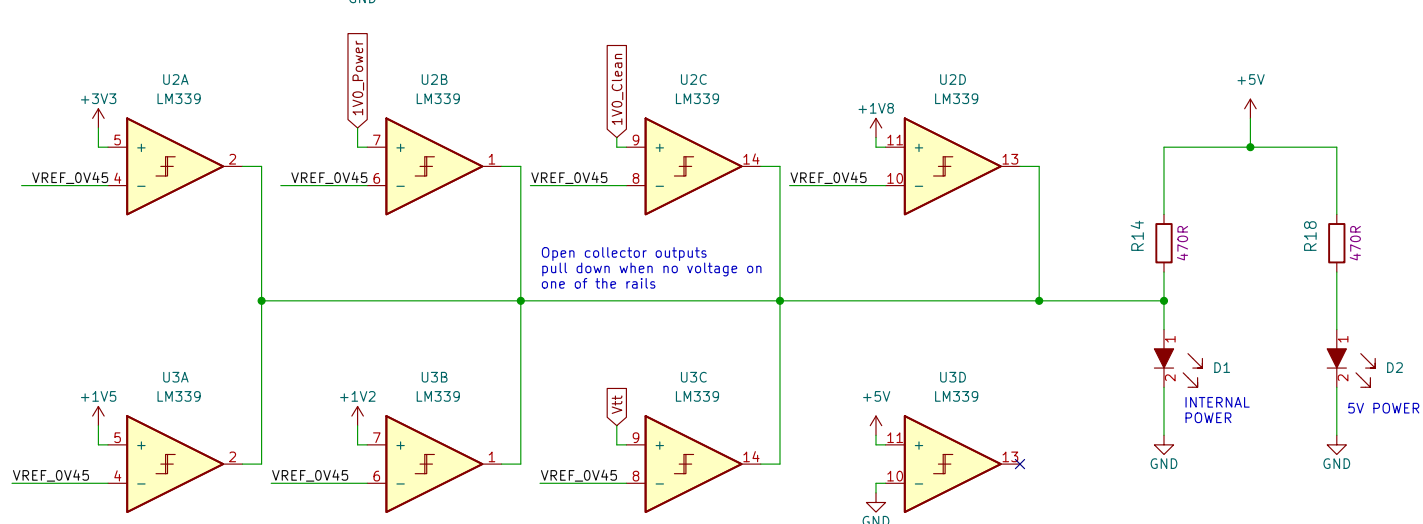
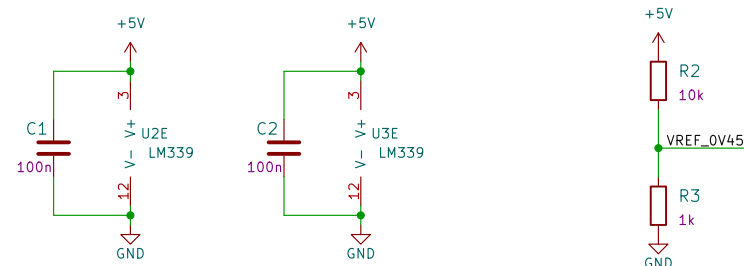
SUPPLY SELECTION



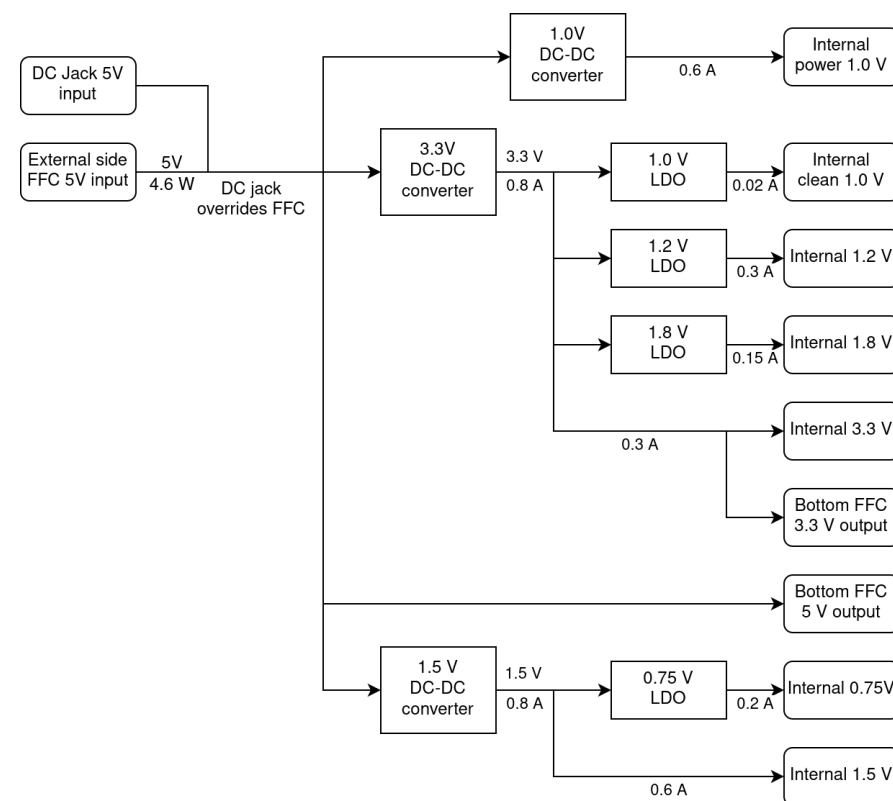
DDR MEMORY TERMINATION SUPPLY



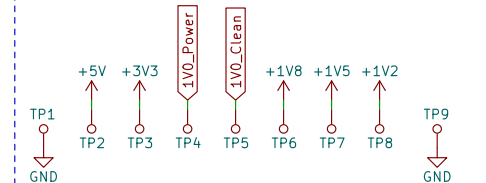
POWER INDICATORS



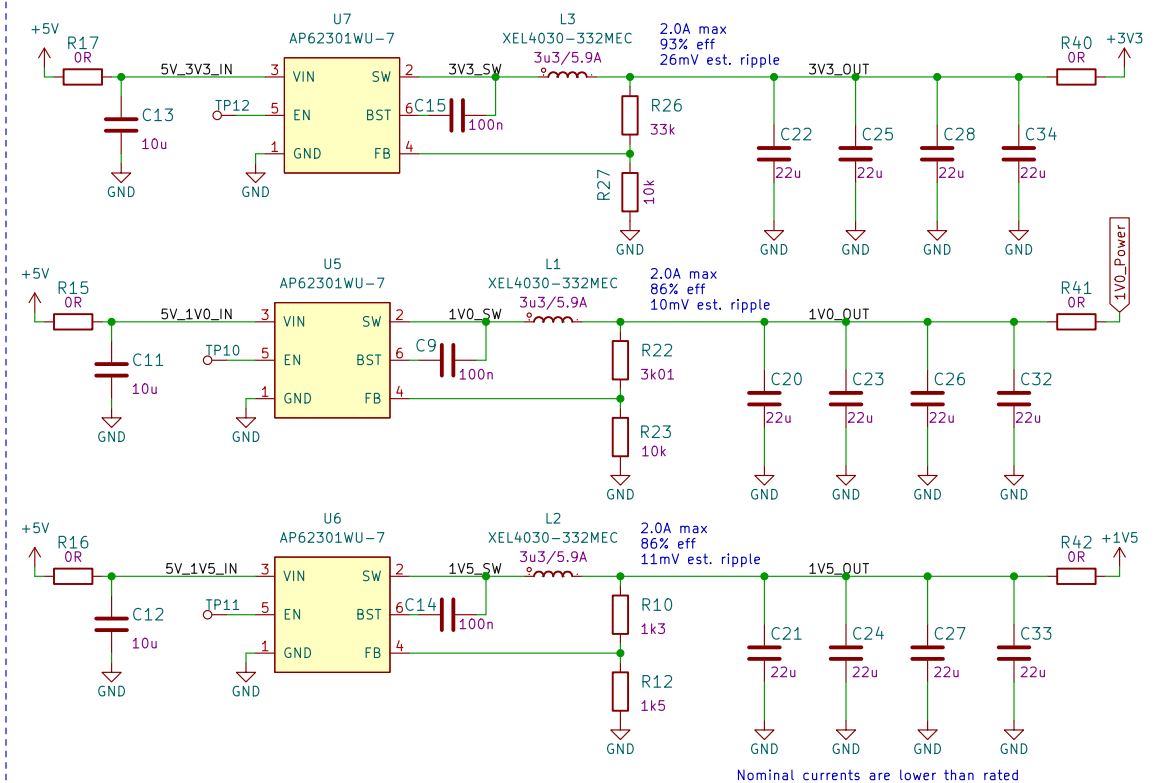
PS BLOCK DIAGRAM



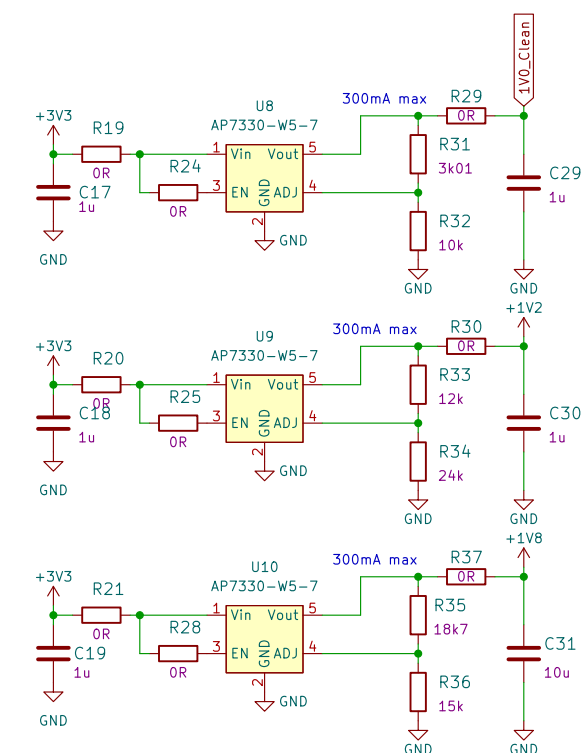
TEST POINTS



CONVERTERS



LDOs



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File: supply.kicad_sch

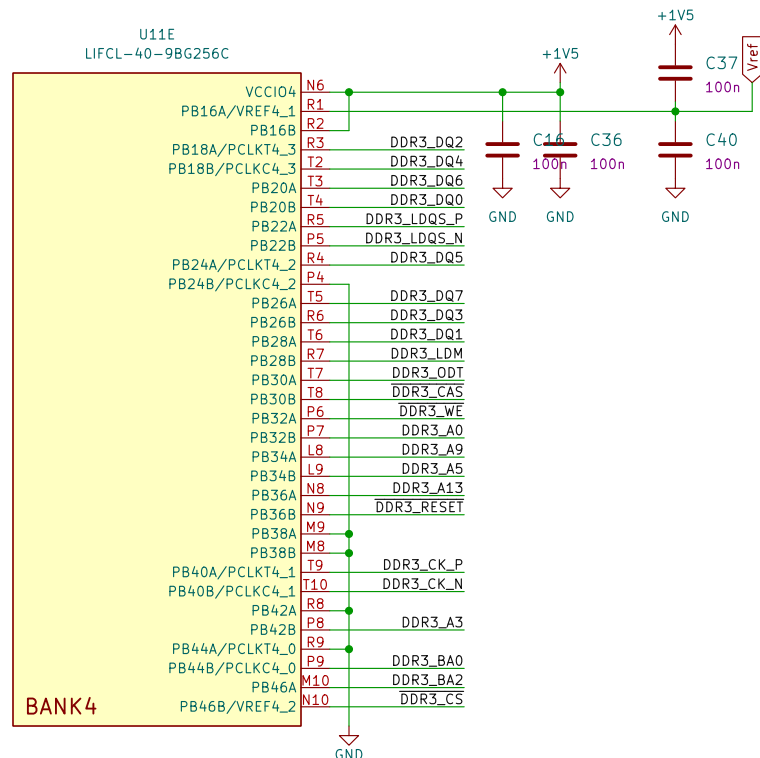
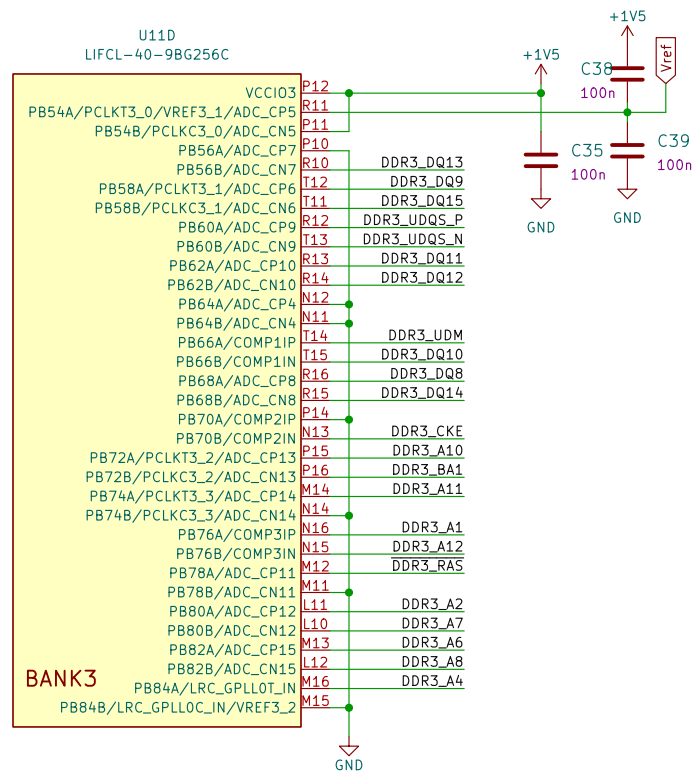
Title: SDI-MIPI Video Converter

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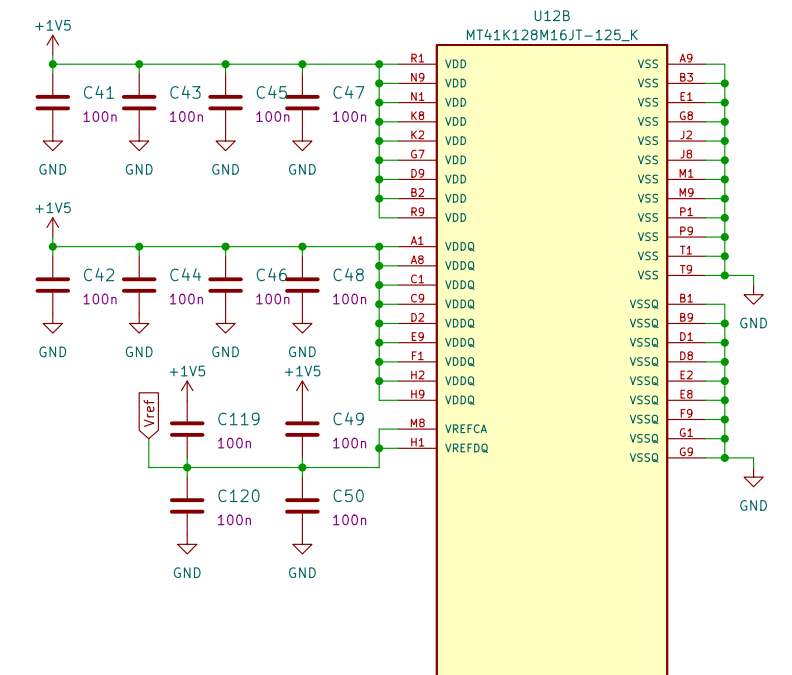
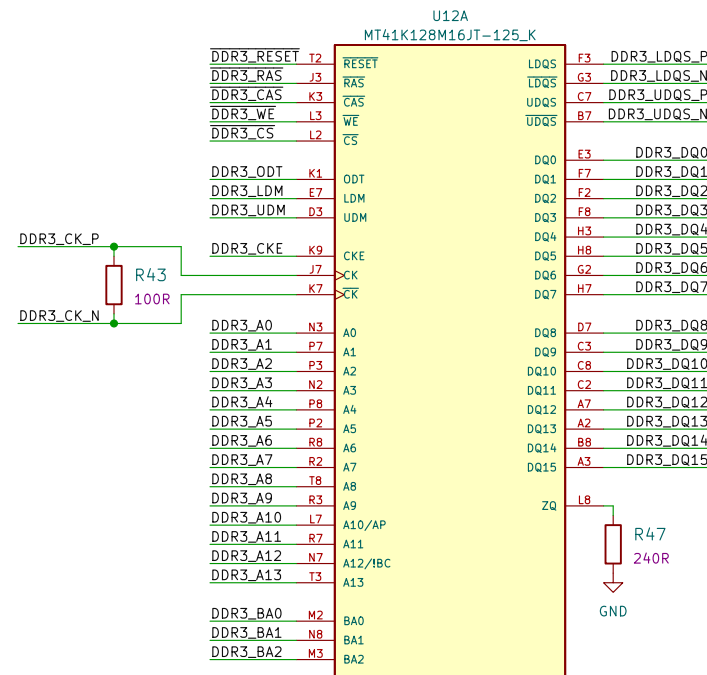
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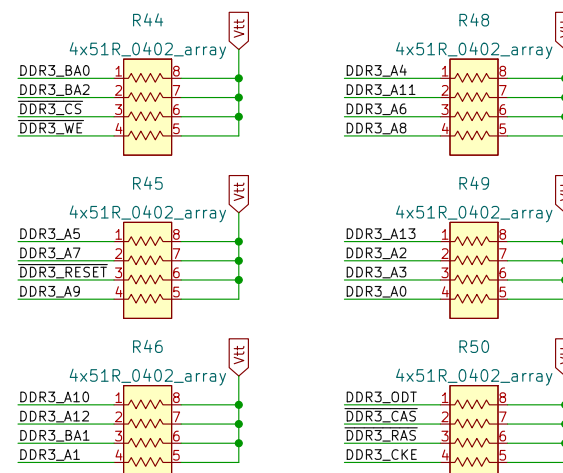
FPGA



RAM



TERMINATION



Signal terminations can be rearranged for easier routing



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Sheet: /DDR3/
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Cable driver

The schematic diagram illustrates a cable driver circuit for a USB 3.0 cable. The central component is the Semtech GS2988-INE3 transceiver (U16). The circuit is powered by a +3V3 supply. Key components and connections include:

- Transceiver (U16):** Semtech GS2988-INE3. Pins include SDO_DISABLE, VCC, EQ_EN, RSET, SDO, SDO_P, SDO_N, and SDI_OUT.
- Resistors:**
 - R57 (750R) connected to SDO_P and SDO_N.
 - R59 (75R) connected to SDI_OUT_N and +3V3.
 - R60 (75R) connected to SDI_OUT_P and +3V3.
 - R61 (75R) connected to SDI_OUT_F2 and +3V3.
 - R62 (75R) connected to SDI_OUT_P and GND.
 - R38 (0R) connected to VCC and EQ_EN.
- Capacitors:**
 - C76 (10n) connected to +3V3 and GND.
 - C77 (4u7) connected to SDI_OUT_P and GND.
 - C78 (4u7) connected to SDI_OUT_F2 and GND.
- Inductor:** L5 (6n2) connected to SDI_OUT_F2 and +3V3.
- Connector (J3):** 031-70526-21, connected to SDI_OUT.
- Test Point (TP14):** Connected to SDO.

SDI Receiver

U15B GS2971A

U15C GS2971A

U15A GS2971A

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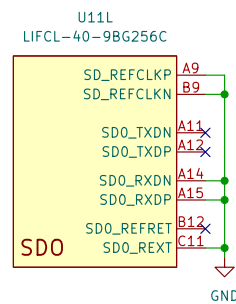
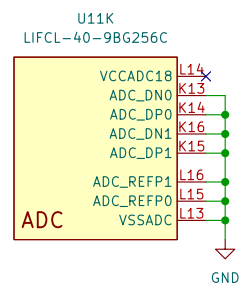
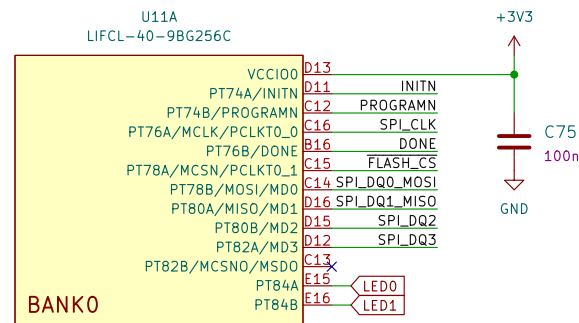
open source hardware

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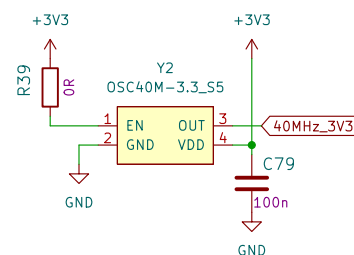
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FPGA



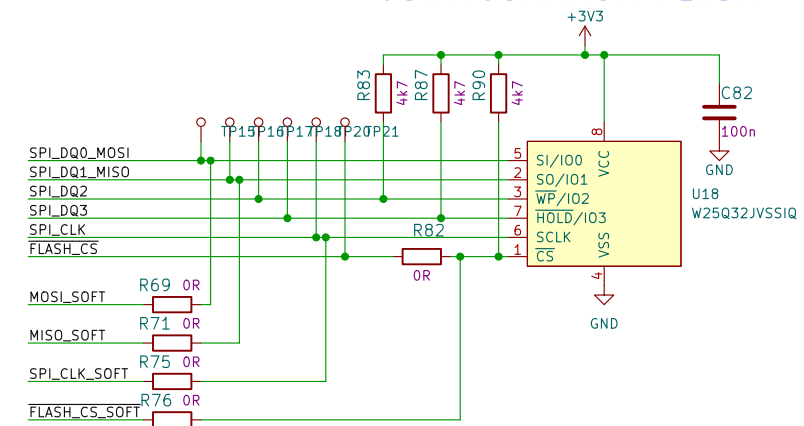
OSCILLATOR

OPTIONAL, NORMALLY DNP

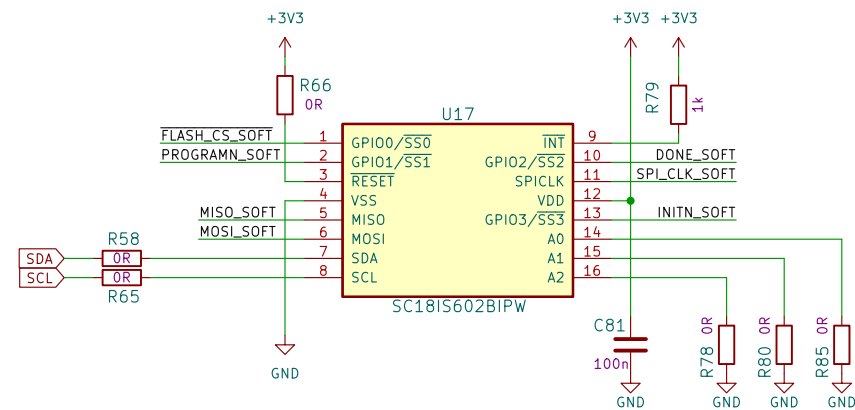


FPGA CONFIGURATION

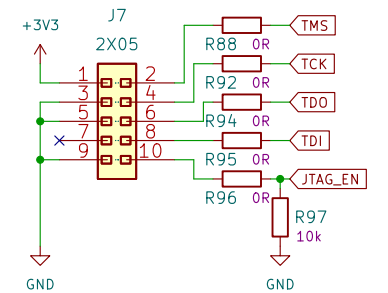
CONFIGURATION FLASH



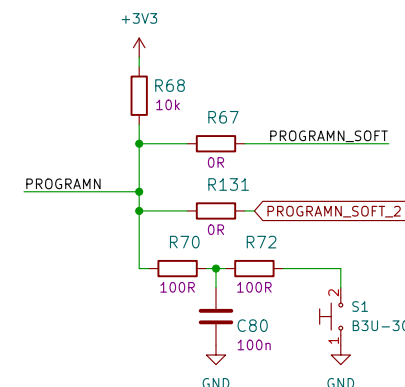
I2C TO SPI BRIDGE



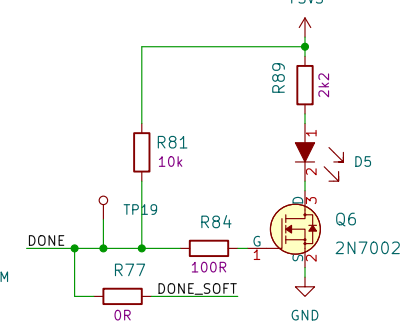
JTAG CONNECTOR



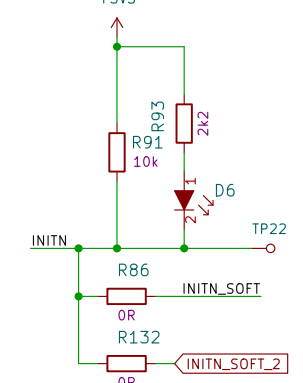
PROGRAMN



DONE



INITN



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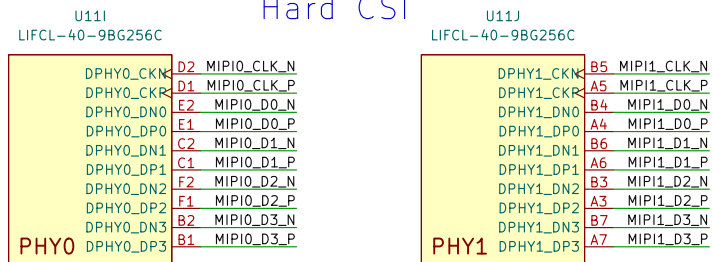
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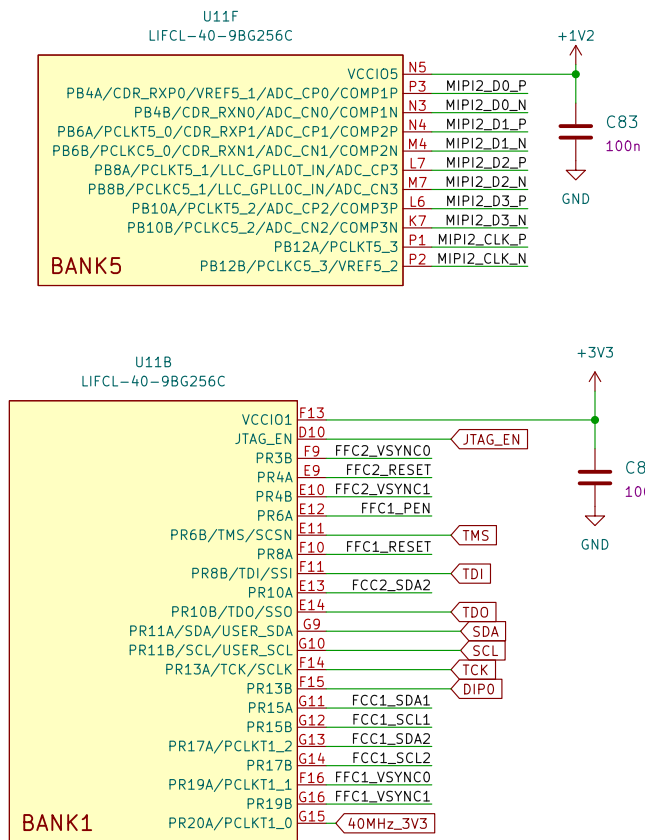
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FPGA
Hard CSI

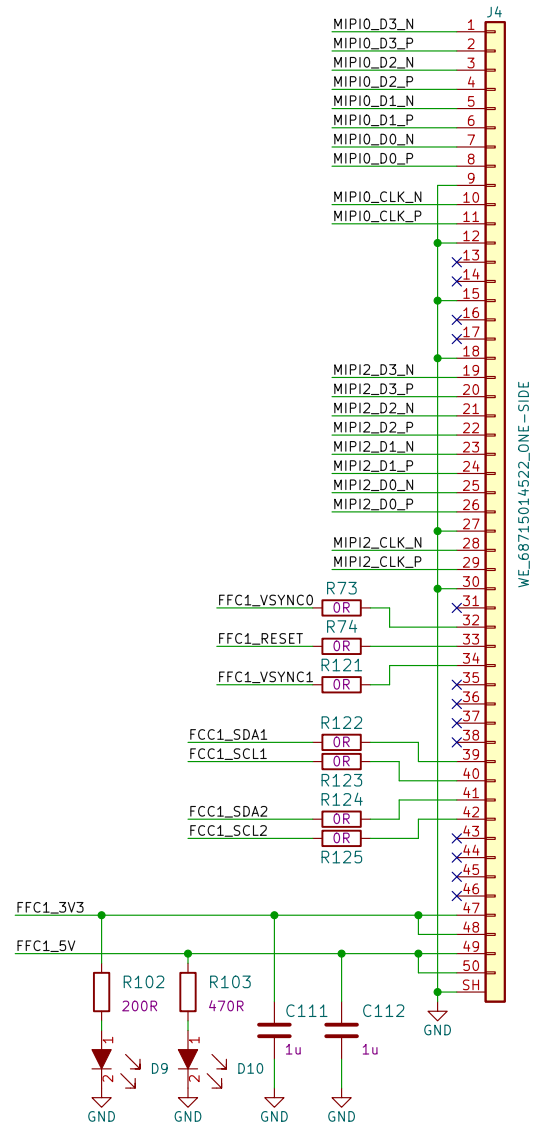


Soft CSI

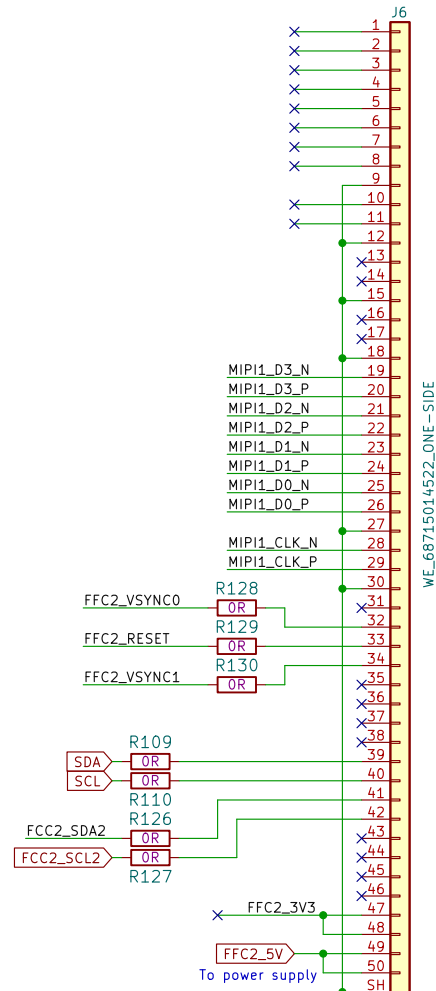


CSI Connectors

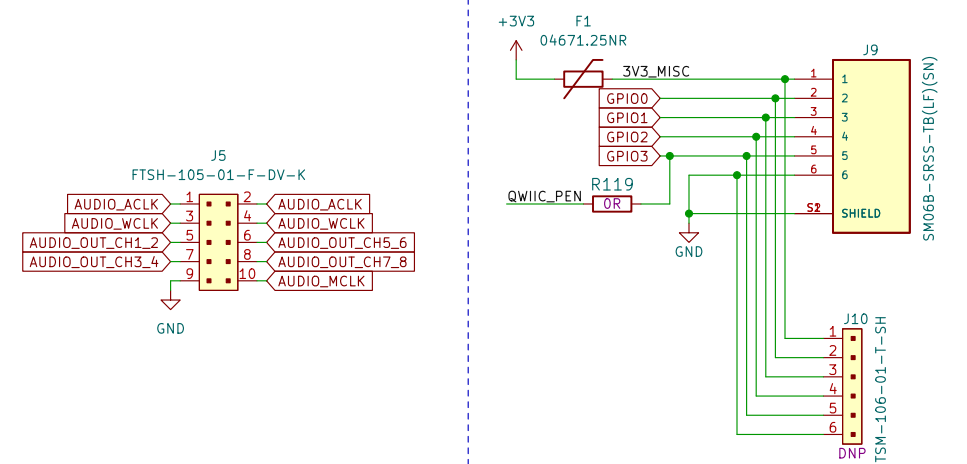
FFC1
POWER SOURCE



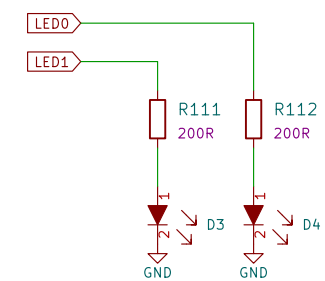
FFC2
POWER SINK



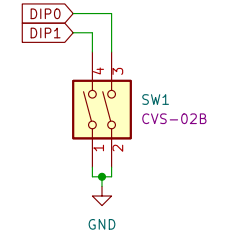
I2S CONNECTOR | GPIO CONNECTOR



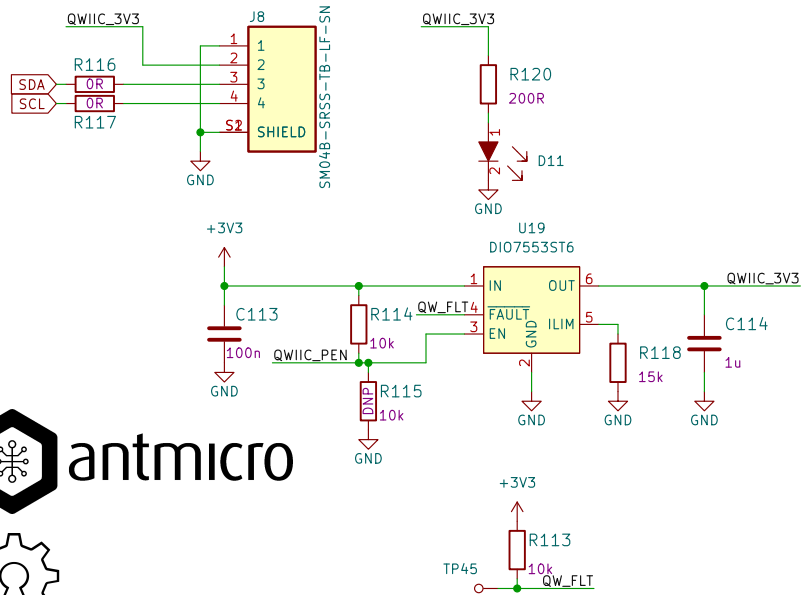
USER LEDs



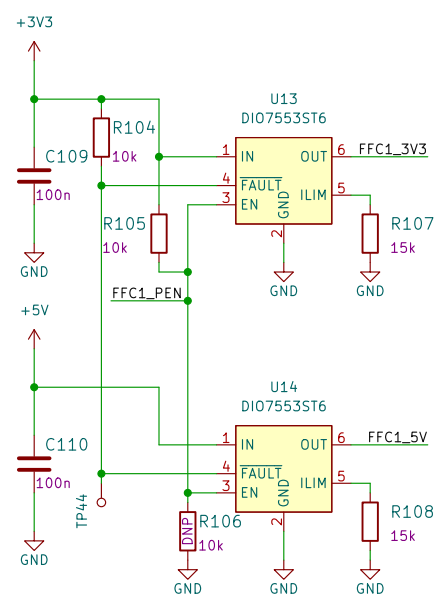
USER SWITCH



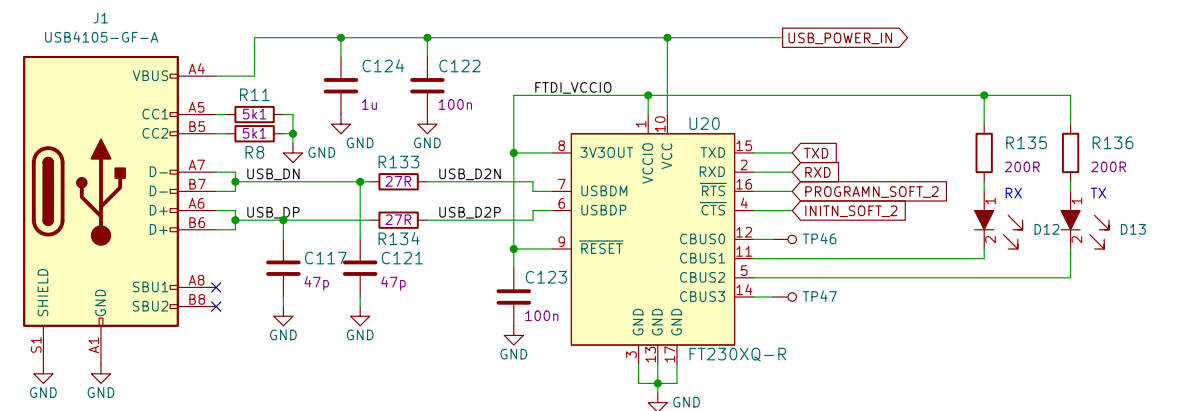
QWIIC CONNECTOR



FFC1 POWER SWITCH



USB-C CONNECTOR



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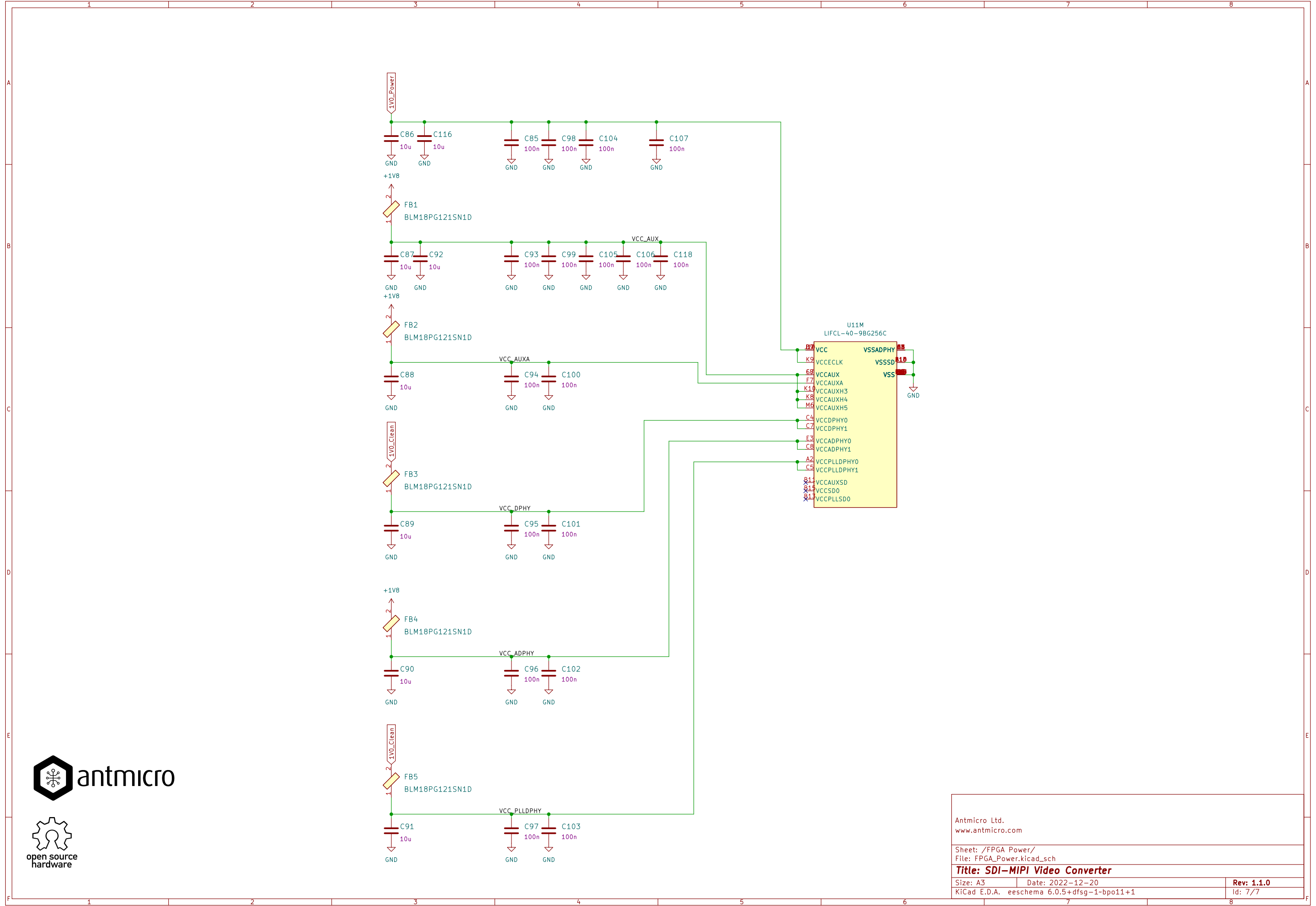
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KiCad E.D.A. eeschema 6.0.5+dfsg-1-bpo11+1		Id: 7/7