

SDI



File: sdi.kicad_sch

FPGA



File: fpga.kicad_sch

FPGA Power



File: FPGA_Power.kicad_sch

Power Supply



File: supply.kicad_sch

DDR3



File: ddr3.kicad_sch

Peripherals



File: peripherals.kicad_sch



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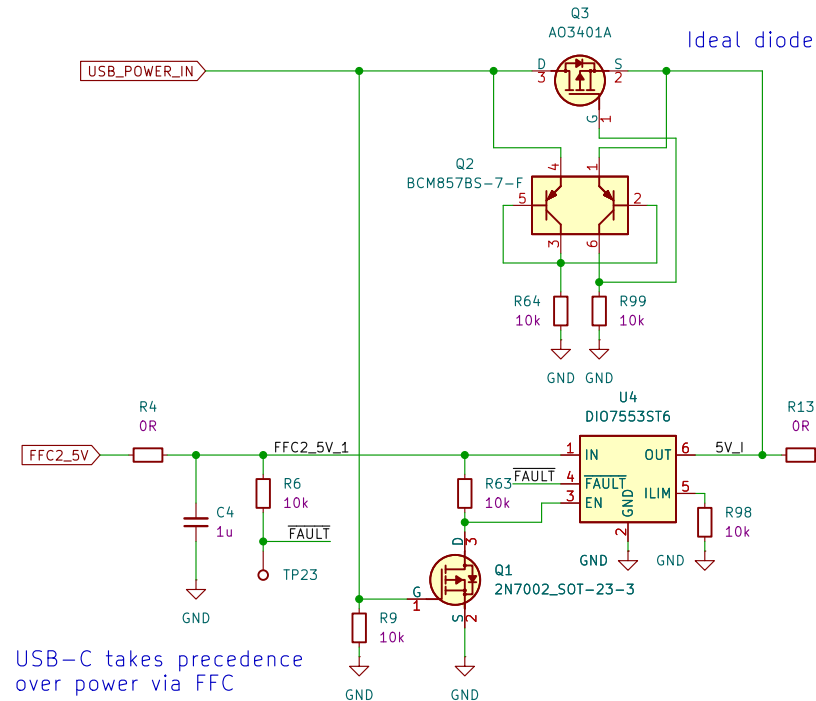
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Title: SDI-MIPI Video Converter

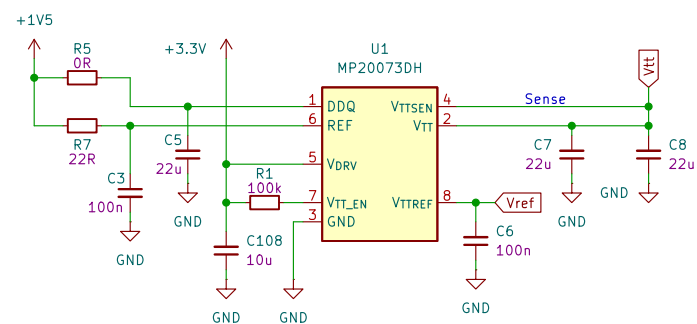
Size: A3 Date: 2023-10-12
KiCad E.D.A. eeschema 6.0.11+dfsg-1-bpo11+1

Rev: 1.1.4
Id: 1/7

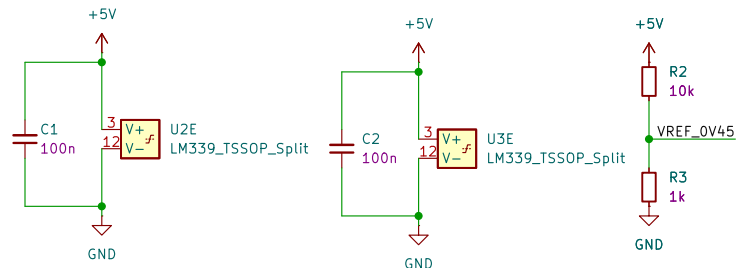
SUPPLY SELECTION



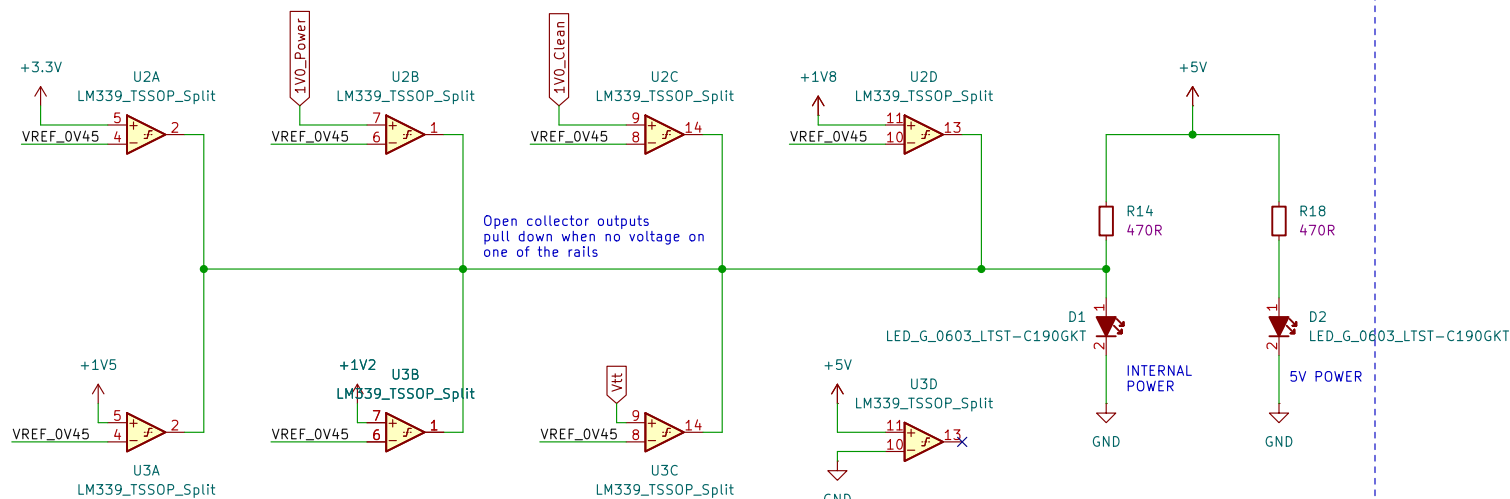
DDR MEMORY TERMINATION SUPPLY



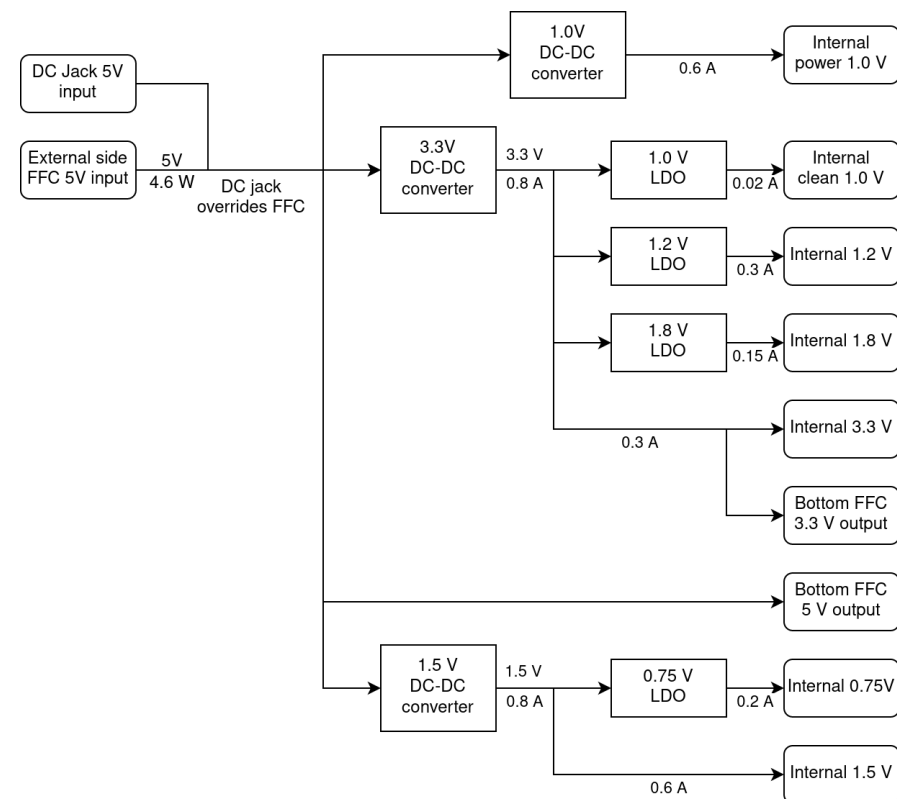
POWER INDICATORS



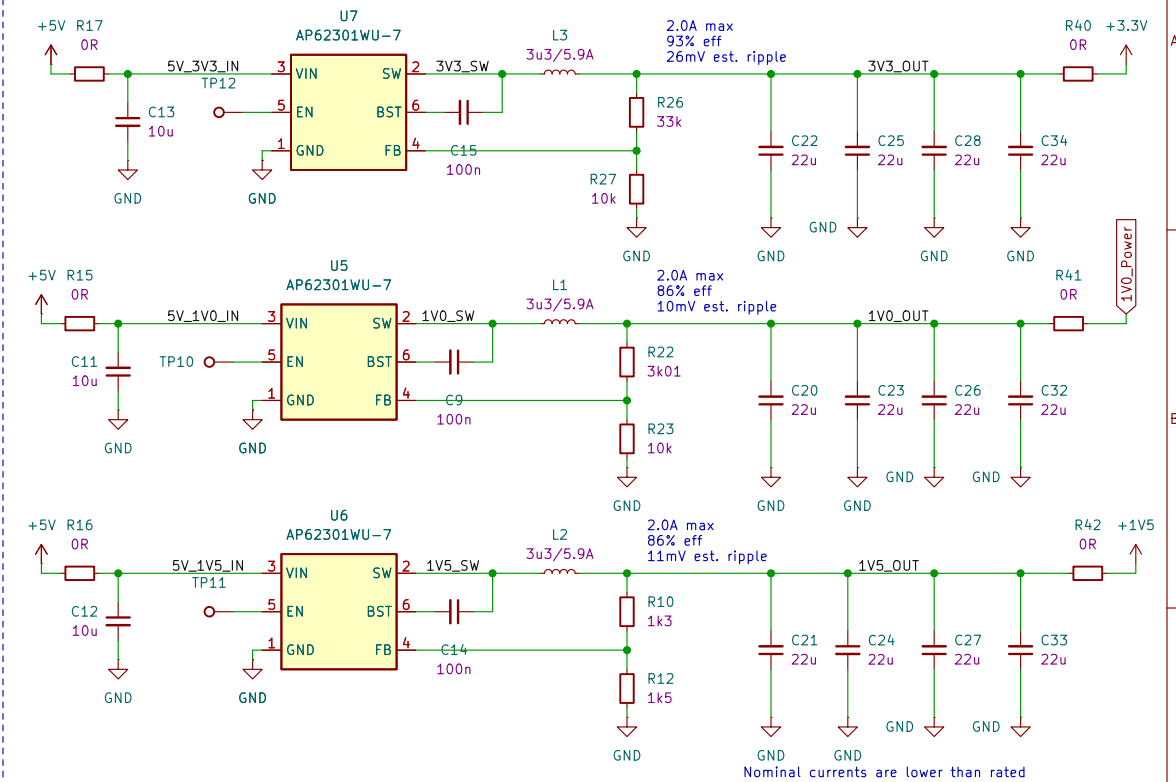
POWER INDICATORS



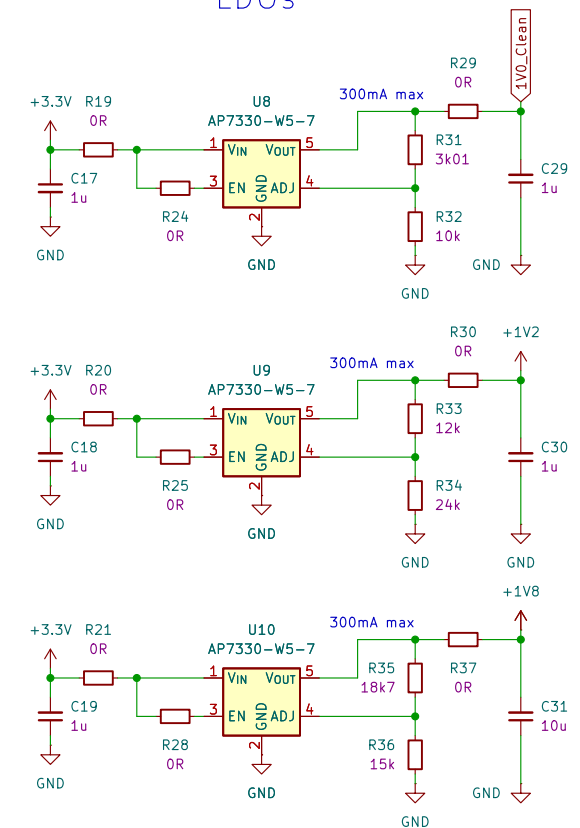
PS BLOCK DIAGRAM



CONVERTERS



LDOs



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Sheet: /Power Supply,
File: supply.kicad_sch

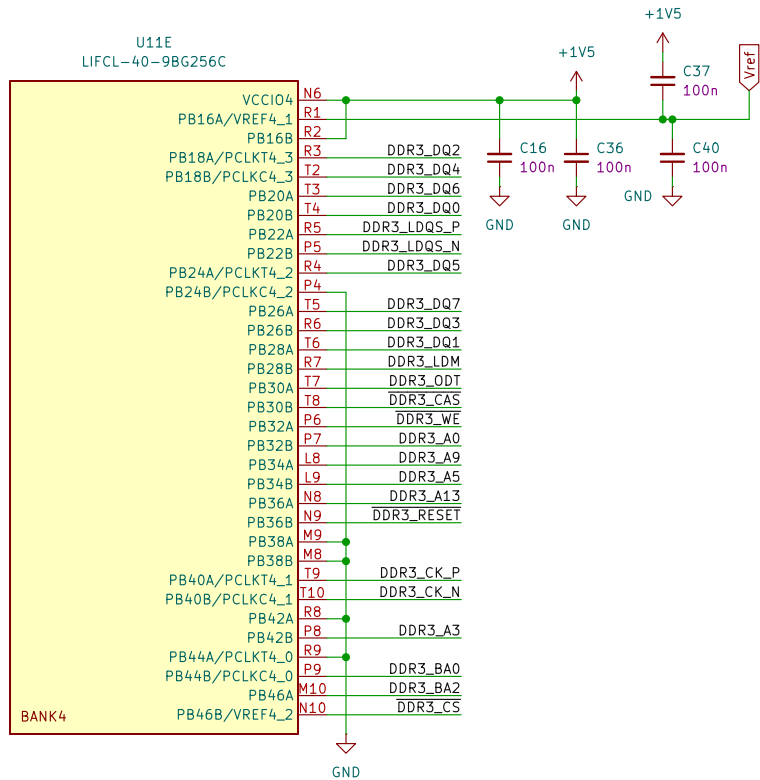
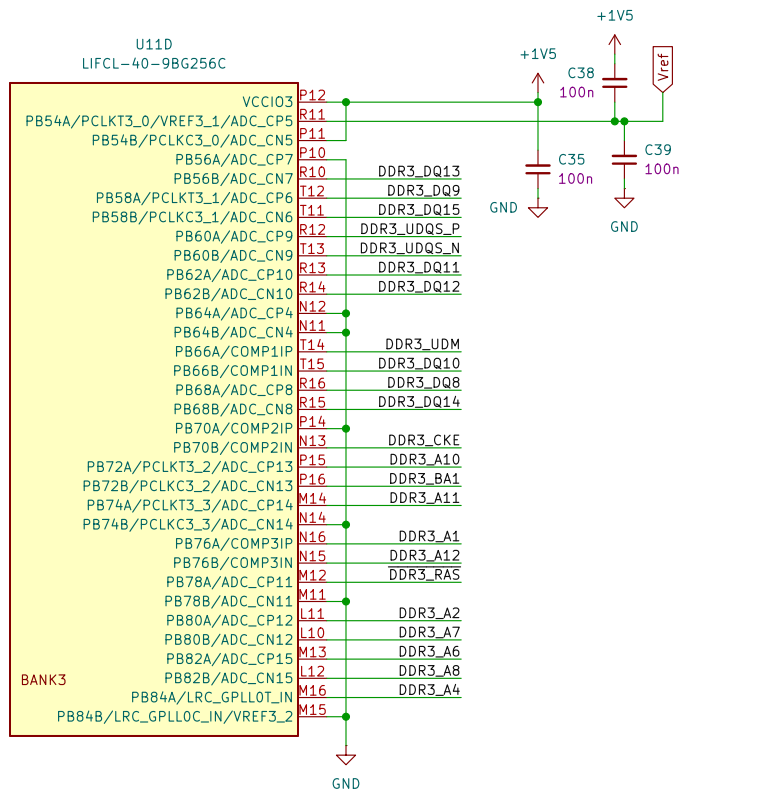
Title: SDI-MIPI Video Converter

Size: A3	Date: 2023-10-12
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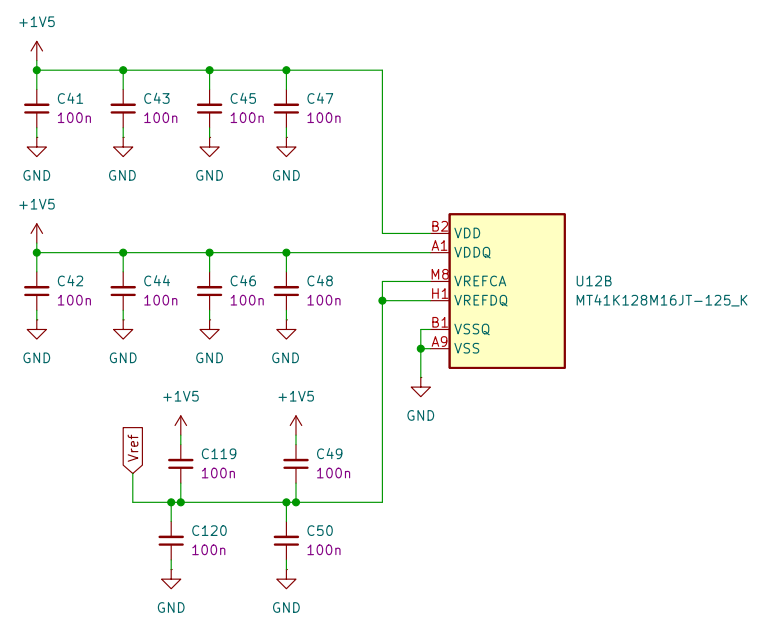
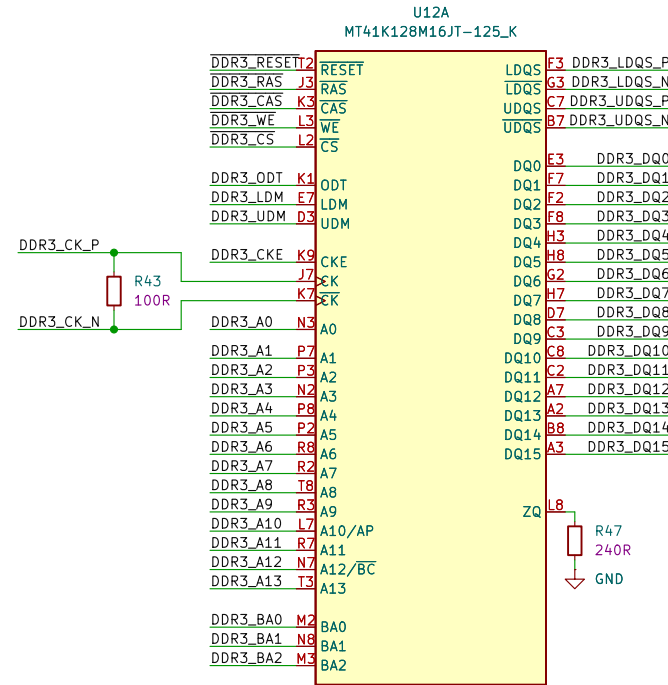
Rev: 1.1.4

Id: 2/7

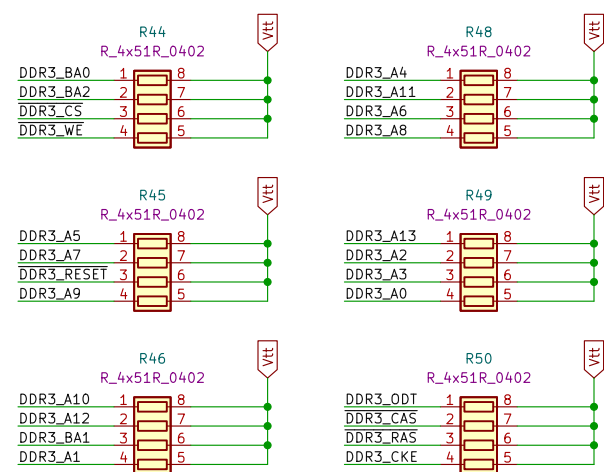
FPGA



RAM



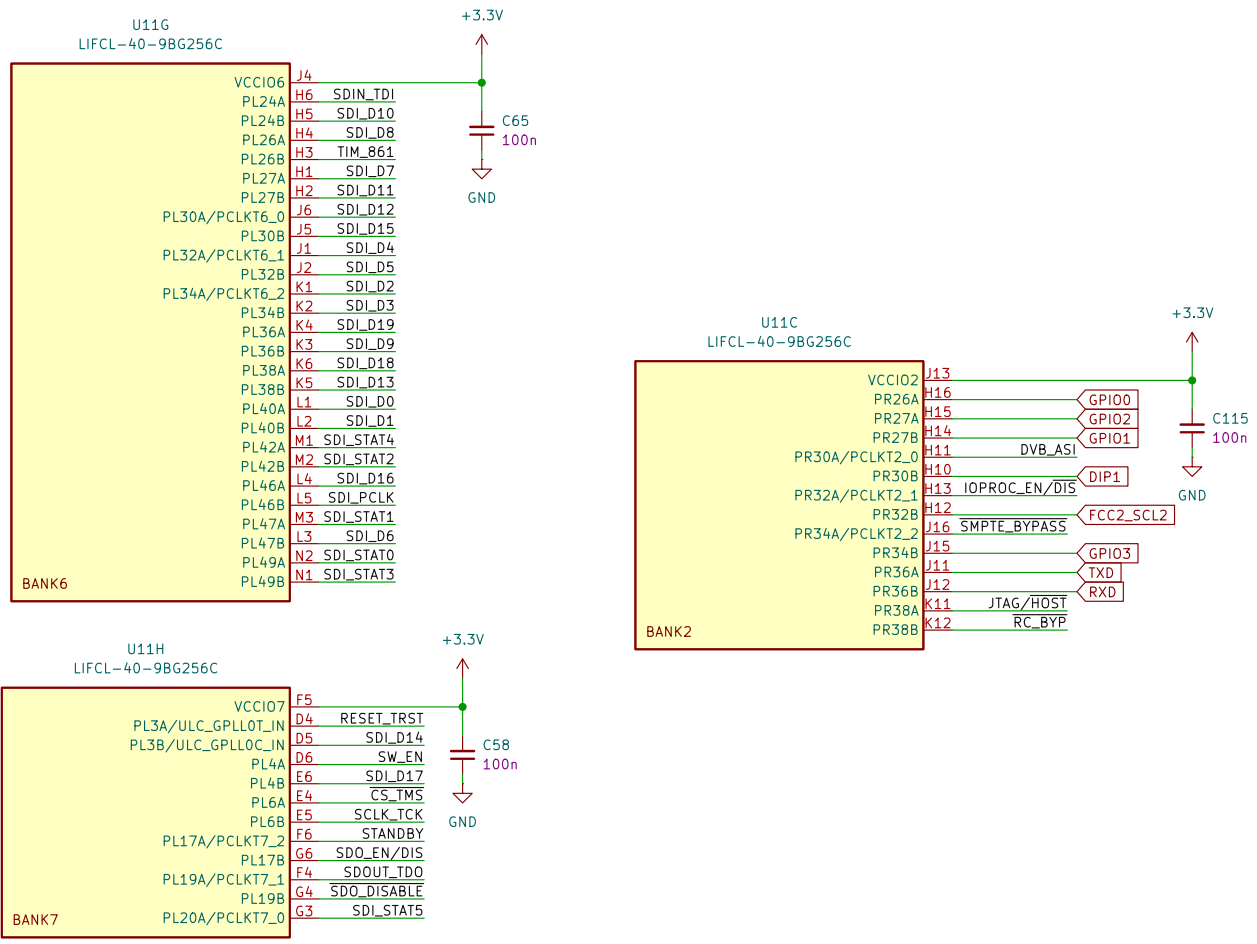
TERMINATION



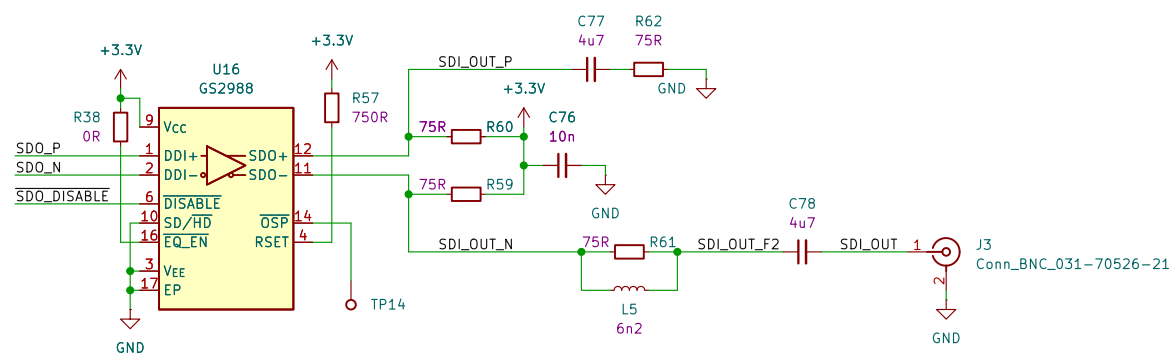
Signal terminations can be rearranged for easier routing



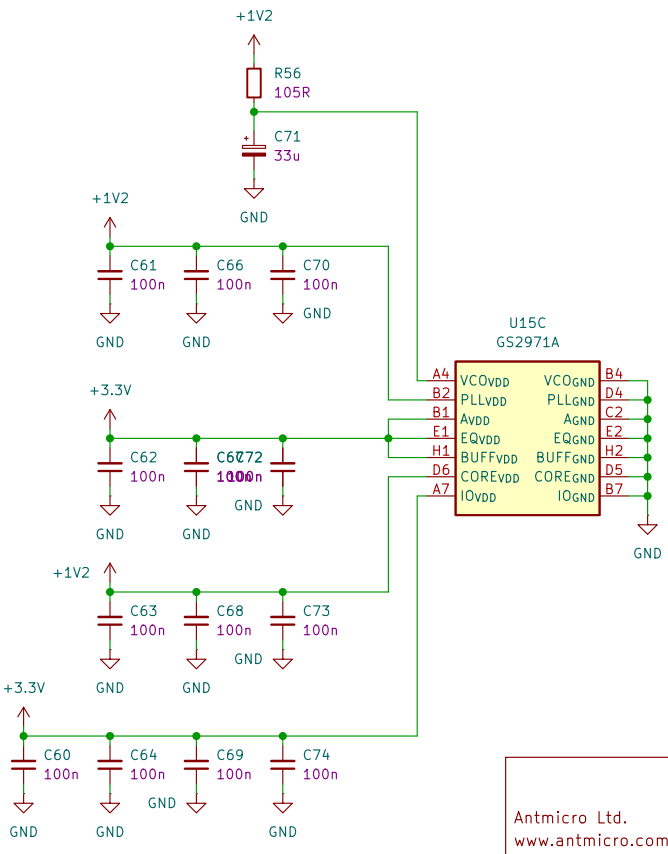
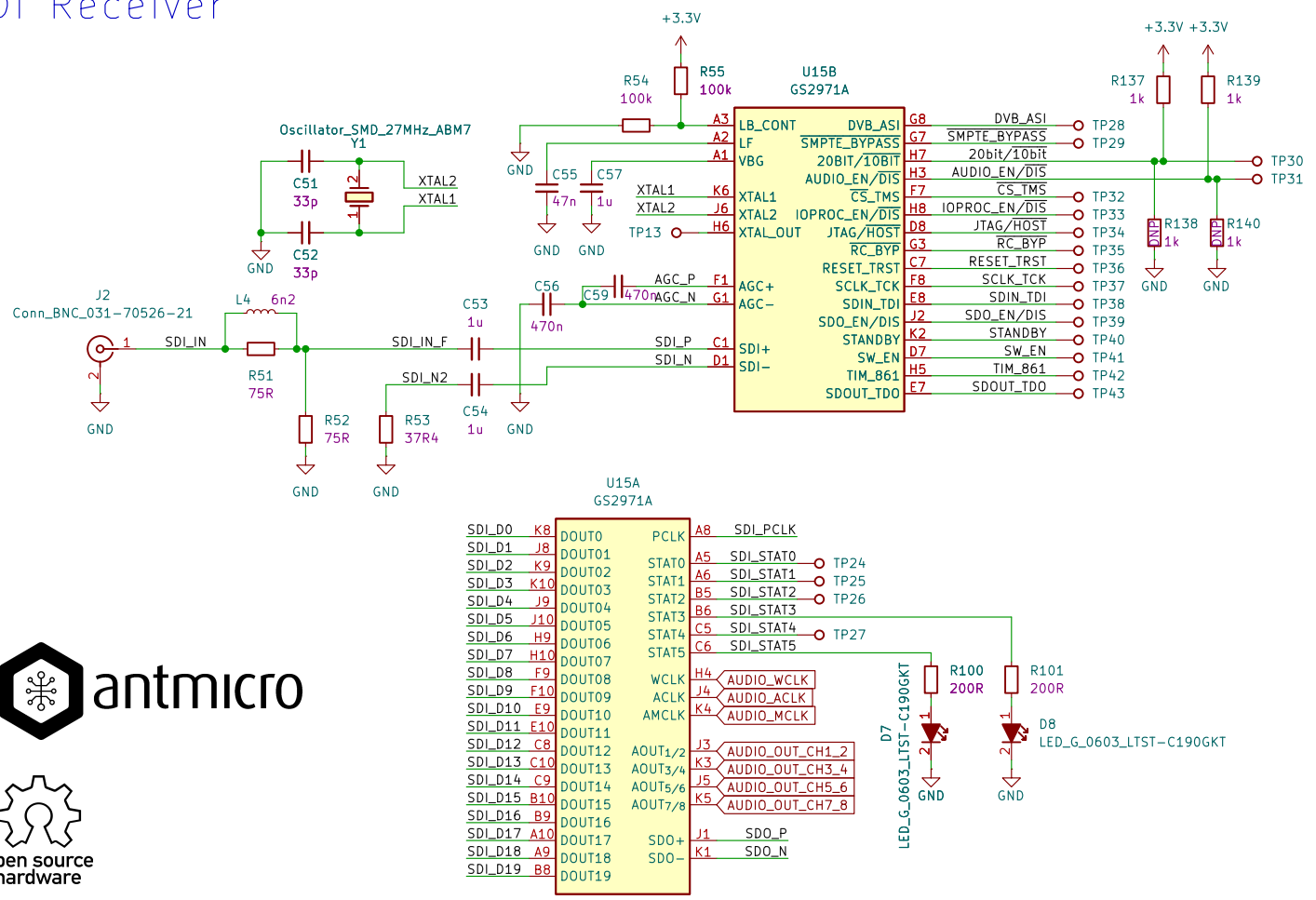
FPGA



Cable driver

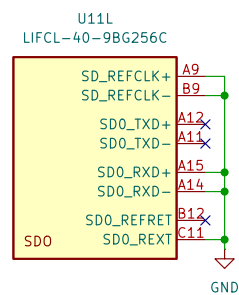
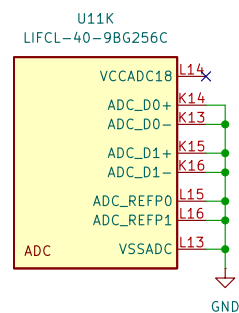
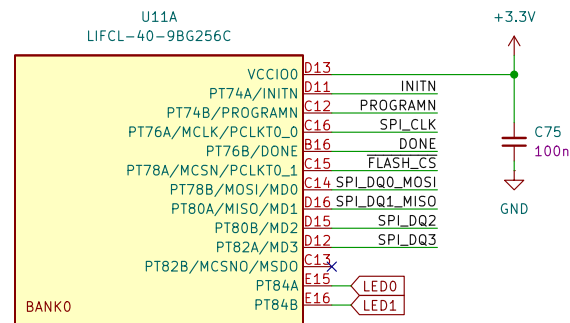


SDI Receiver

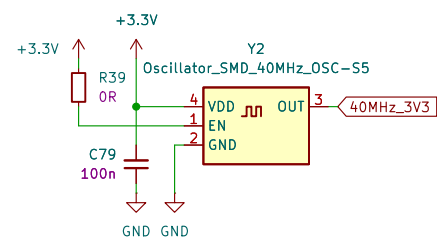


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Sheet: /SDI/ File: sdi.kicad_sch		
Title: SDI-MIPI Video Converter		
Size: A3	Date: 2023-10-12	Rev: 1.1.4
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FPGA

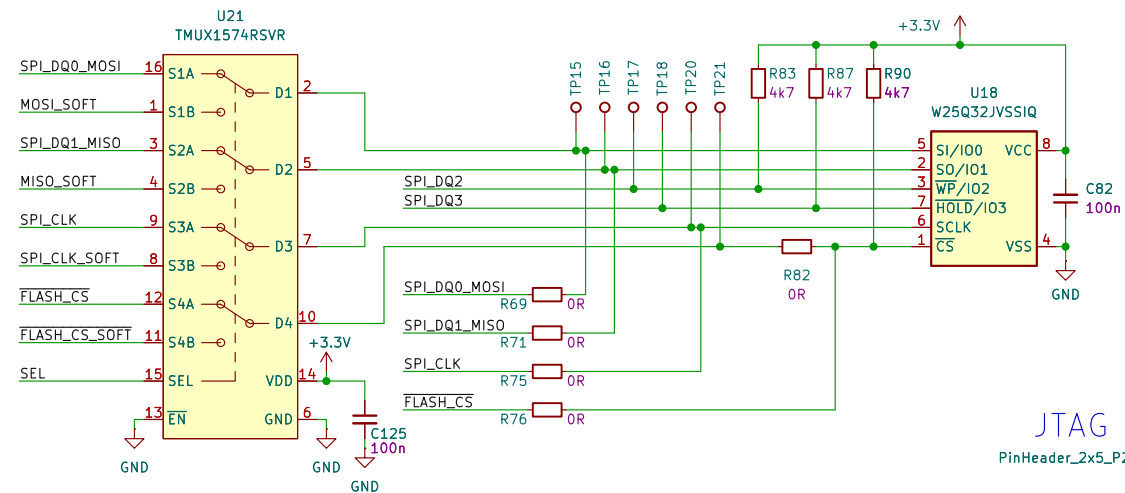


OSCILLATOR OPTIONAL, NORMALLY DNP



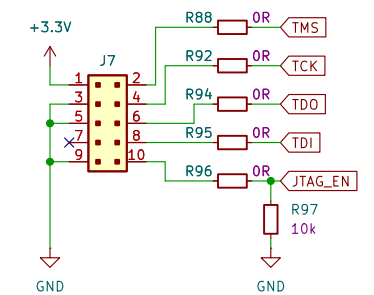
FPGA CONFIGURATION

CONFIGURATION FLASH

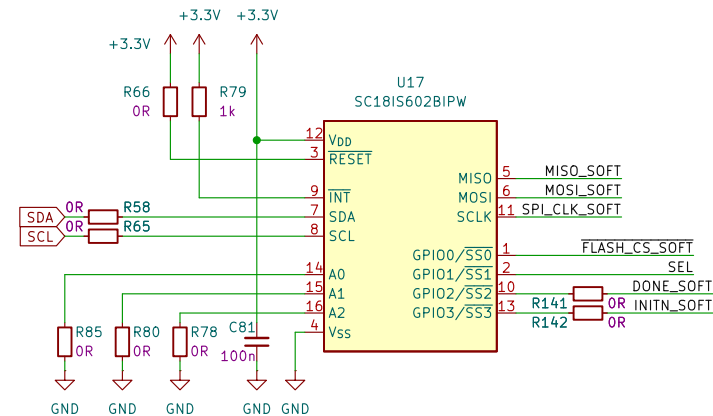


JTAG CONNECTOR

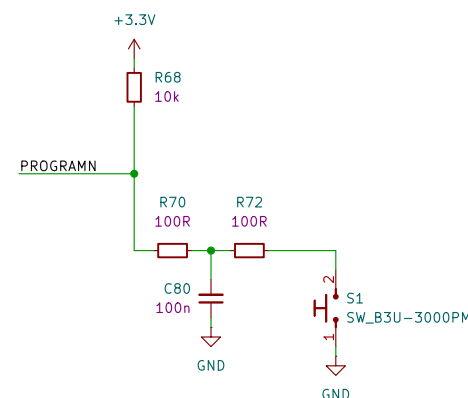
PinHeader_2x5_P2mm_Drill0.71mm_Shrouded



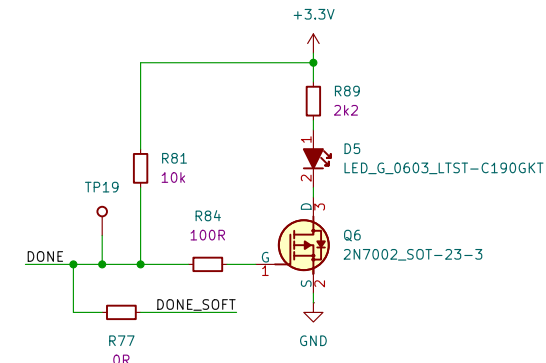
I2C TO SPI BRIDGE



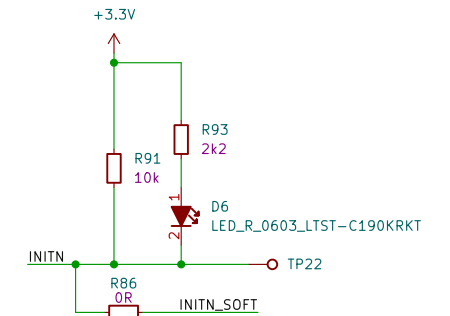
PROGRAMN



DONE



INITN



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Sheet: /FPGA/
File: fpga.kicad_sch

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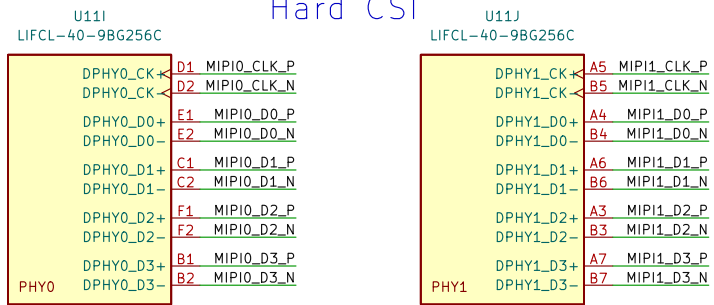
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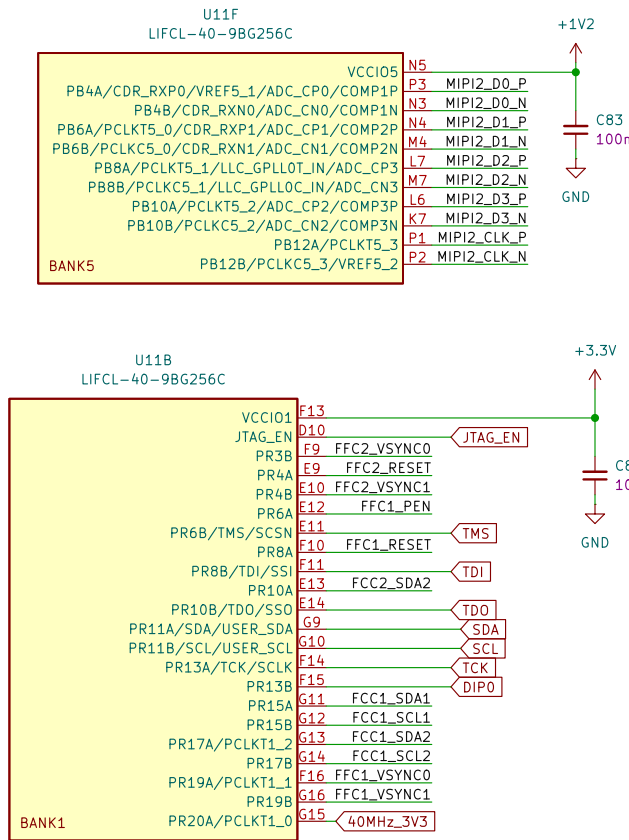
Rev: 1.1.4

Id: 6/7

Hard CSI

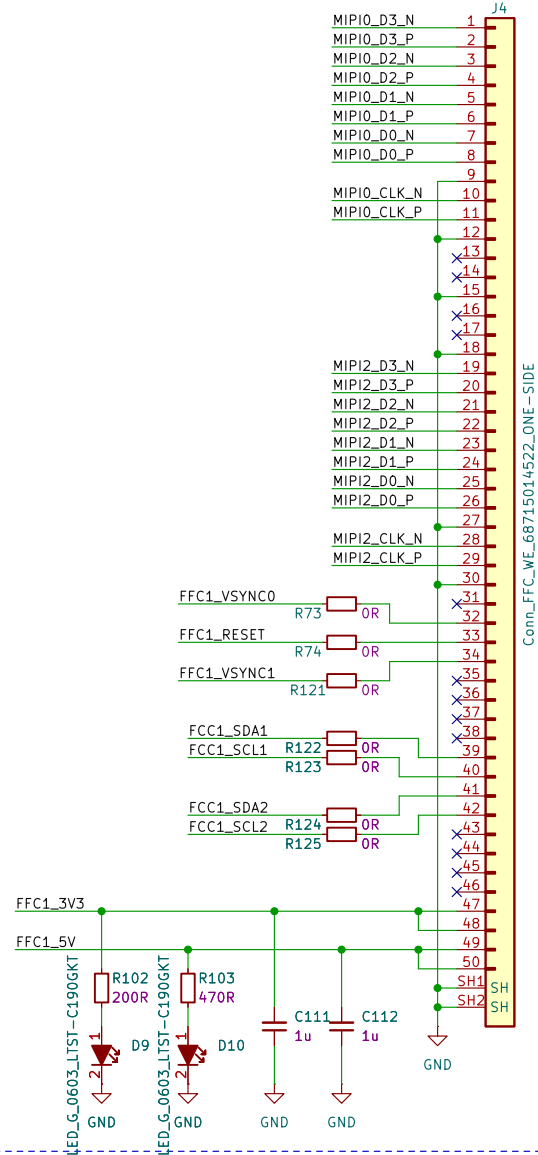


Soft CSI

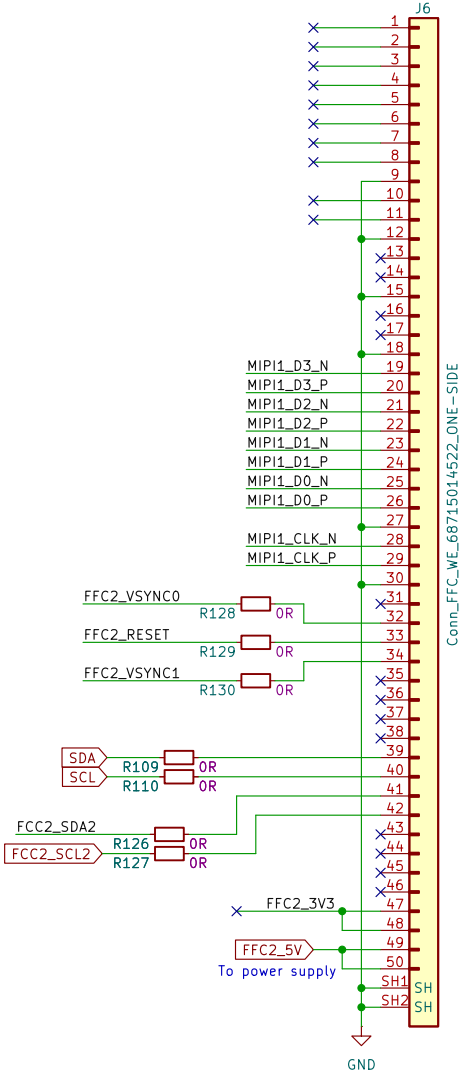


CSI Connectors

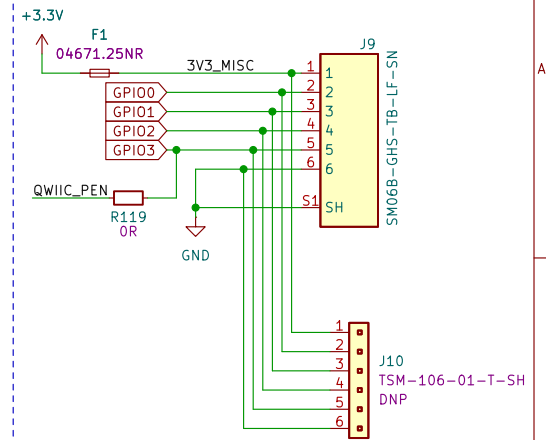
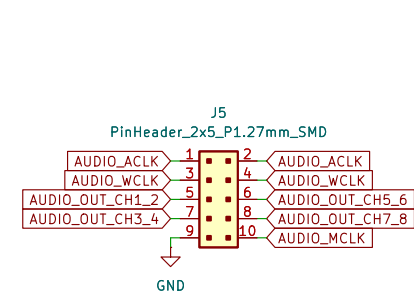
FFC1
POWER SOURCE



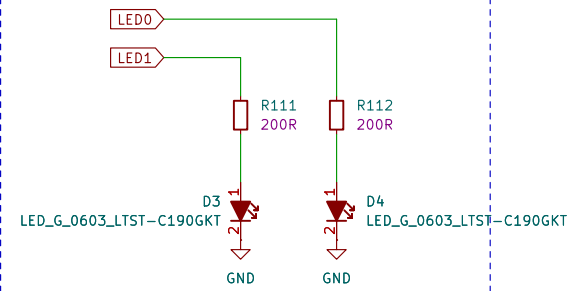
FFC2
POWER SINK



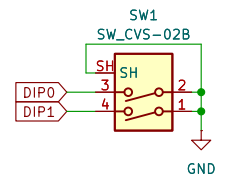
I2S CONNECTOR | GPIO CONNECTOR



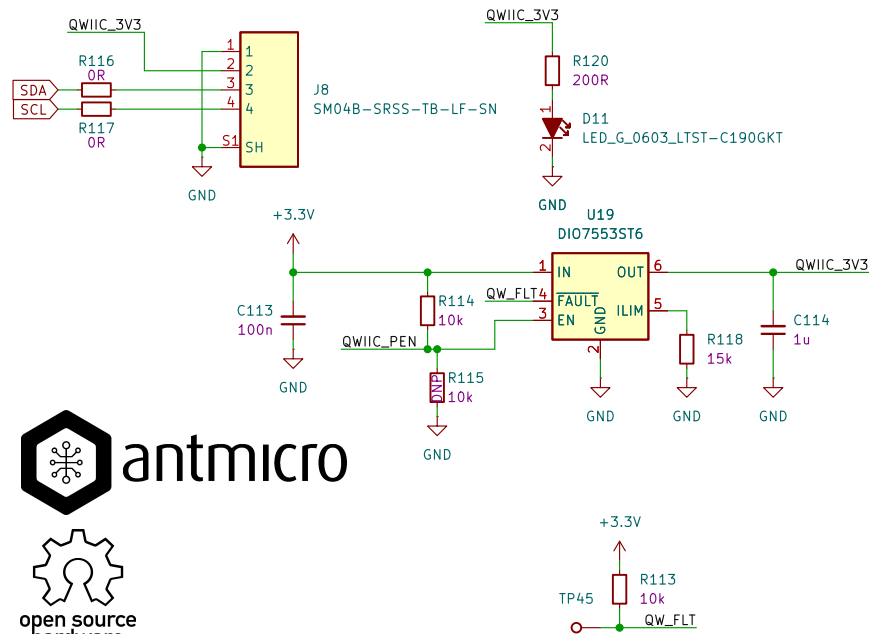
USER LEDs



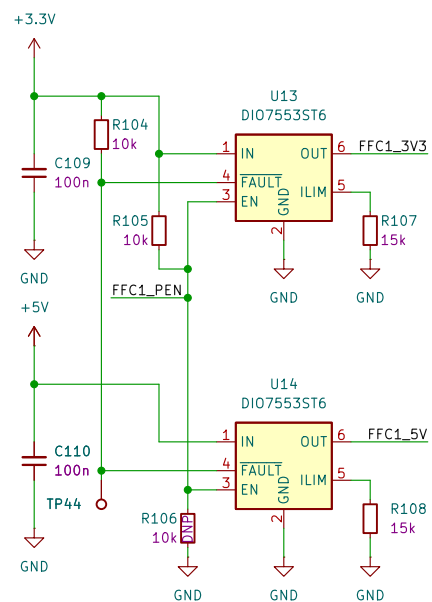
USER SWITCH



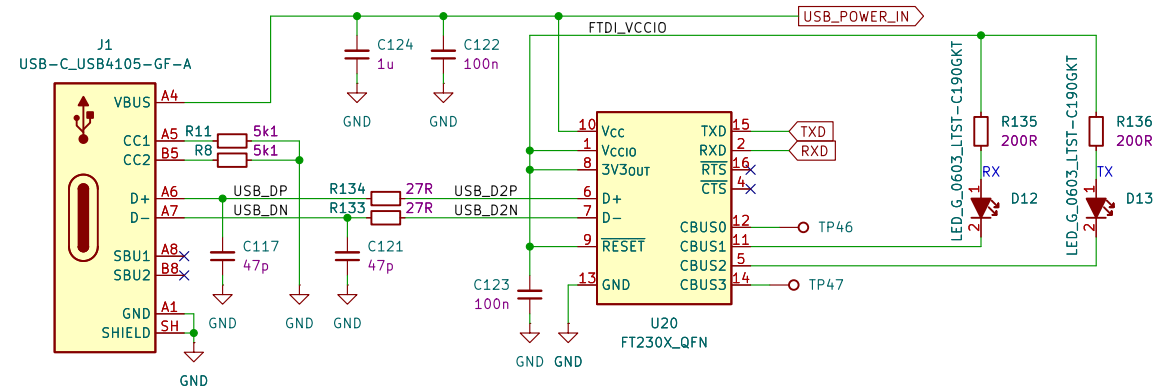
QWIIC CONNECTOR



FFC1 POWER SWITCH



USB-C CONNECTOR



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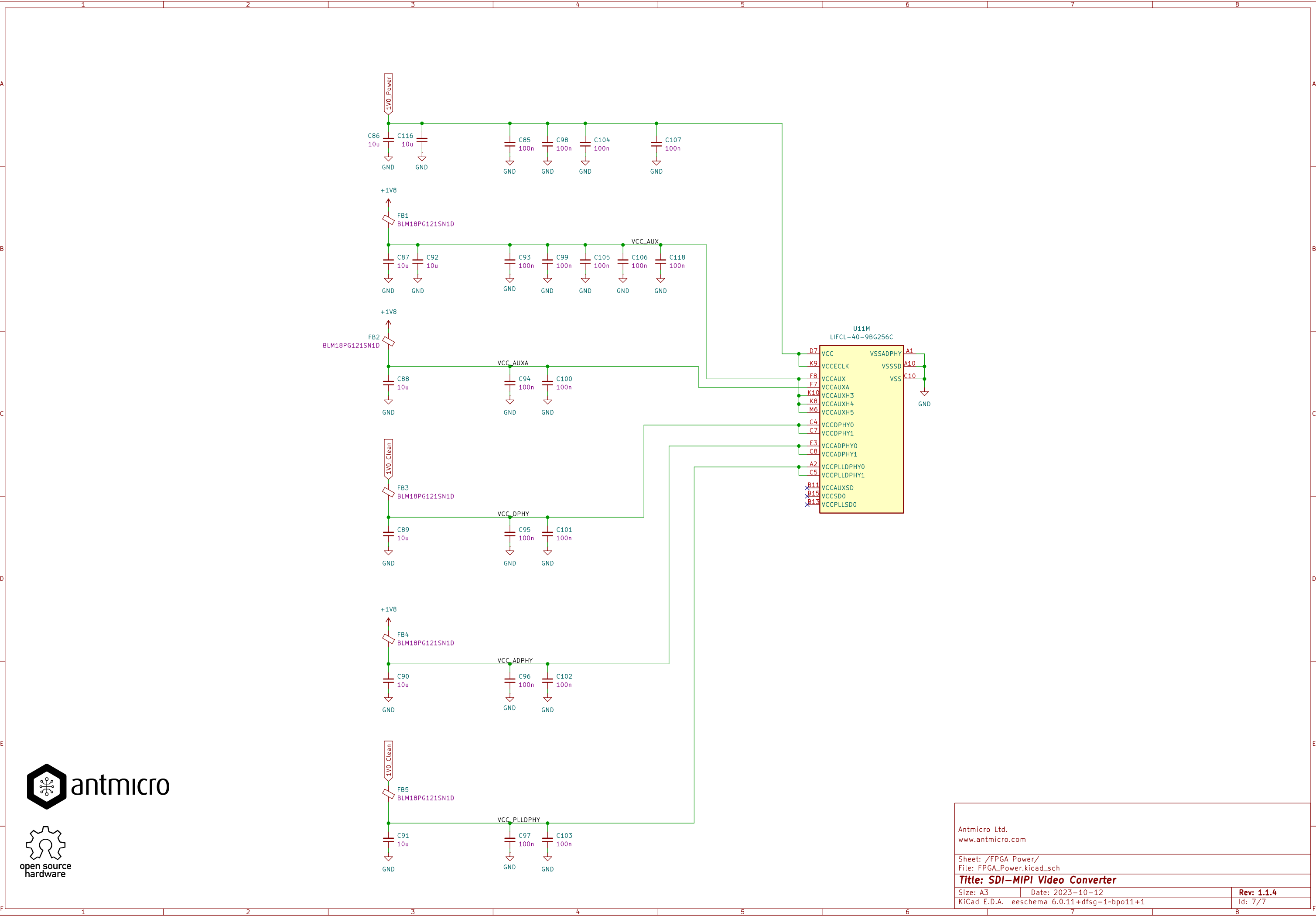
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