

File: sdi.kicad\_sch

File: fpga.kicad\_sch

FPGA Power File: FPGA\_Power.kicad\_sch File: supply.kicad\_sch

DDR3 File: ddr3.kicad\_sch Peripherals File: peripherals.kicad\_sch



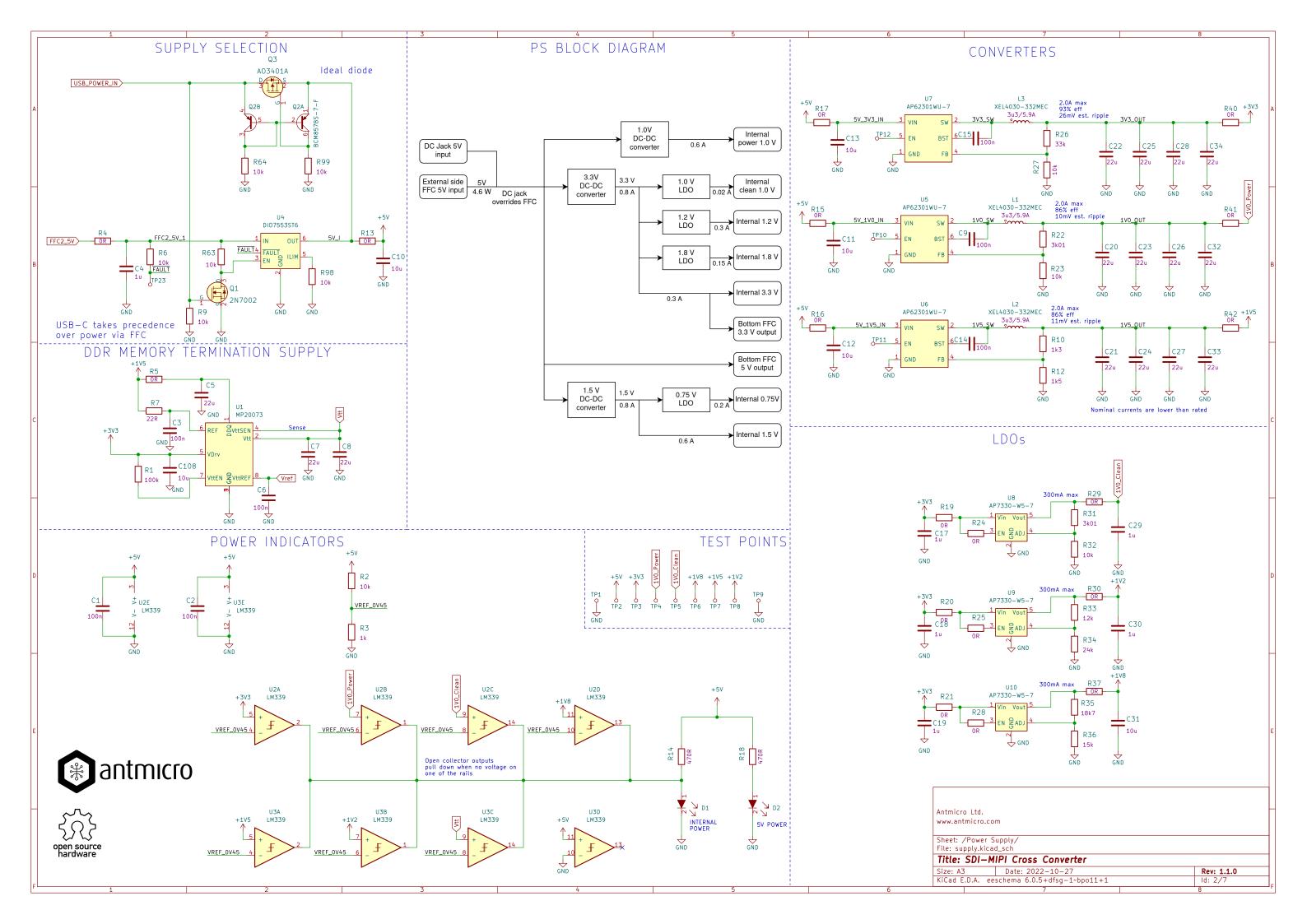


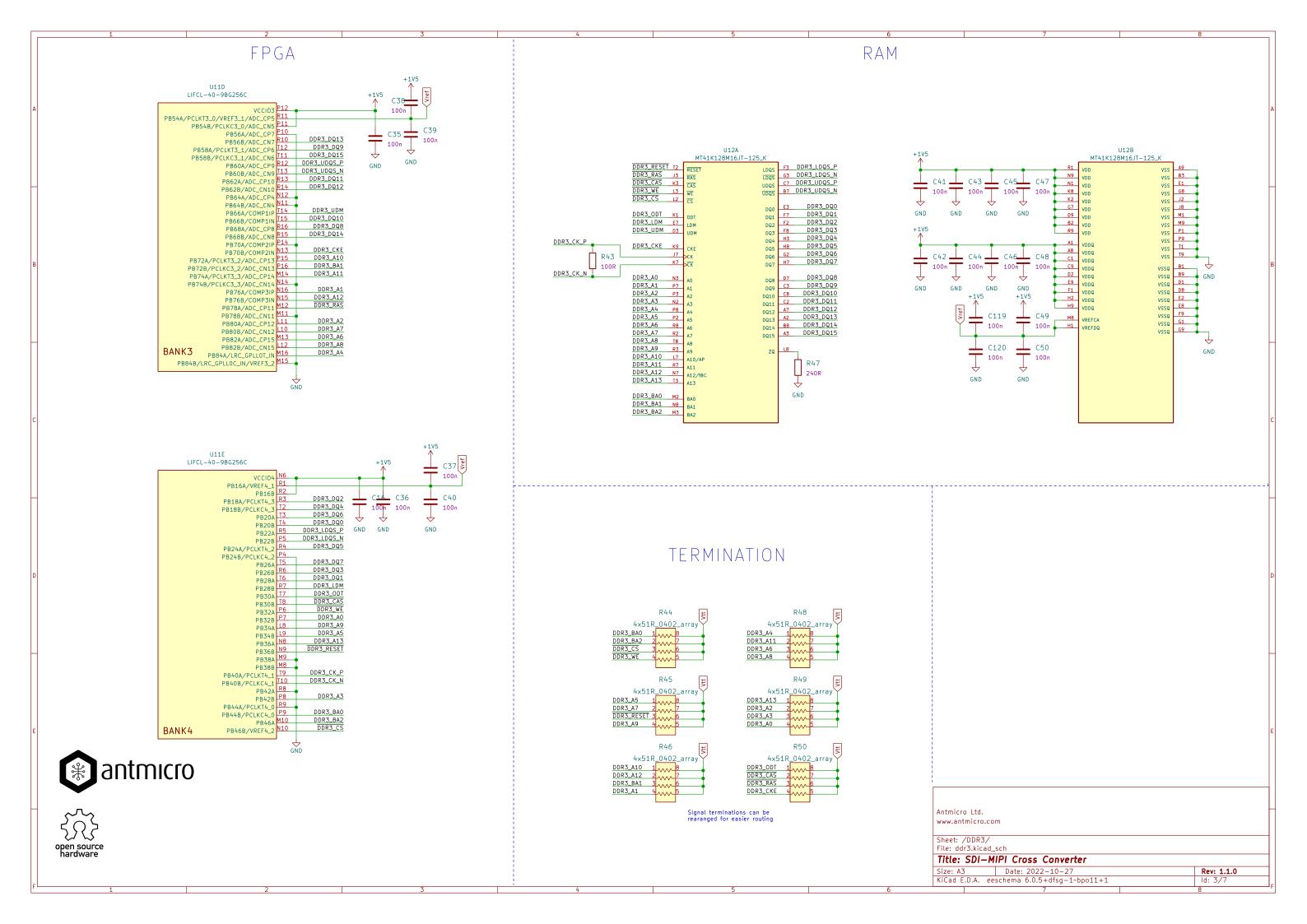
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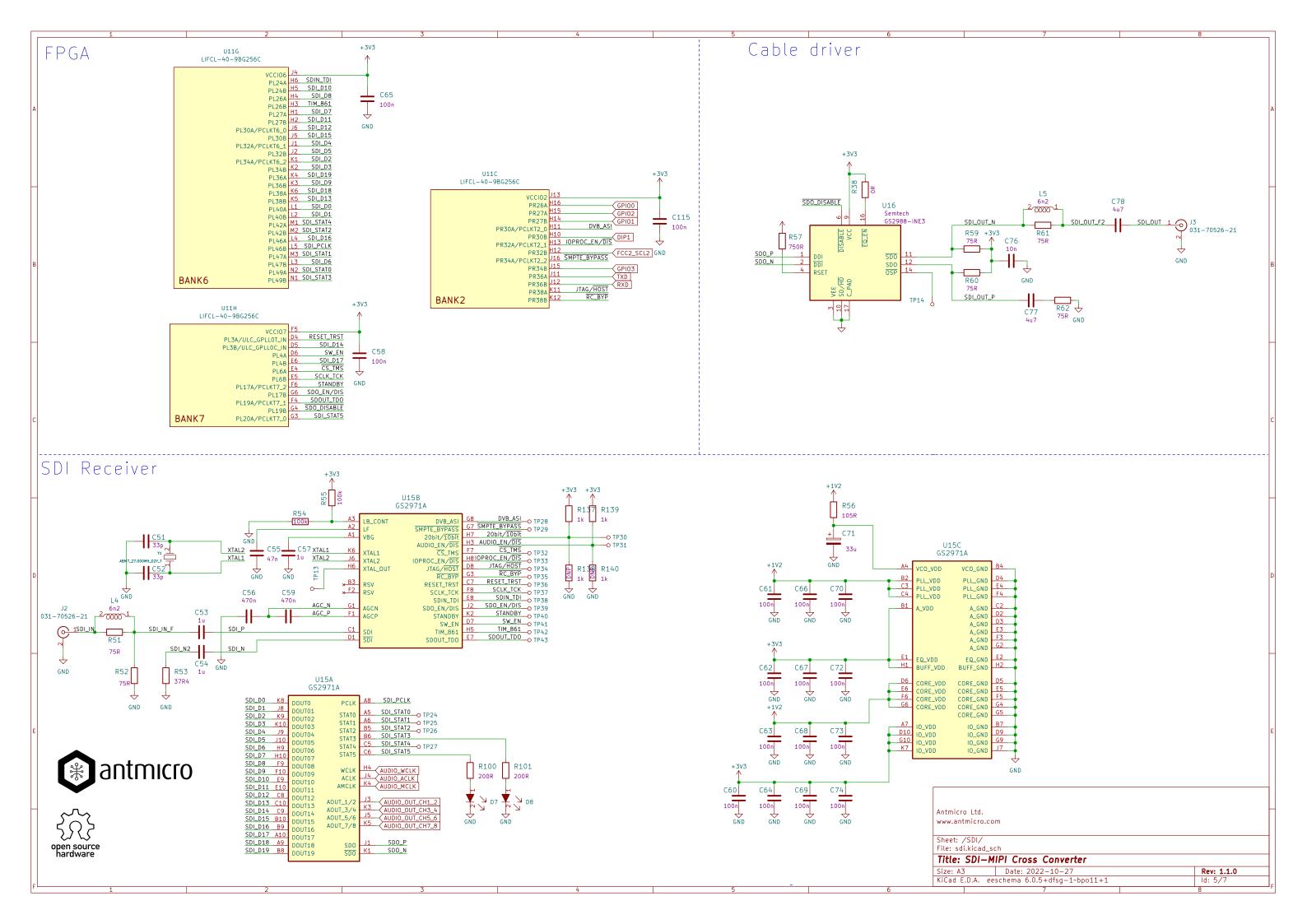
Sheet: /
File: sdi-mipi-cross-converter.kicad\_sch

Title: SDI-MIPI Cross Converter

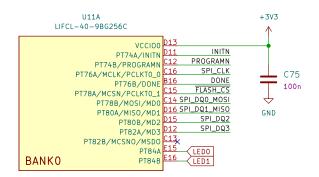
Size: A3 Date: 2022-10-27 KiCad E.D.A. eeschema 6.0.5+dfsg-1~bpo11+1

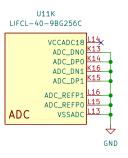


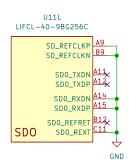




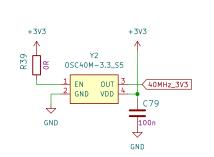
## FPGA



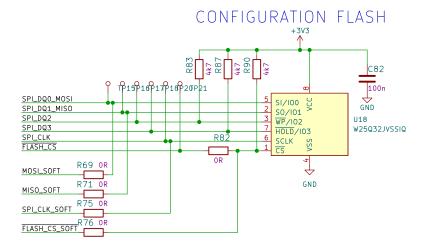




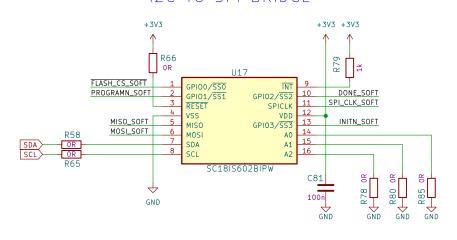
# OPTIONAL, NORMALLY DNP



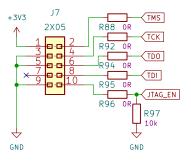
## FPGA CONFIGURATION



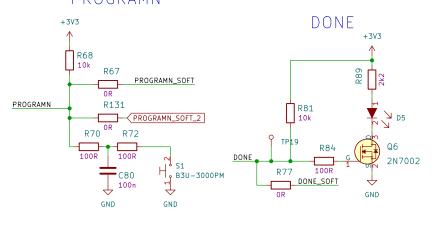
### 12C TO SPI BRIDGE

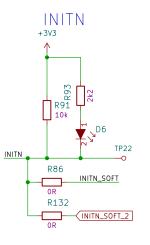


#### JTAG CONNECTOR



#### PROGRAMN





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Sheet: /FPGA/	
File: fpga.kicad_sch	
Title: SDI-MIPI Cross Converter	
Size: A3 Date: 2022-10-27	Rev: 1.1.0
KiCad E.D.A. eeschema 6.0.5+dfsg-1~bpo11+1	ld: 6/7





