

SA800U-WF

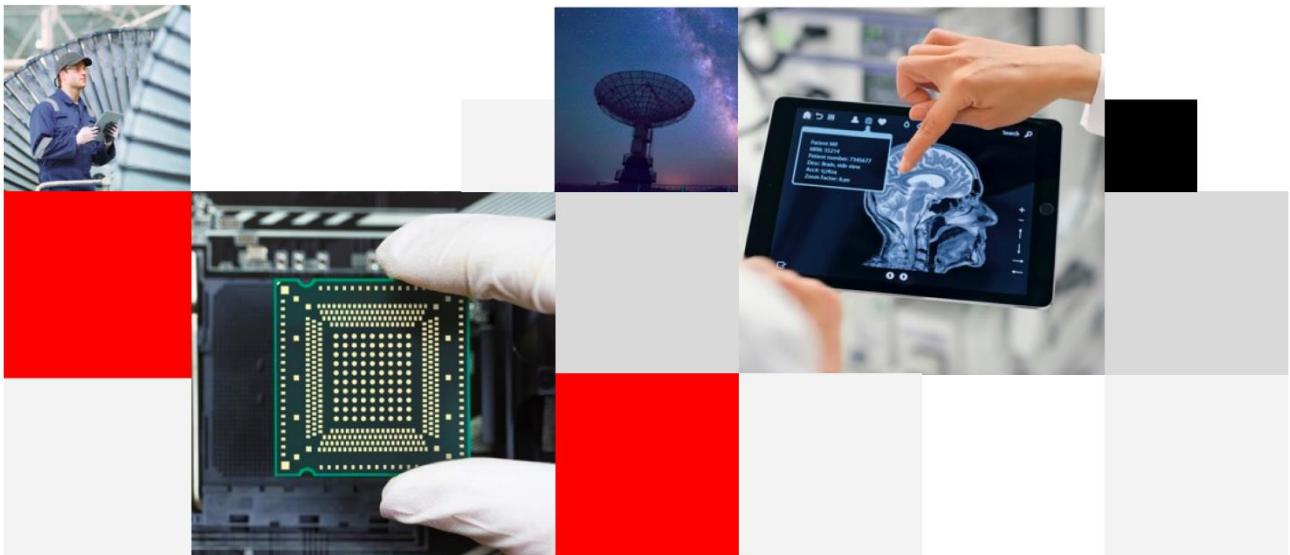
Reference Design

Smart Module Series

Version: 1.0

Date: 2021-03-31

Status: Released



Build a Smarter World

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Quectel Wireless Solutions Co., Ltd.

Building 5, Shanghai Business Park Phase III (Area B), No.1016 Tianlin Road, Minhang District, Shanghai 200233, China

Tel: +86 21 5108 6236

Email: info@quectel.com

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About the Document

Revision History

Version	Date	Author	Description
-	2021-03-26	Light WANG	Creation of the document
1.0	2021-03-31	Light WANG	First official release

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1 Reference Design

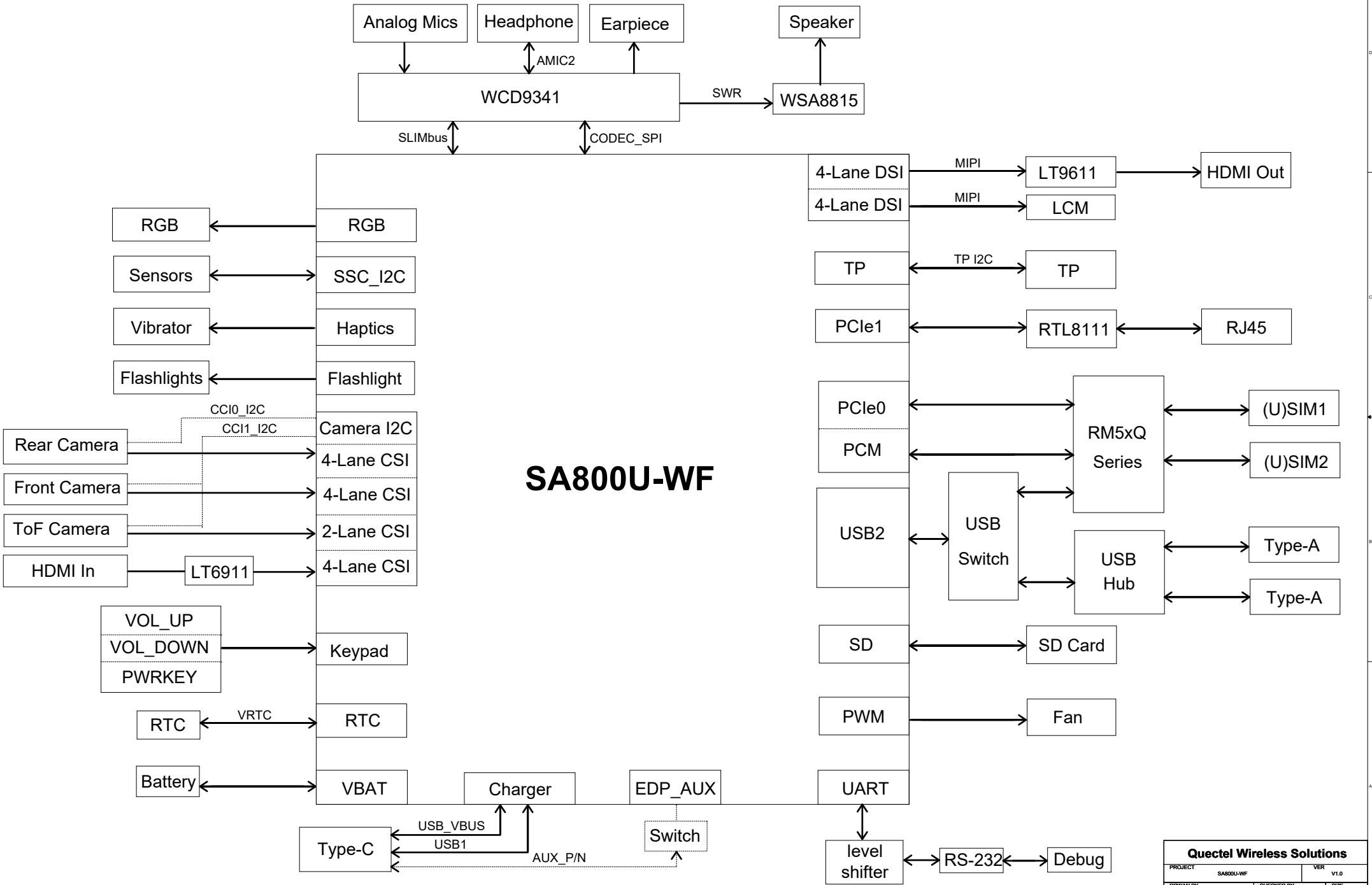
1.1. Introduction

This document provides a reference design for Quectel SA800U-WF module, including the design of power supply, audio, camera, HDMI In, HDMI Out, LCM, TP, Ethernet, USB, sensor interfaces, etc., as well as the interface design for the use of SA800U-WF in conjunction with Quectel 5G module - RM50xQ series.

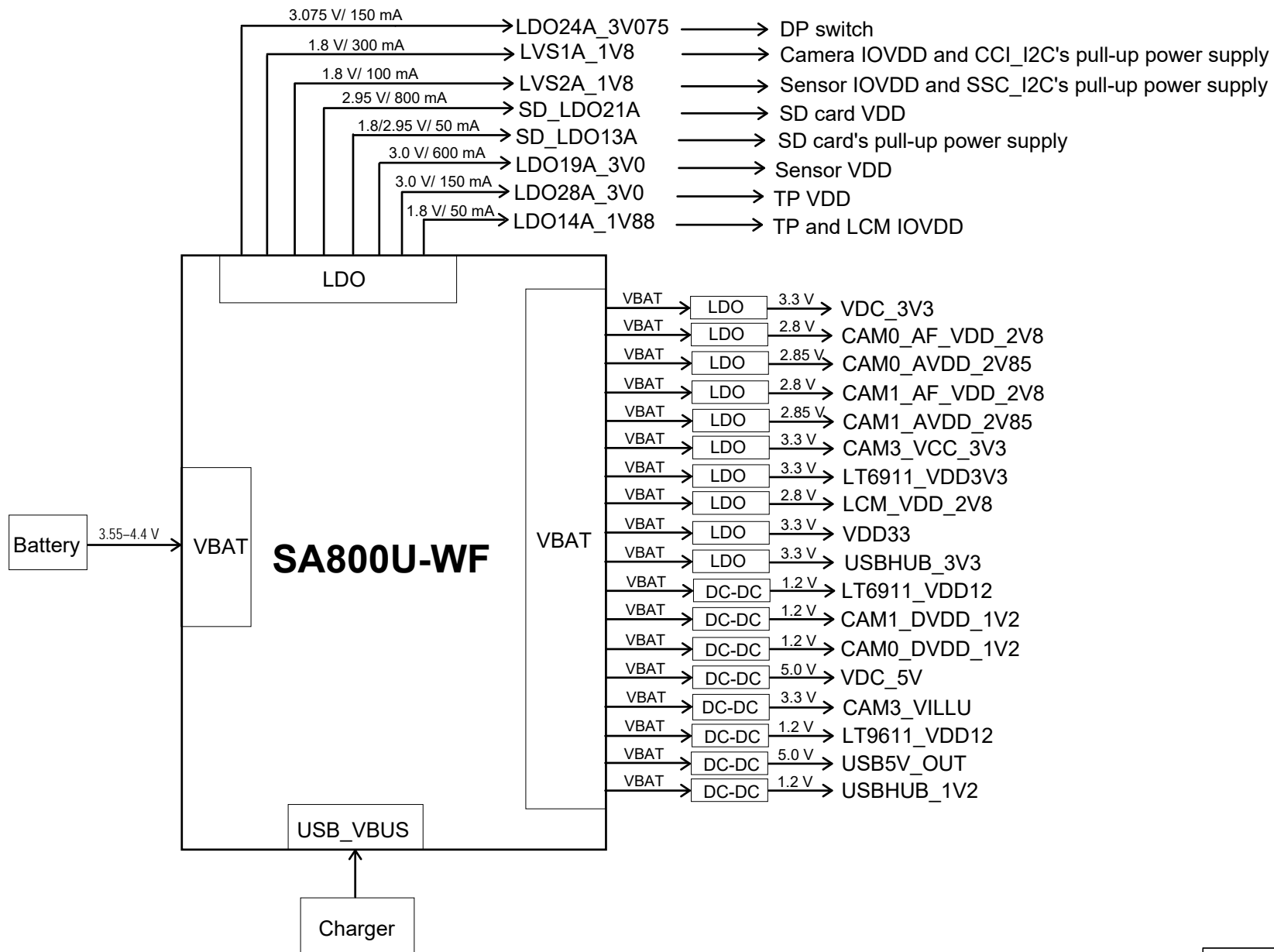
1.2. Schematics

The schematics illustrated in the following pages are provided for reference only.

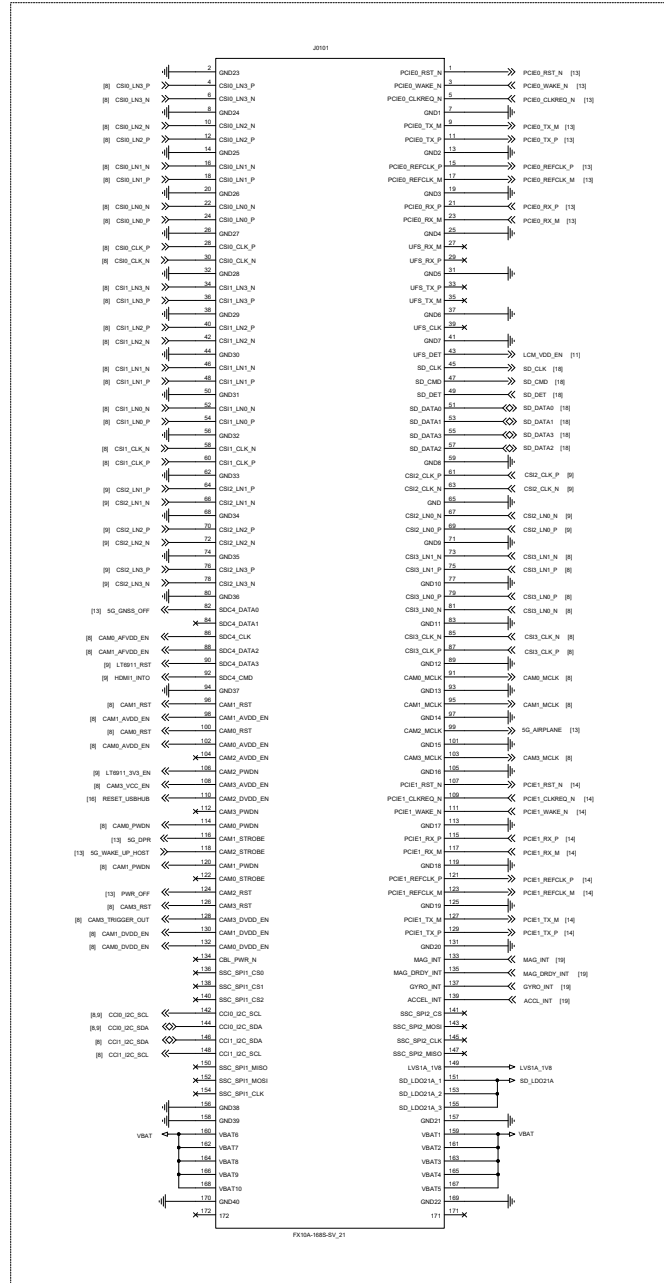
System Diagram



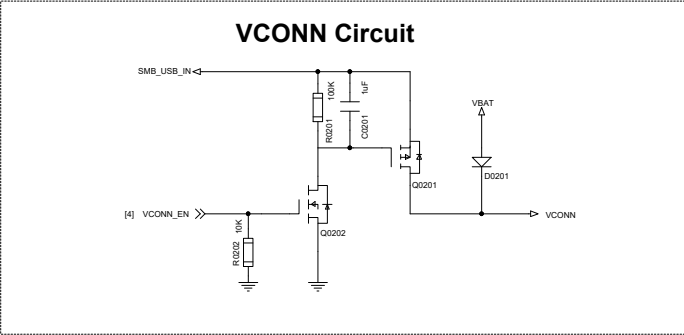
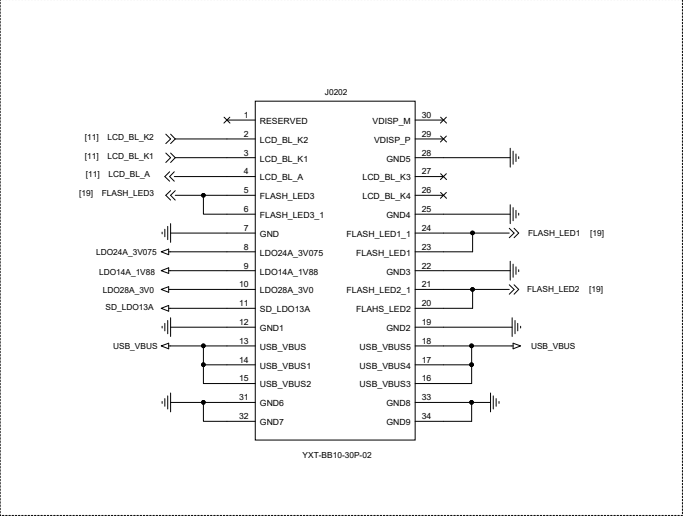
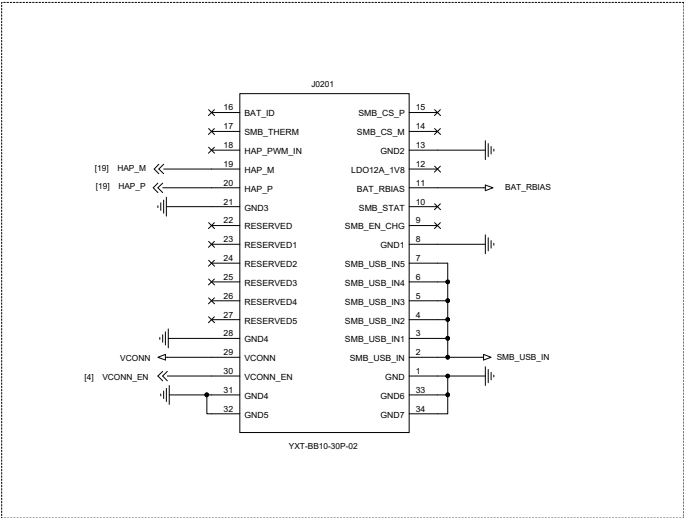
Power Diagram



Module Interfaces



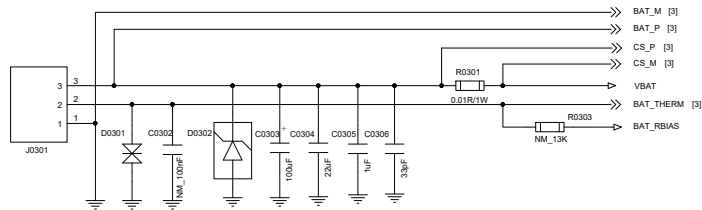
FPC Connectors



Note:
If functional pins in FPC connectors are not used, keep them unconnected.

Power Supply

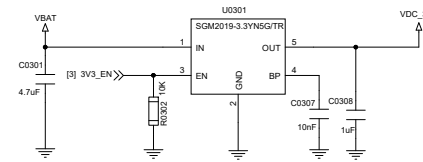
Battery Application



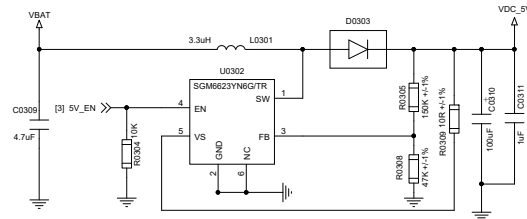
Notes:

1. If battery NTC = 47 k Ω , R0303 = NM.
2. If battery NTC = 10 k Ω , R0303 = 13 k Ω .

3.3 V Power Supply

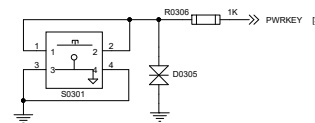


5.0 V Power Supply

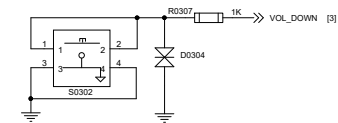


Keypad

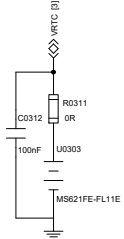
PWRKEY



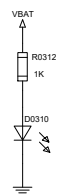
VOL_DOWN



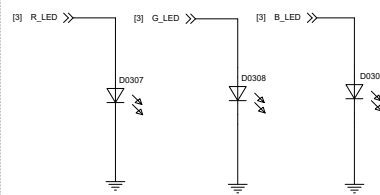
Backup Battery



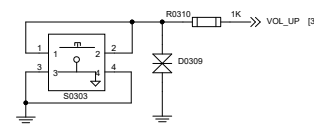
Indicator



RGB LED



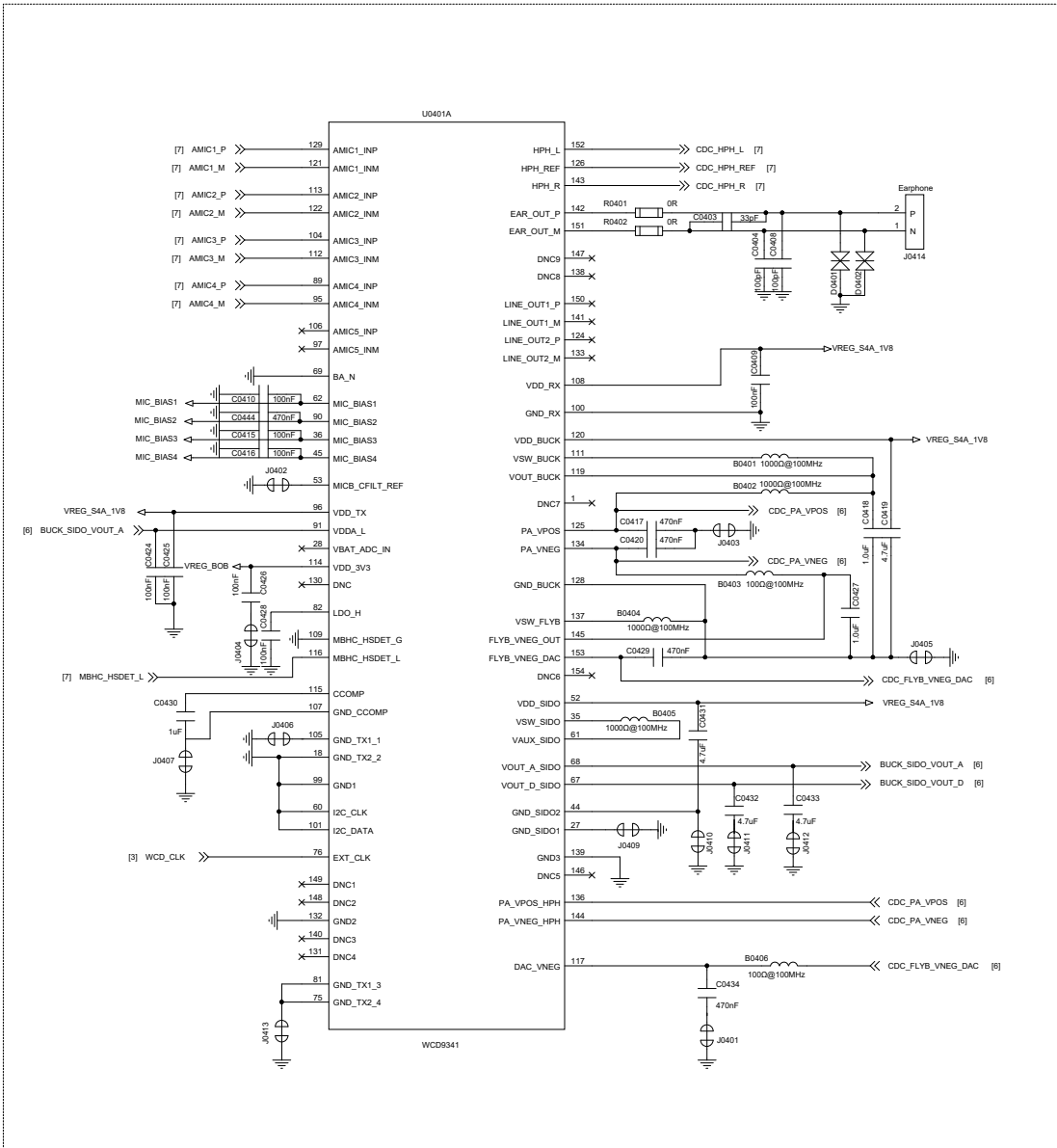
VOL_UP



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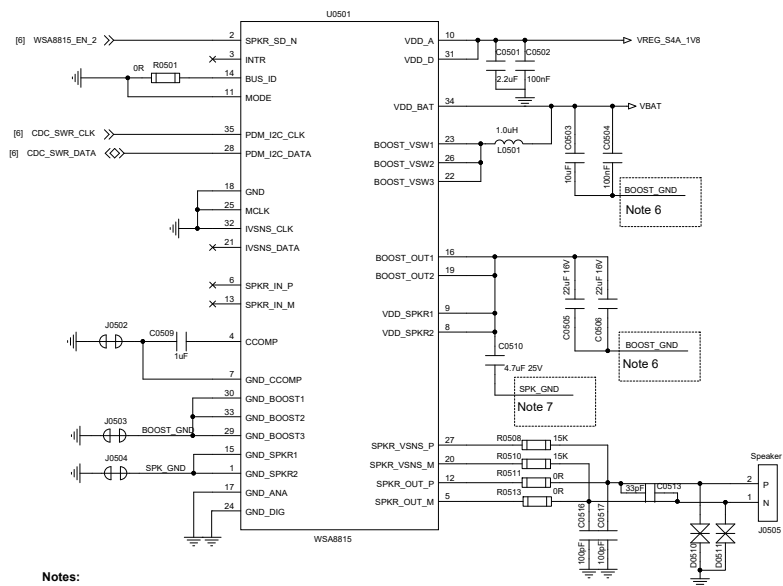
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Codec Interfaces



Audio Interfaces

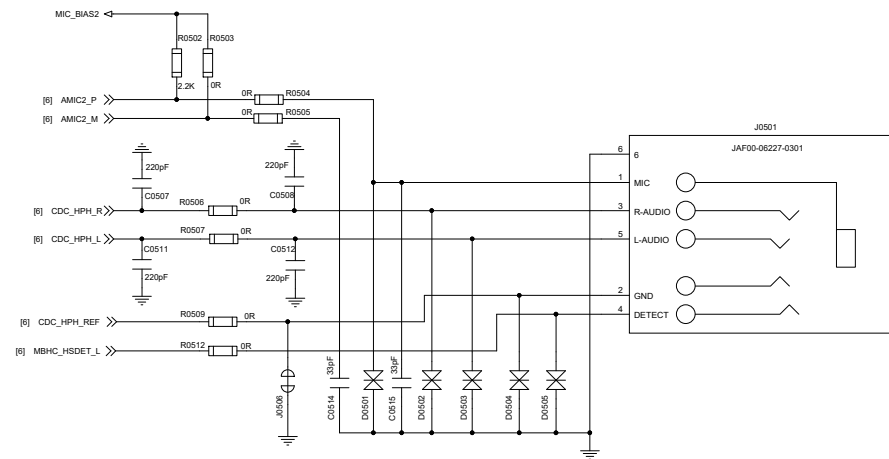
Audio PA



Notes:

1. Pin BUS_ID should be pulled high for one WSA8815 and low for another WSA8815.
2. Pin MODE should be pulled to GND for SoundWire mode and pulled to 1.8 V for analog mode.
3. Leave pin INTR unconnected for PDM mode.
4. J0502–J0504 should be connected directly to the main GND plane.
5. Pin SPKR_VSNS_P and pin SPKR_VSNS_M should be placed close to the speaker transducer for optimum feedback and speaker protection.
6. C0503–C0506 should be connected to pin 29, 30, 33 of WSA8815, and then directly to the main GND plane.
7. C0510 should be connected to pin 1 and 15 of WSA8815, and then directly to the main GND plane.

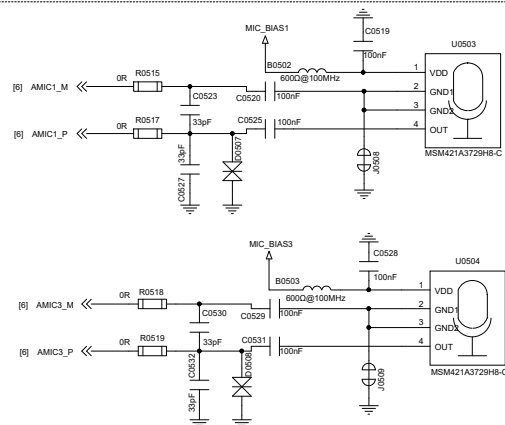
Headphone



Note:

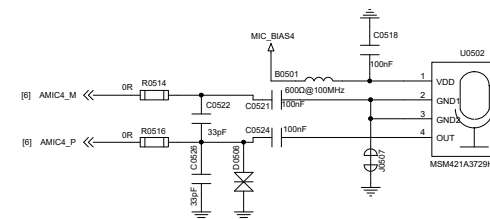
J0506 should be connected directly to the main GND plane.

Microphones



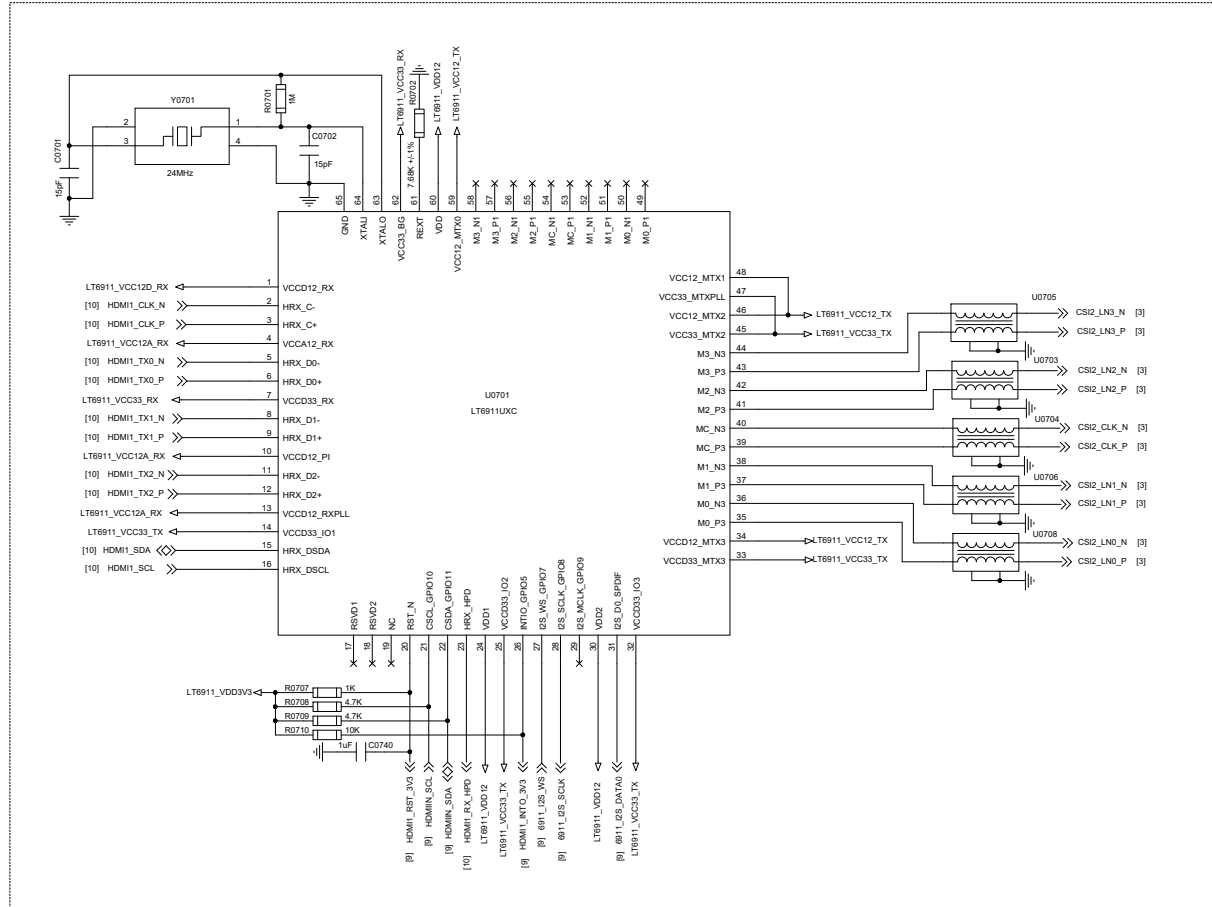
Note:

J0507–J0509 should be connected directly to the main GND plane.

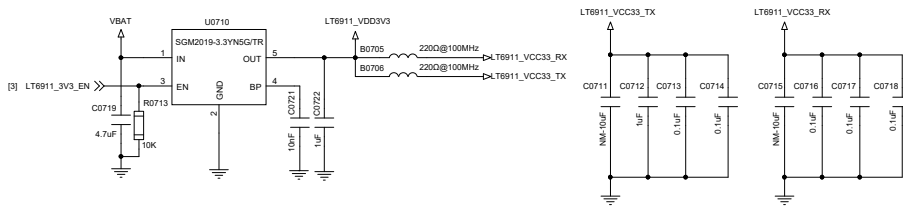


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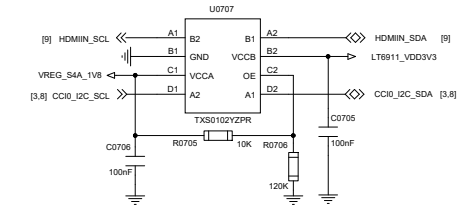
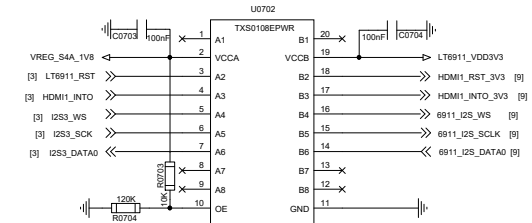
HDMI In Interface (Part 1)



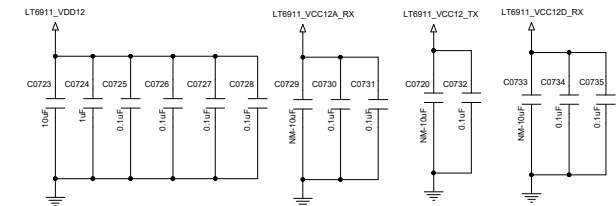
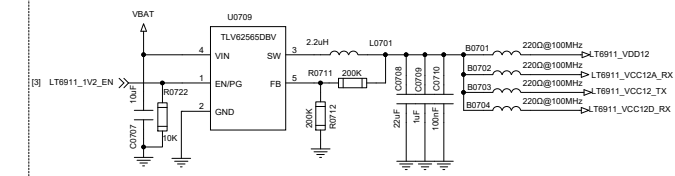
3.3 V Power Supply



Level Shift Circuit



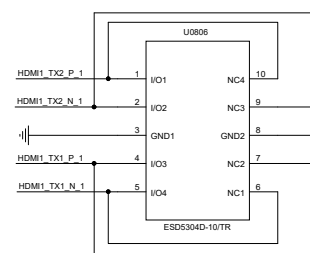
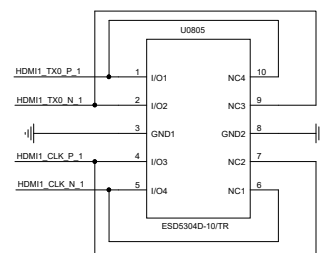
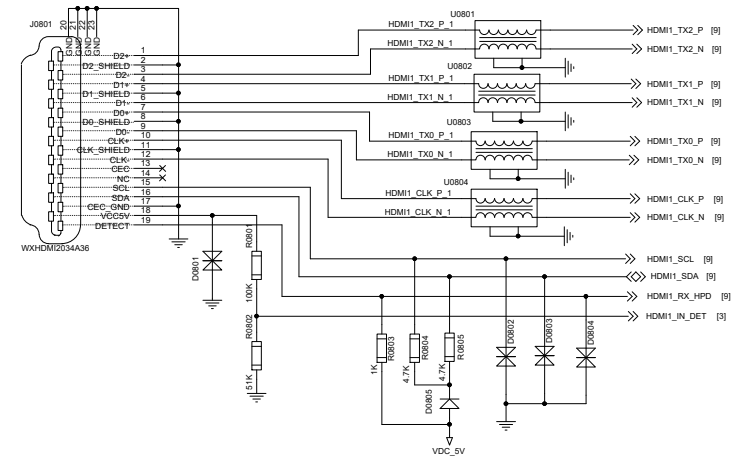
1.2 V Power Supply



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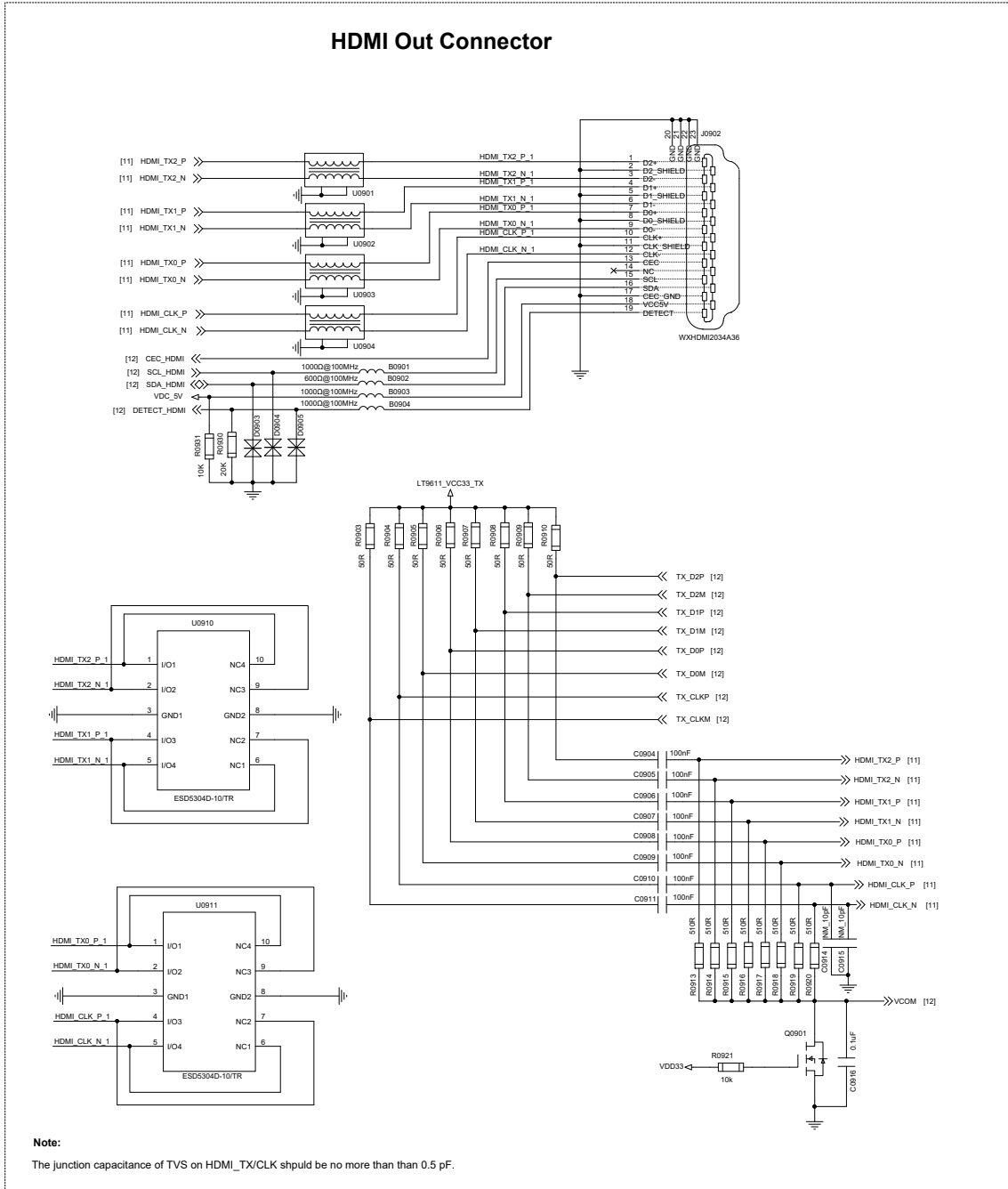
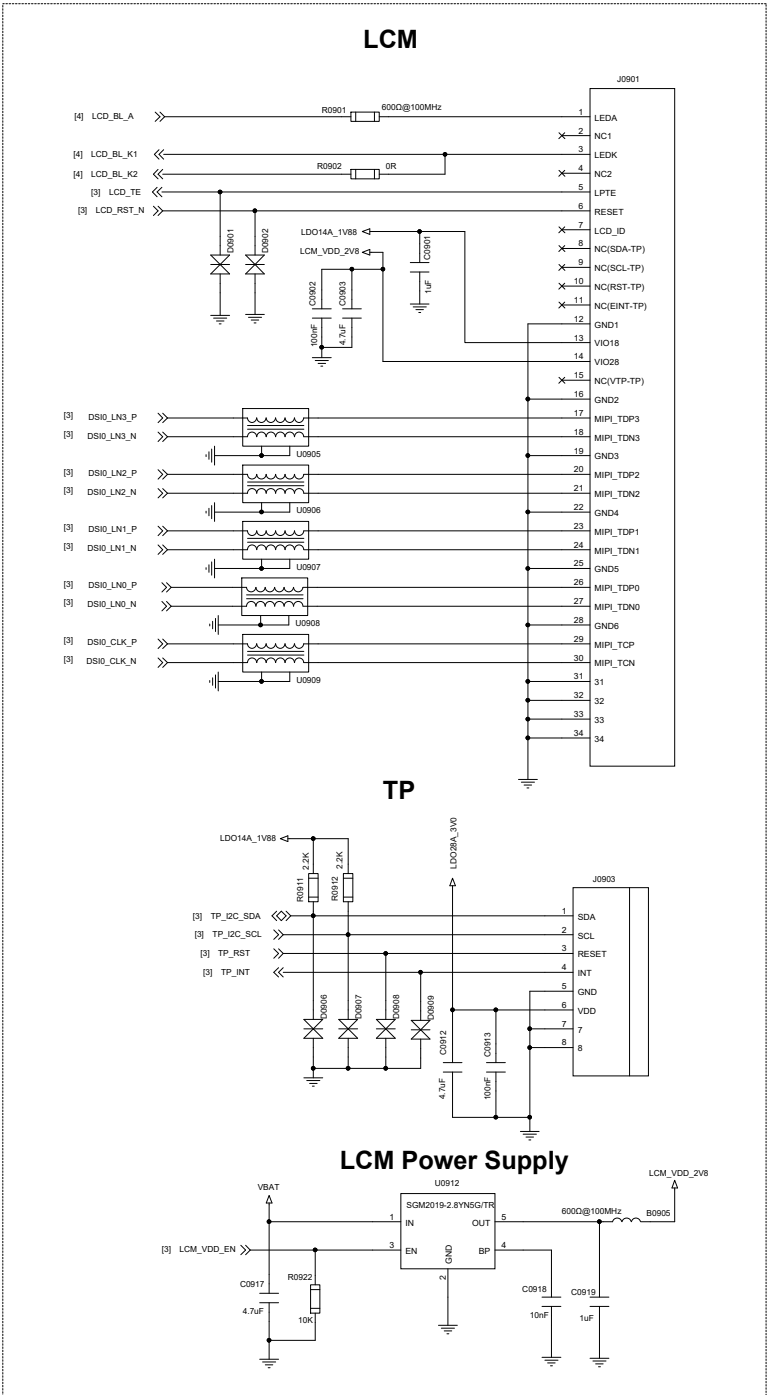
HDMI In Connector



Note:

The junction capacitance of TVS on HDMI_TX/CLK should be no more than 0.5 pF.

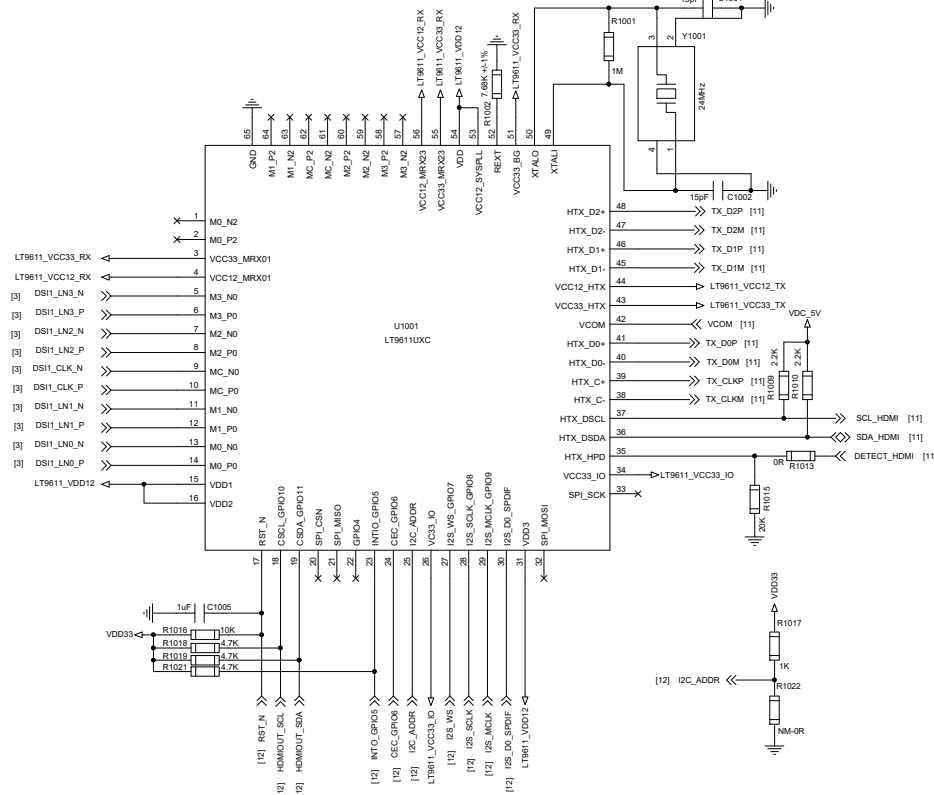
Display Interfaces



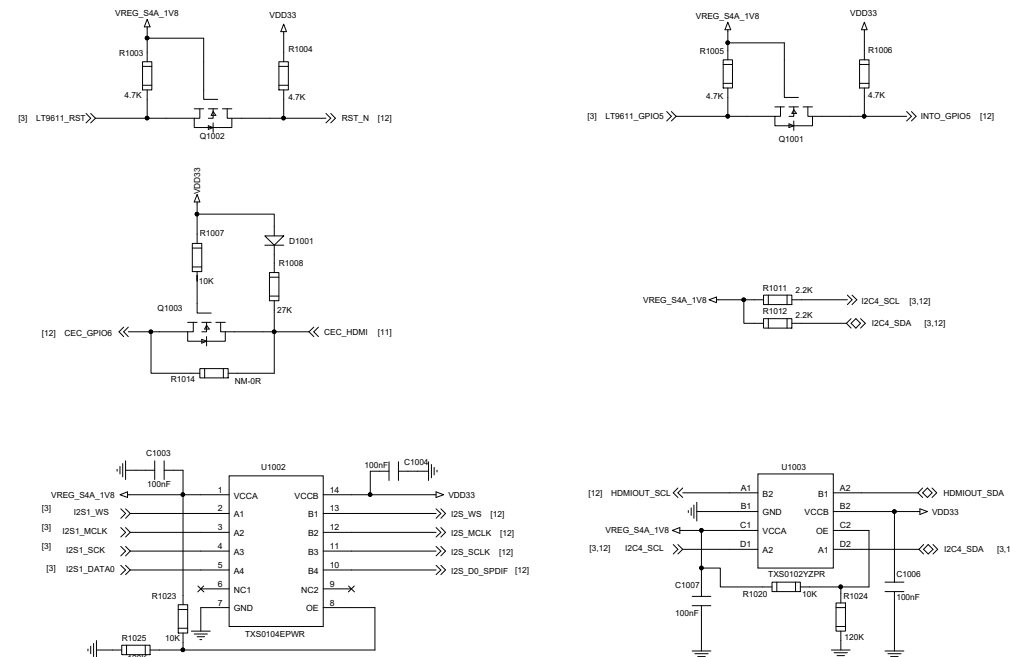
Note:

The junction capacitance of TVS on HDMI_TX/CLK should be no more than 0.5 pF.

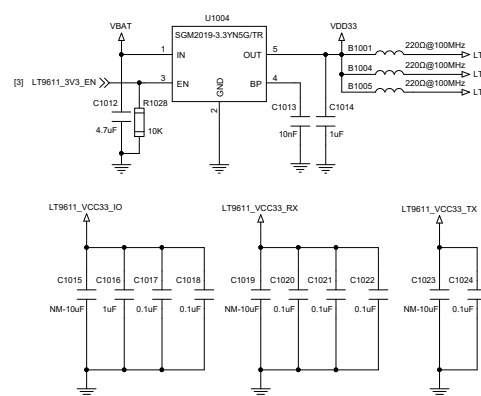
HDMI Out Interface



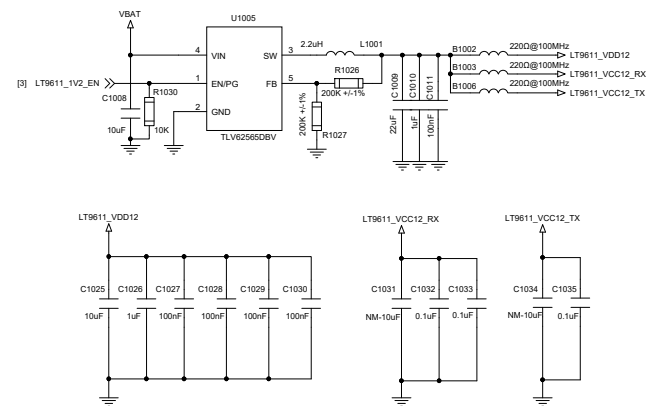
Level Shift Circuit



3.3 V Power Supply

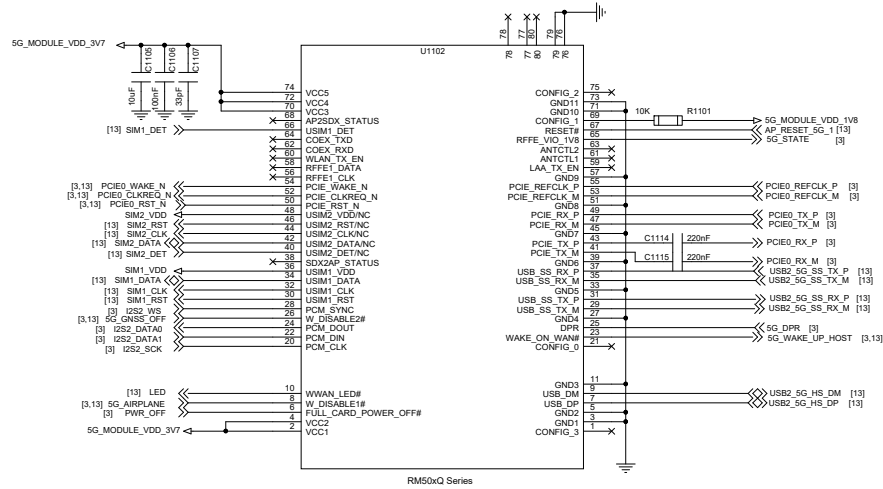


1.2 V Power Supply

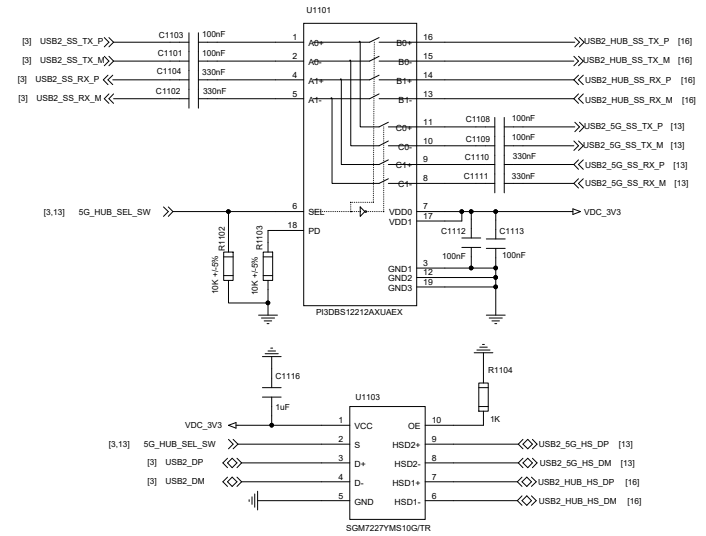


5G Module Interfaces

RM50xQ Series Interfaces



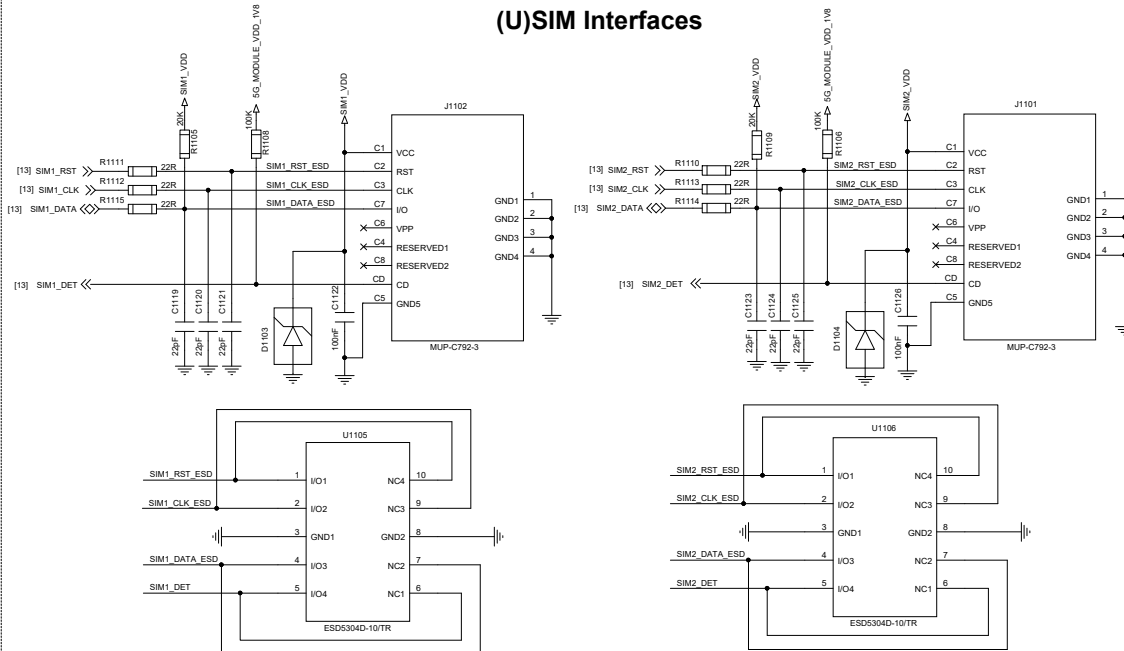
USB2 Interface



Note:

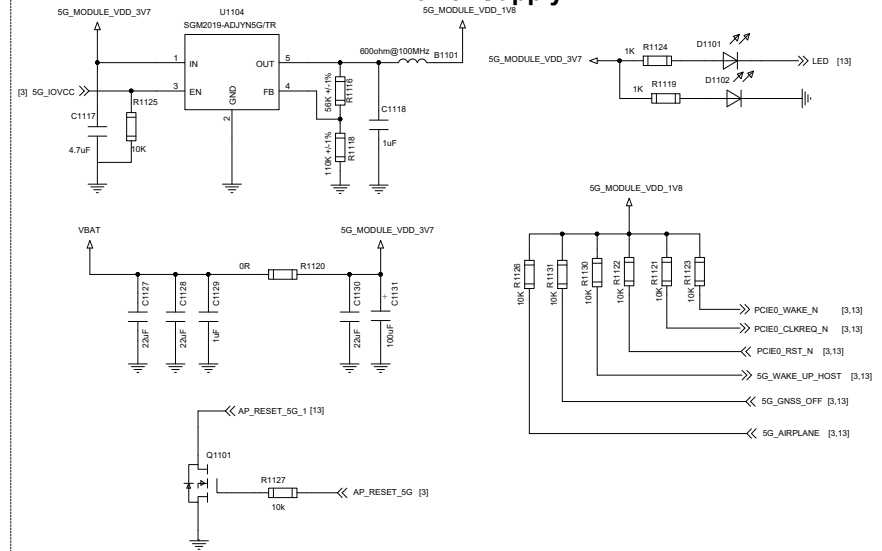
USB2 should be connected to hub by default.

(U)SIM Interfaces



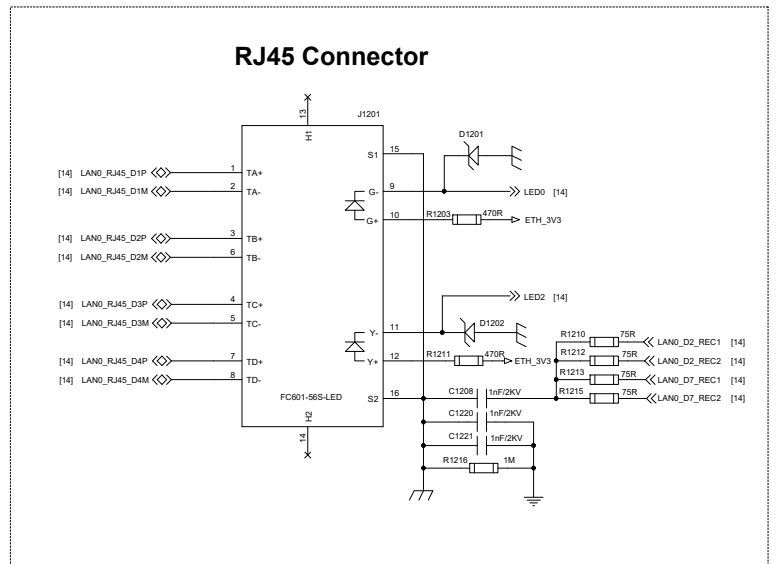
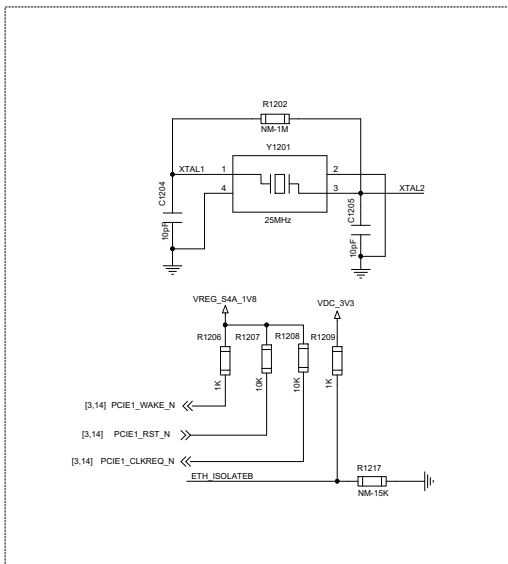
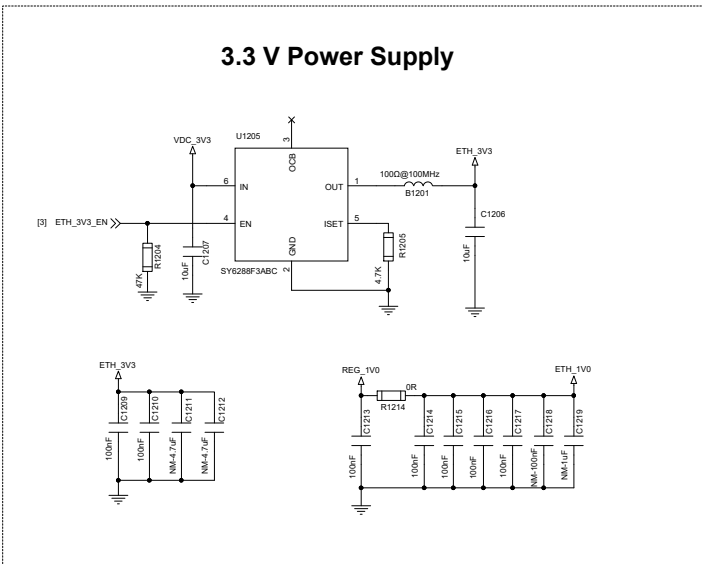
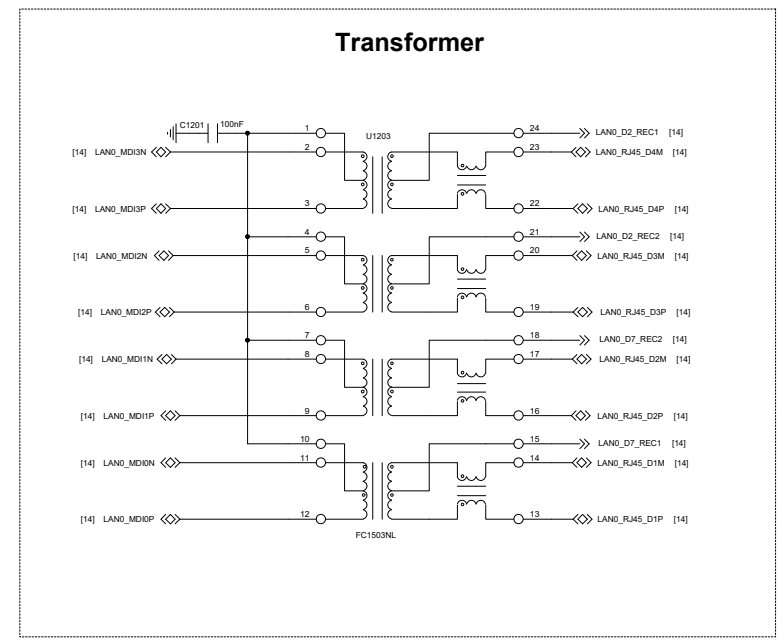
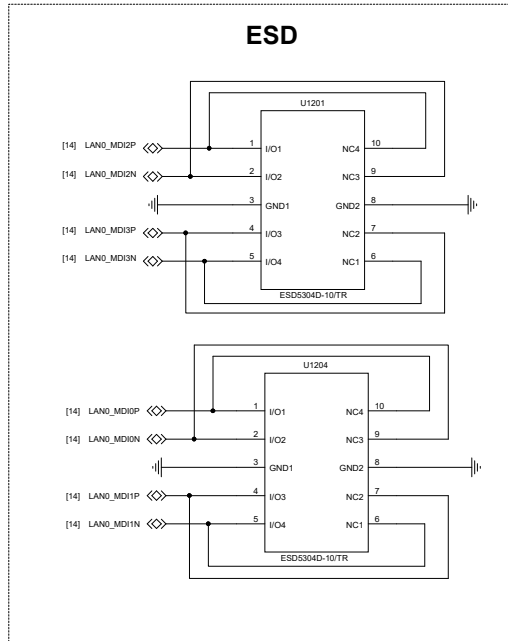
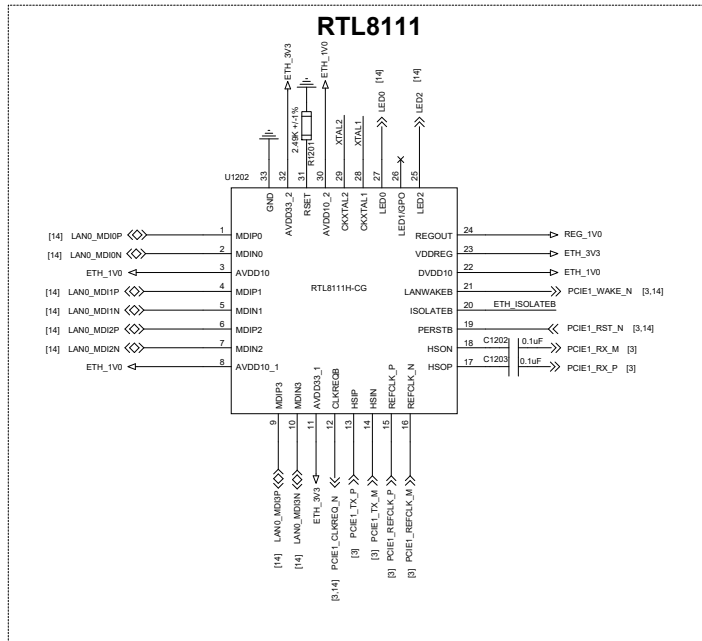
Note:
R1110–R1115 are applied to suppress EMI and enhance ESD protection.

Power Supply



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Ethernet Interface



Type-C Interface

Type-C

Note:

It is recommended to add TVS to USB1 interface:

The junction capacitance of TVS on USB 2.0 traces should be no more than 2 pF;

The junction capacitance of TVS on USB 3.1 traces should be no more than 0.5 pF.

ESD

DisplayPort Switch

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Type-C Interface

Type-C

The Type-C schematic shows a USB-UICP24S-3.1 controller connected to various USB signals. It includes capacitors C1301, C1303, C1302, C1304, C1305, C1307, C1306, C1308, and C1309. Signal traces include USB1_SS_TX0_P, USB1_SS_TX0_M, USB1_SS_RX0_P, USB1_SS_RX0_M, USB1_DP, USB1_DM, USB1_SBU1, USB1_SS_RX1_M, USB1_SS_RX1_P, USB1_TX1_P, and USB1_TX1_M.

ESD

The ESD protection circuit uses two ESD5304D-10/TR diodes (U1301 and U1302) to protect the USB signals from electrostatic discharge. The diodes are connected between the signal lines and ground.

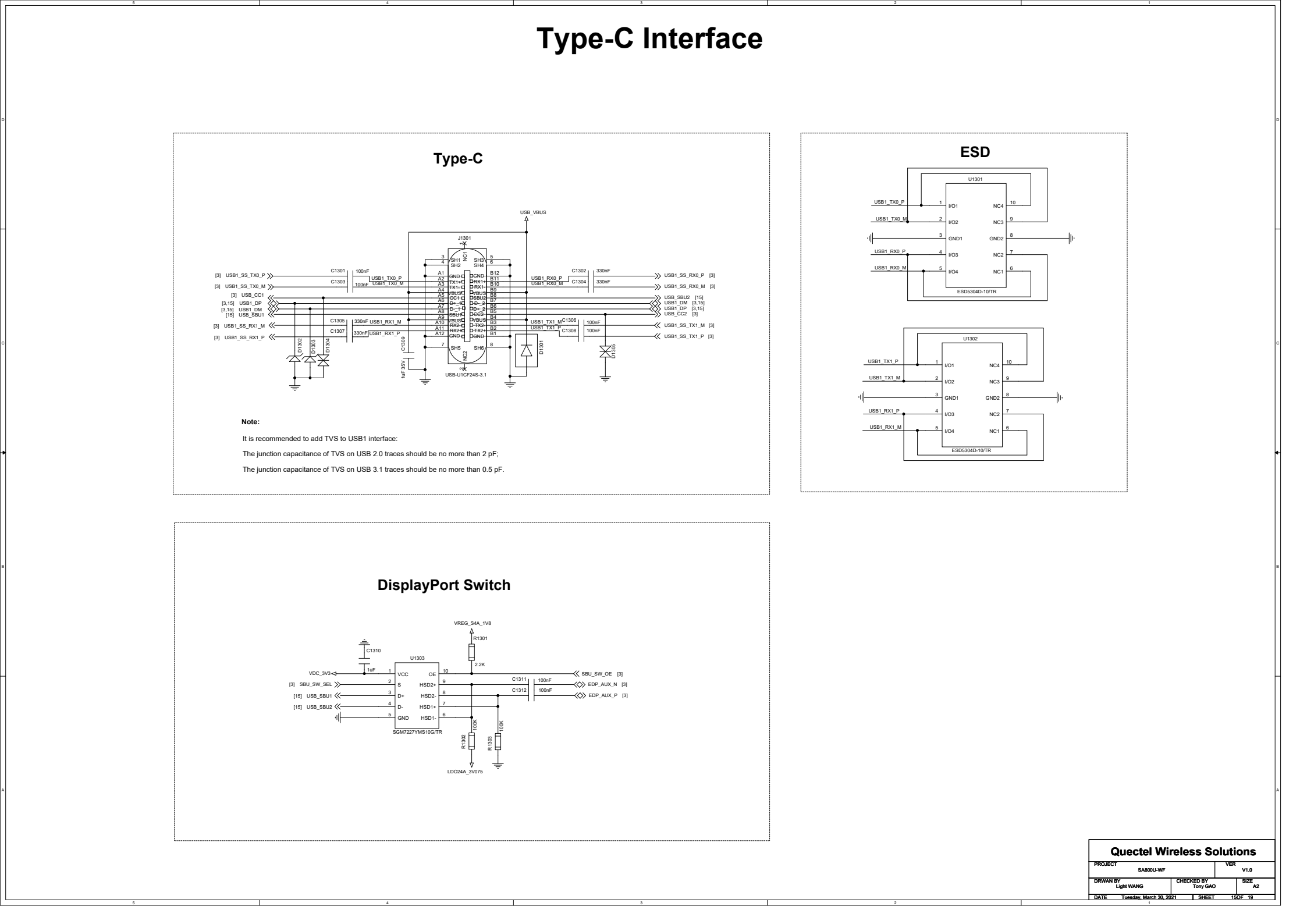
Note:

It is recommended to add TVS to USB1 interface:
The junction capacitance of TVS on USB 2.0 traces should be no more than 2 pF;
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DisplayPort Switch

The DisplayPort switch schematic features an SGM7227YMS10G/TR switch (U1303) controlled by VDC_3V3 and SBU_SW_SEL. It connects the DP+ and DP- signals to the HSD1+ and HSD1- outputs. The circuit includes resistors R1301, R1302, R1303, and R1304, and capacitors C1310, C1311, and C1312.

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Type-C Interface

Type-C

Note:

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ESD

DisplayPort Switch

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Type-C Interface

Type-C

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ESD

DisplayPort Switch

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Type-C Interface

Type-C

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ESD

DisplayPort Switch

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Type-C Interface

Type-C

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ESD

DisplayPort Switch

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Type-C Interface

Type-C

Note:

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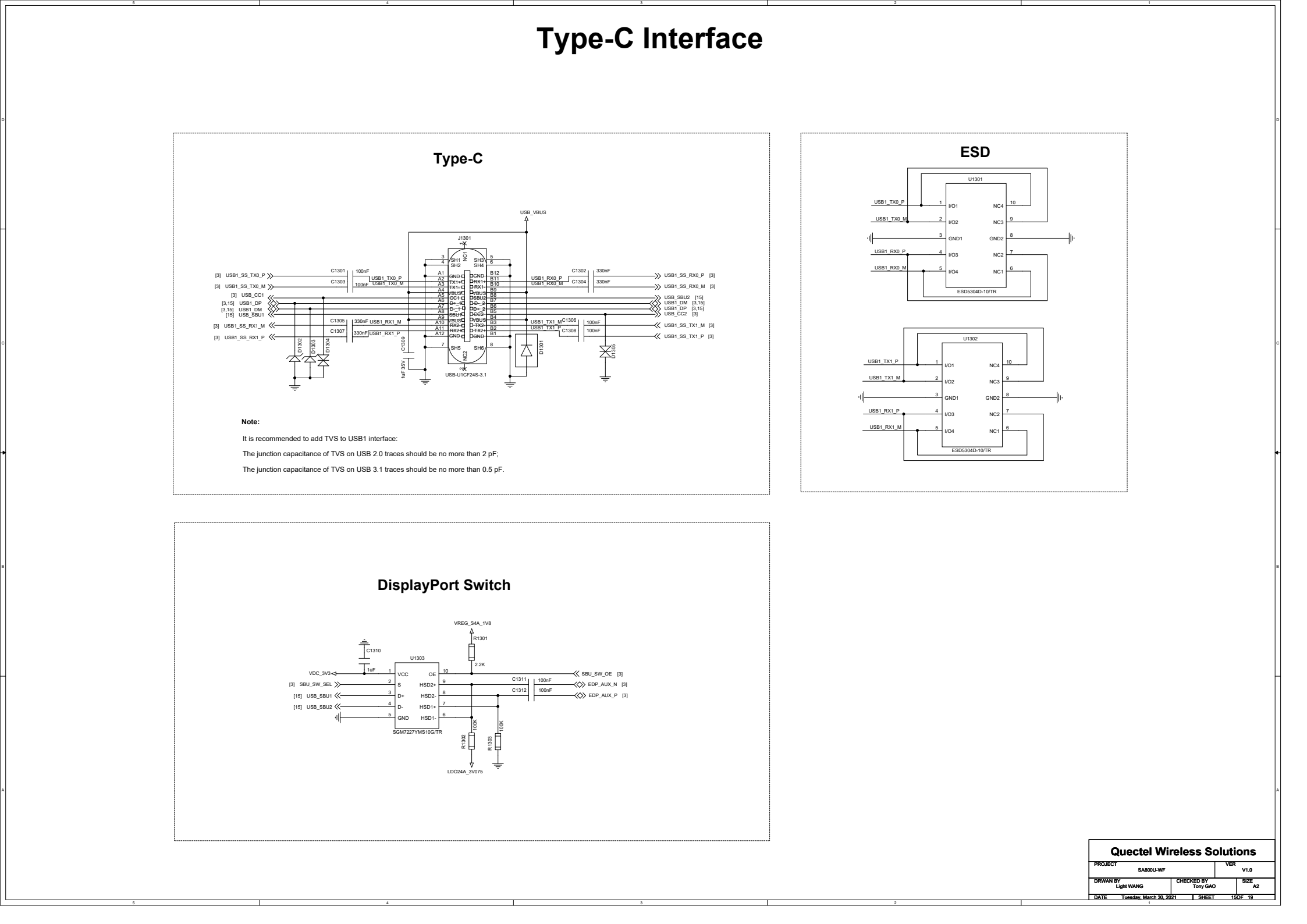
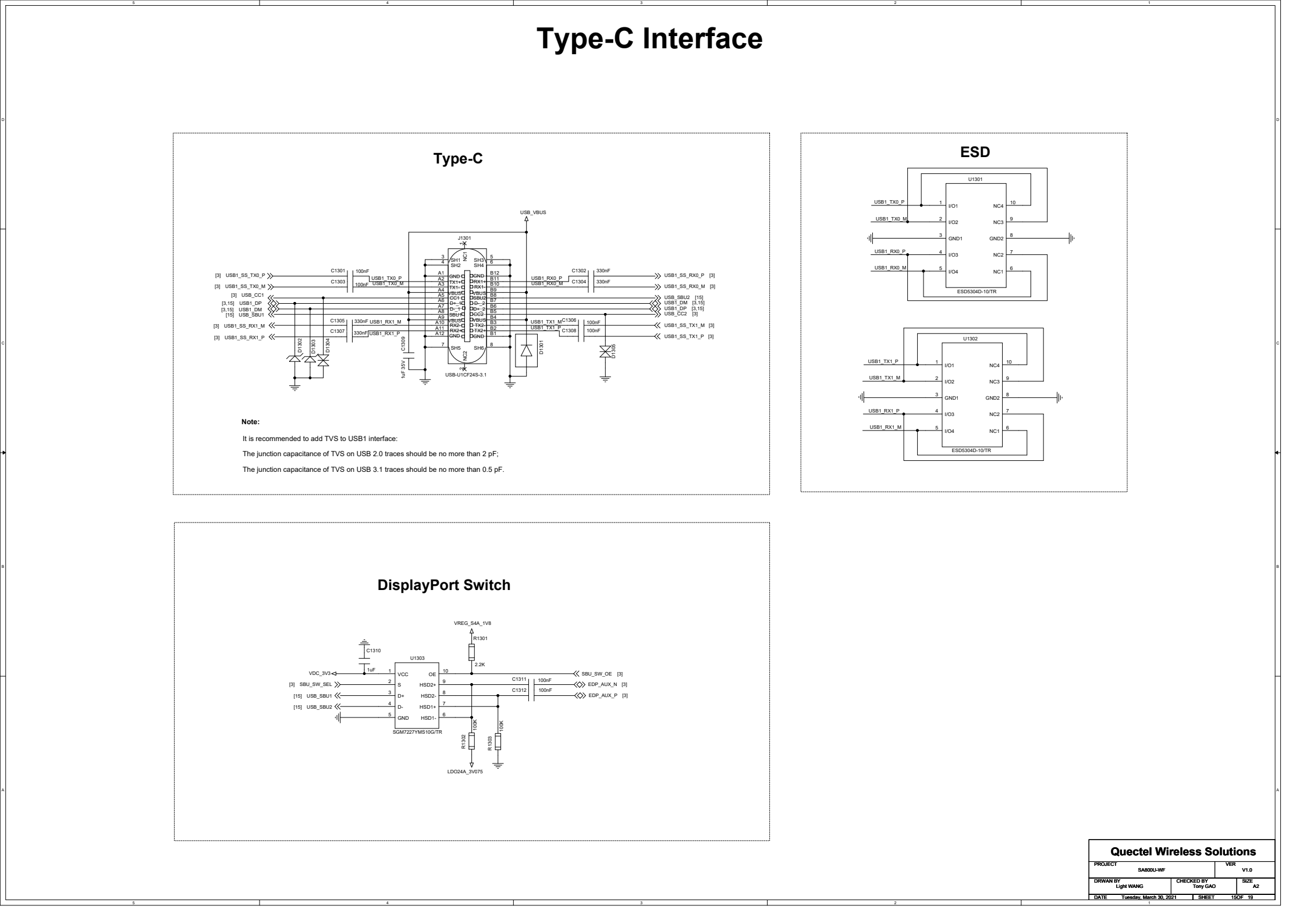
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ESD

DisplayPort Switch

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Type-C Interface

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ESD

The ESD protection circuit uses two ESD5304D-10/TR diodes (U1301 and U1302) to protect the USB signals from electrostatic discharge. The diodes are connected between the signal lines and ground.

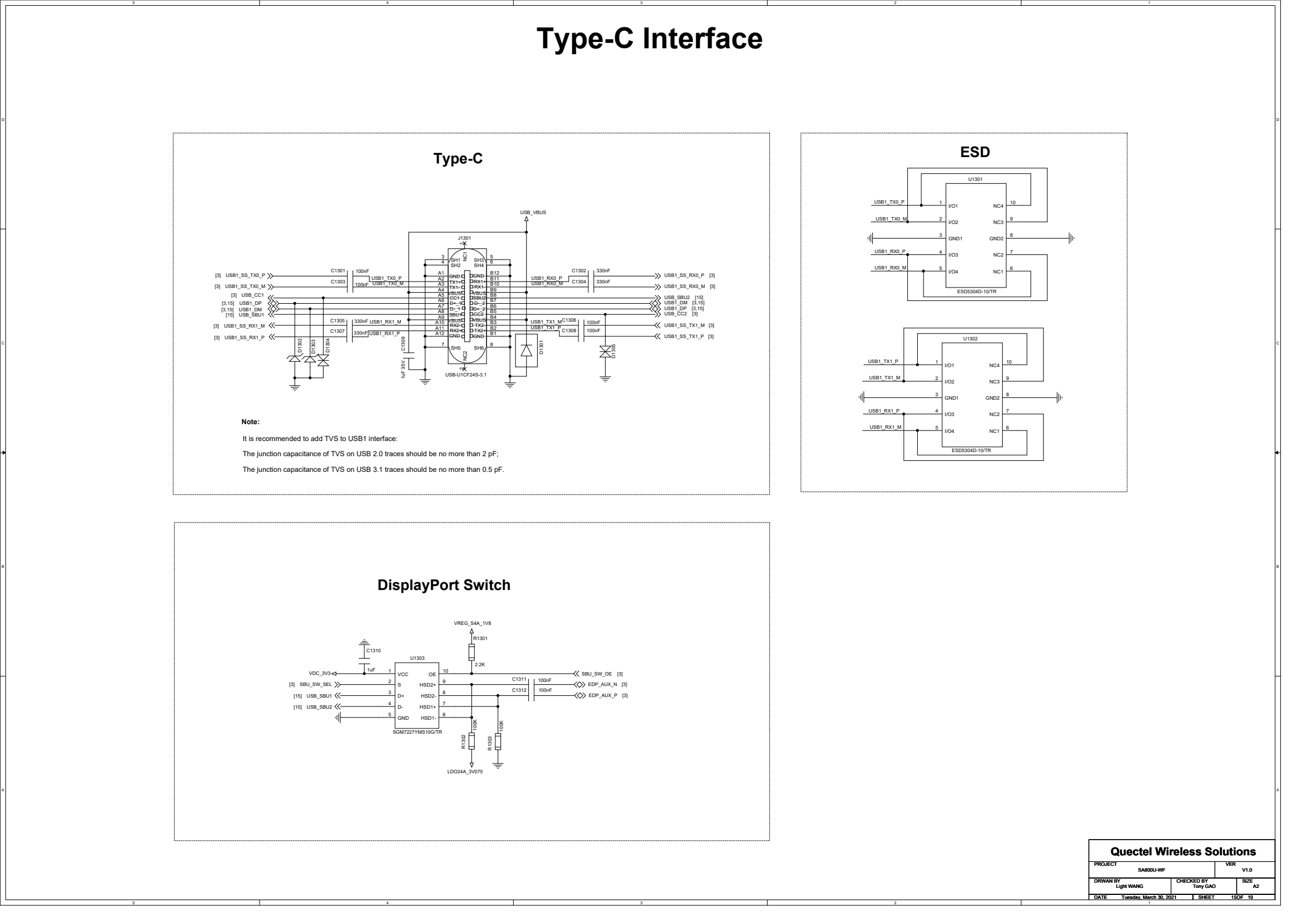
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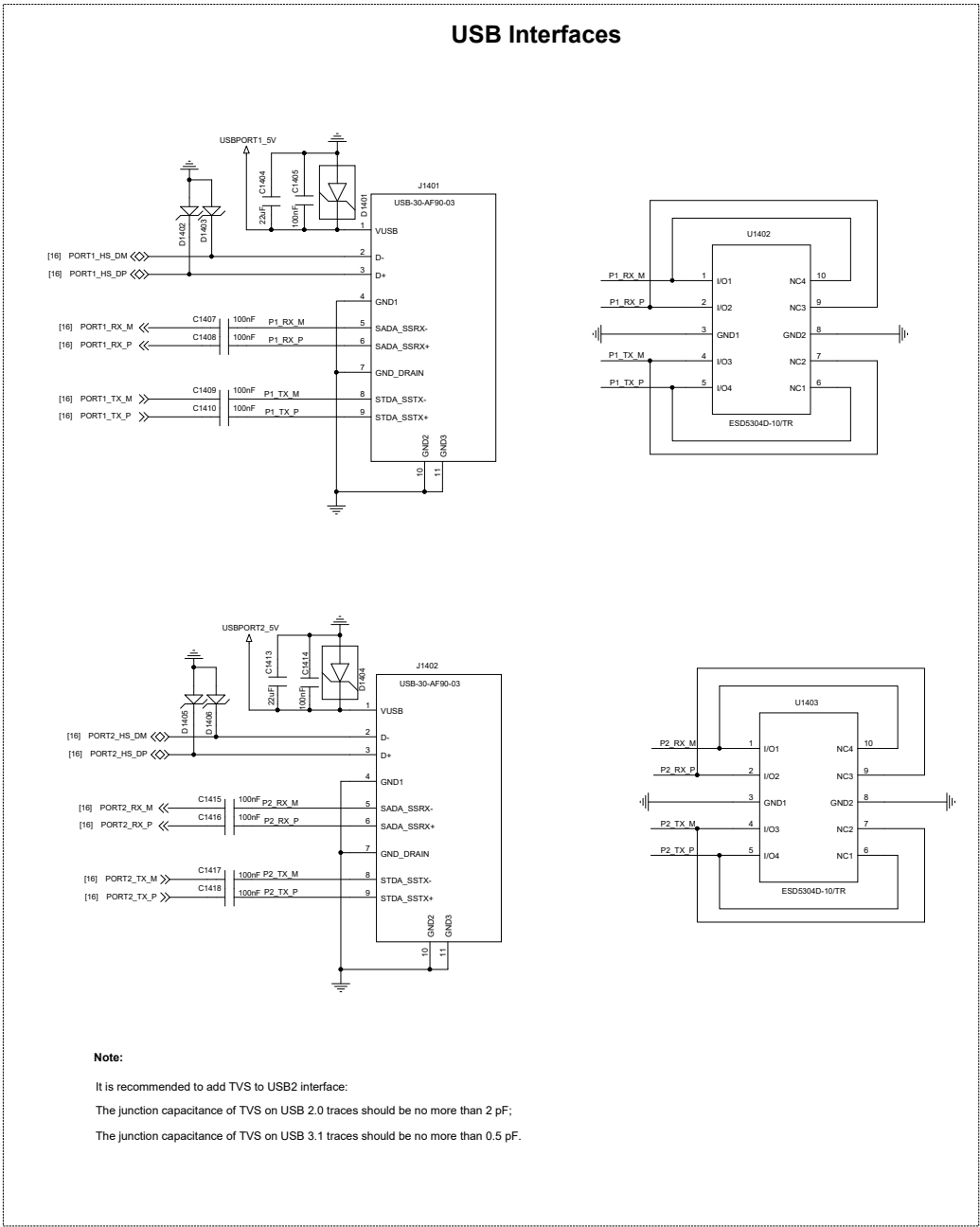
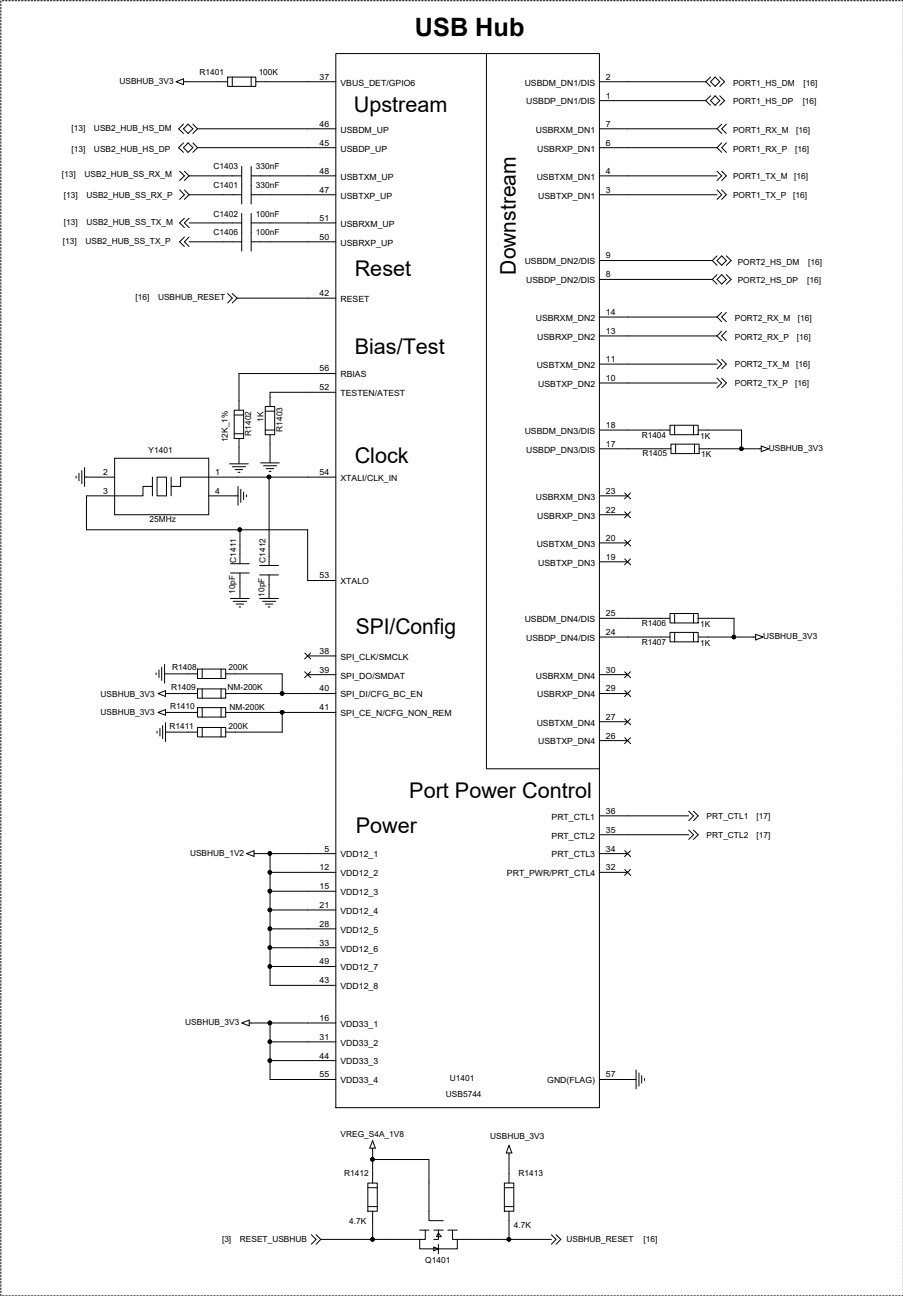
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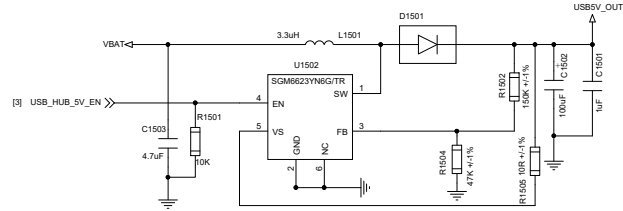


USB Hub (Part 1)

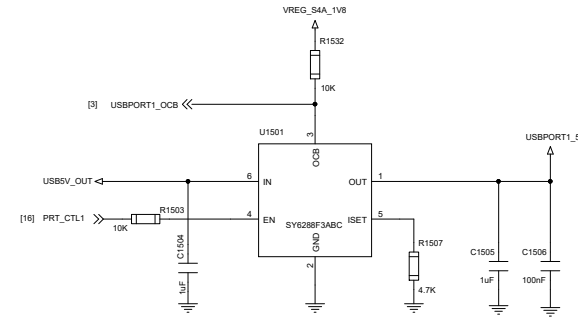


USB Hub (Part 2)

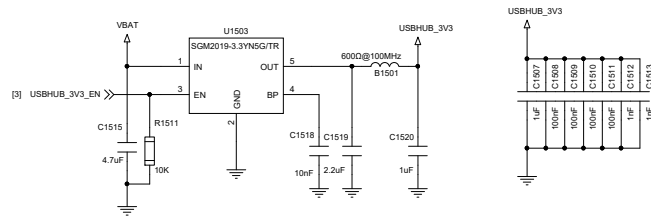
5.0 V Power Supply



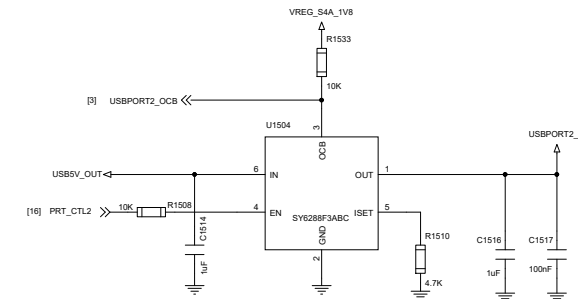
Overcurrent Protection



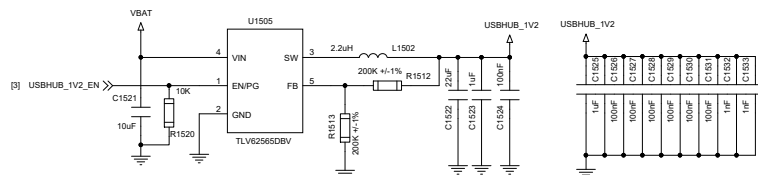
3.3 V Power Supply



Overcurrent Protection



1.2 V Power Supply

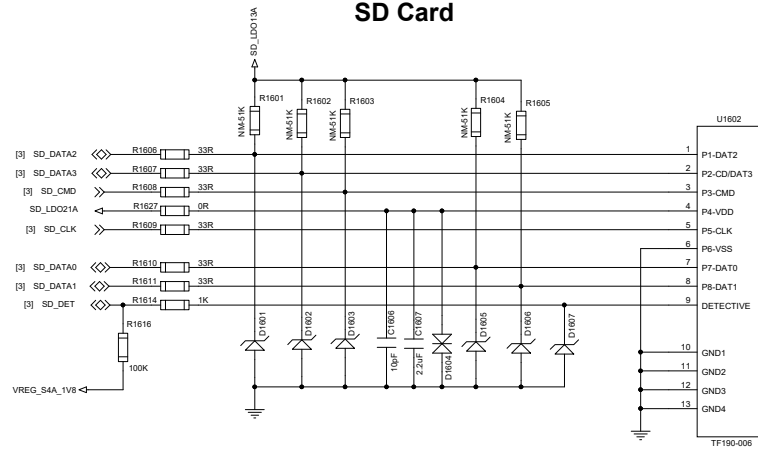


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Other Interfaces

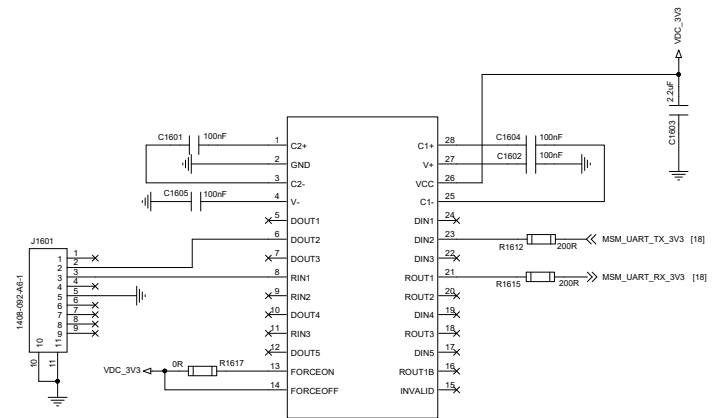
SD Card



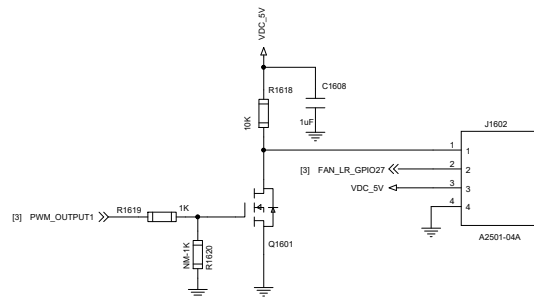
Note:

R1606-R1611 are applied to suppress EMI and enhance ESD protection.

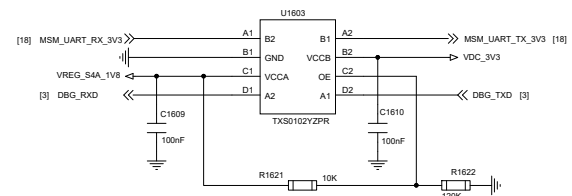
Debug UART



Fan Interface



Level Shift Circuit



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DATE	Tuesday, March 30, 2021	SHEET	18 OF 19

