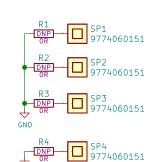
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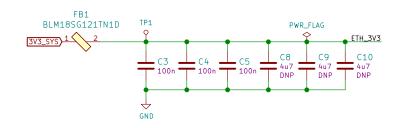
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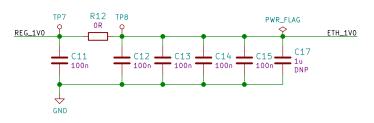
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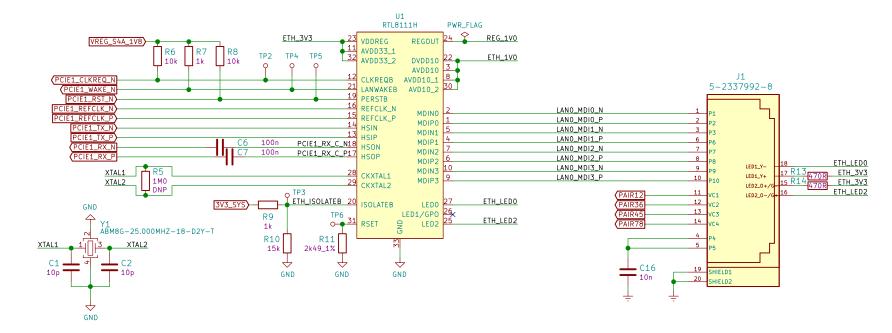
itle: SA800	U (Snapdragon	845) Baseboard
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e: A3	Date: 2022-03-28	Rev: 1.0.0
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# Ethernet controller











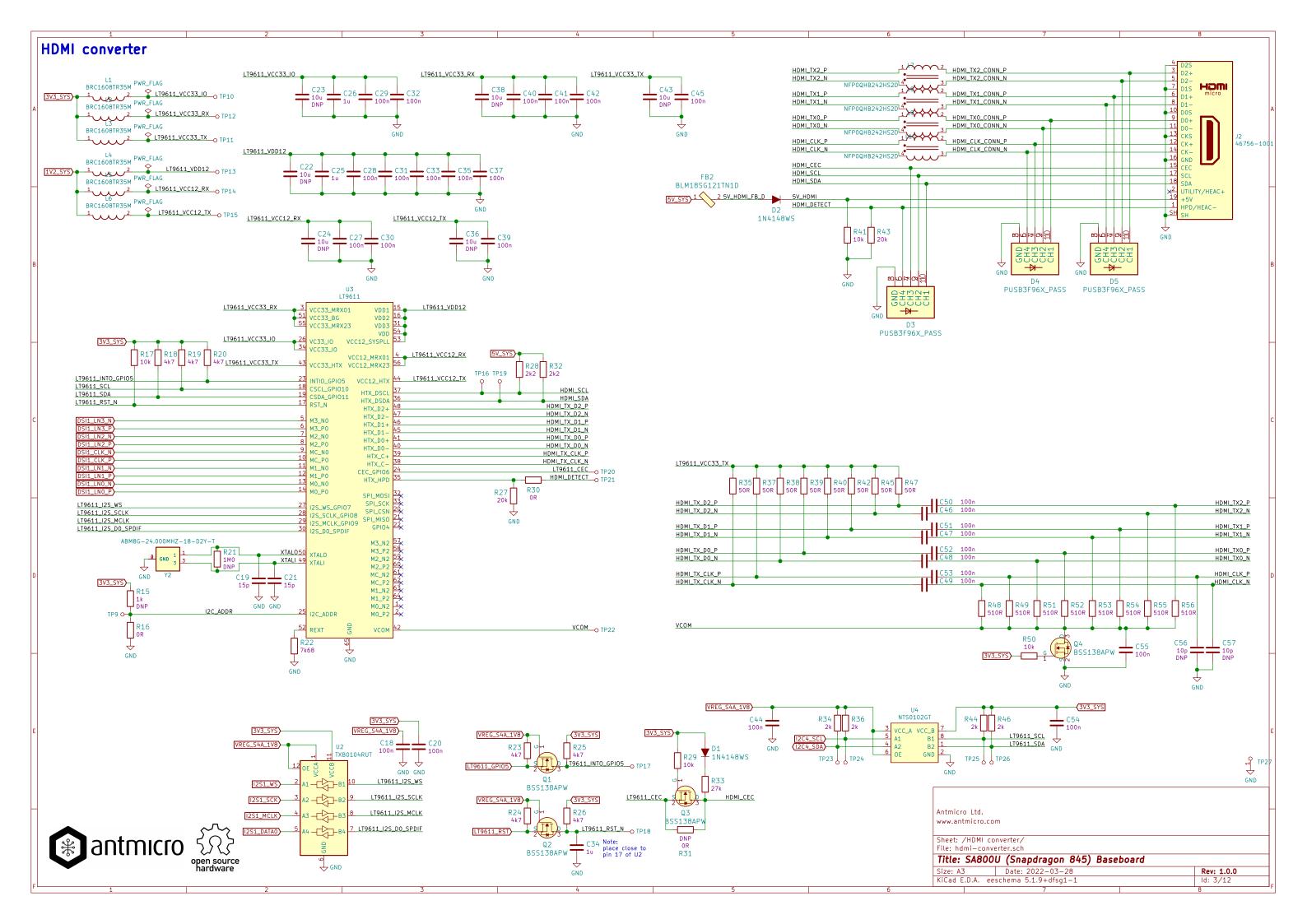
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Title: SA800U (Snapdragon 845) Baseboard

 Size: A3
 Date: 2022-03-28
 Rev: 1.0.0

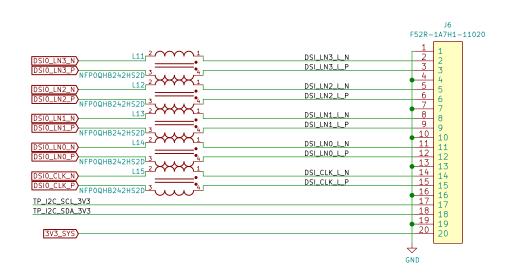
 KiCad E.D.A. eeschema 5.1.9+dfsg1-1
 Id: 2/12



#### **USB-C** interfaces USB-C OTG/DP R57 100k USB1C\_RX0\_C\_P USB1C\_RX1\_C\_N EDP\_AUX\_P USB1C\_RXO\_C\_N USB1C\_TXO\_C\_P USB1C\_RX1\_C\_P USB1C\_TX1\_C\_N USB1\_SBU\_P EDP\_AUX\_N) USB1\_D\_P USB1\_D\_N USB1\_SBU\_P USB1\_SBU\_N C61 R58 100n 100k SBU ending with N/P USB1\_SBU\_N to enable routing with differential tools C80 100n USB1C\_RX1\_C\_N USB1C\_RX1\_C\_P A10 A11 GND GND GND GND GND <del>-</del> <del>|</del> <del>-</del> <del>-k</del>-D10 PUSB3F96X\_PASS PUSB3F96X\_PASS PUSB3F96X\_PASS PUSB3F96X\_PASS Debug D6 1N4148WS TPS7A0518PDBVT TPS7A0518PDBVT J5 12401598E4\_2A TP111 PWR FLAG Texas Instruments PWR FLAG PWR FLAG TP101 2 1 DBG\_VBUS 5V0\_DBG FB3 BLM18EG221SN1D DBG\_USB\_D\_N DBG\_USB\_D\_P DBG\_USB\_D\_P × A6 DBG\_VBUS DBG\_VBUS DBG\_USB\_D\_N A7 B6 DBG\_USB\_D\_F DBG\_VBUS ×A10 ×A11 DBG\_USB\_D\_P DBG\_USB\_D\_N GND USBDM 1 TEST 26 <del>-N-</del> TP100 D11 PUSB3F96X\_PASS GND GND GND USB-C Host PWR\_FLAG USB2\_5V USB2\_CC1 USB2\_EN\_5\ USB2\_5V USB2\_5V 11 USB2\_CC2 CC2 USB2\_FAULT\_5V\_\_O TP108 nFAUL1 GND USB2\_SINK\_5V\_O TP109 USB2\_CC\_SELECT\_O TP110 GND nPOL 100k\_0.5% USB2\_CC1 B4 B3 USB2C TX2 C N B2 USB2C TX2 C C N GND C95 100n USB2C\_TX2\_N C91 100n USB2C\_TX2\_P U9 CBTL02043ABQ,115 -\(5V\_SYS\) **(1**V8\_SYS <u>19 USB2C\_RX1\_P</u> <u> 18 USB2C\_RX1\_N</u> B0\_N 7\_USB2C\_TX1\_P B1\_N 16 USB2C\_TX1\_N USB2\_SINK\_5V 1 USB2\_FAULT\_5V TP102 O USB2\_EN\_5V USB2\_CC\_SELECT Antmicro Ltd. www.antmicro.com GND \* antmicro Sheet: /USB-C Interface / File: usb-c-interface.sch <del>-</del> open source hardware Title: SA800U (Snapdragon 845) Baseboard PUSB3F96X\_PASS Size: A3 Date: 2022-03-28 KiCad E.D.A. eeschema 5.1.9+dfsg1-1

DSI LDC connector









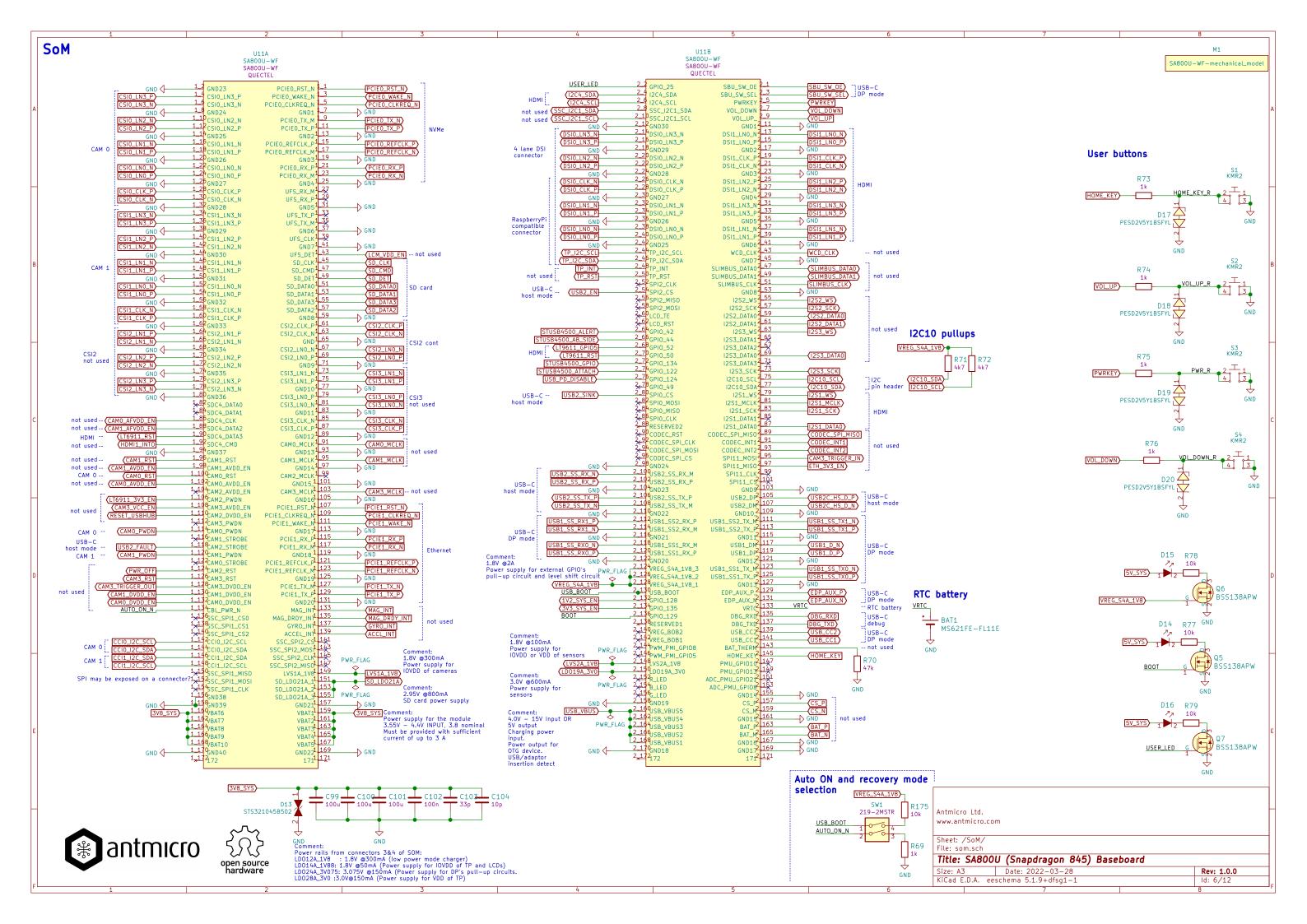
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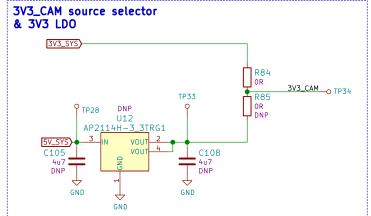
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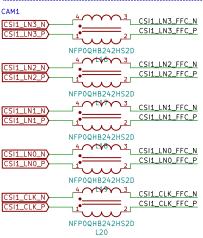
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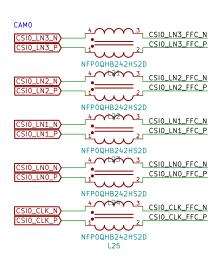
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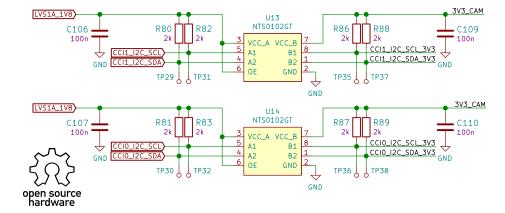


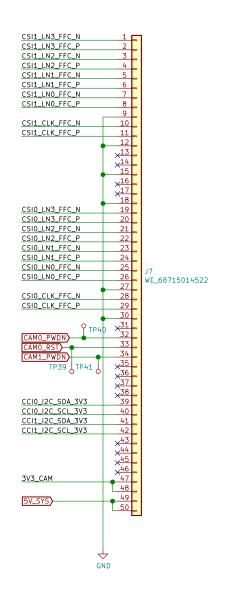
### Camera interfaces











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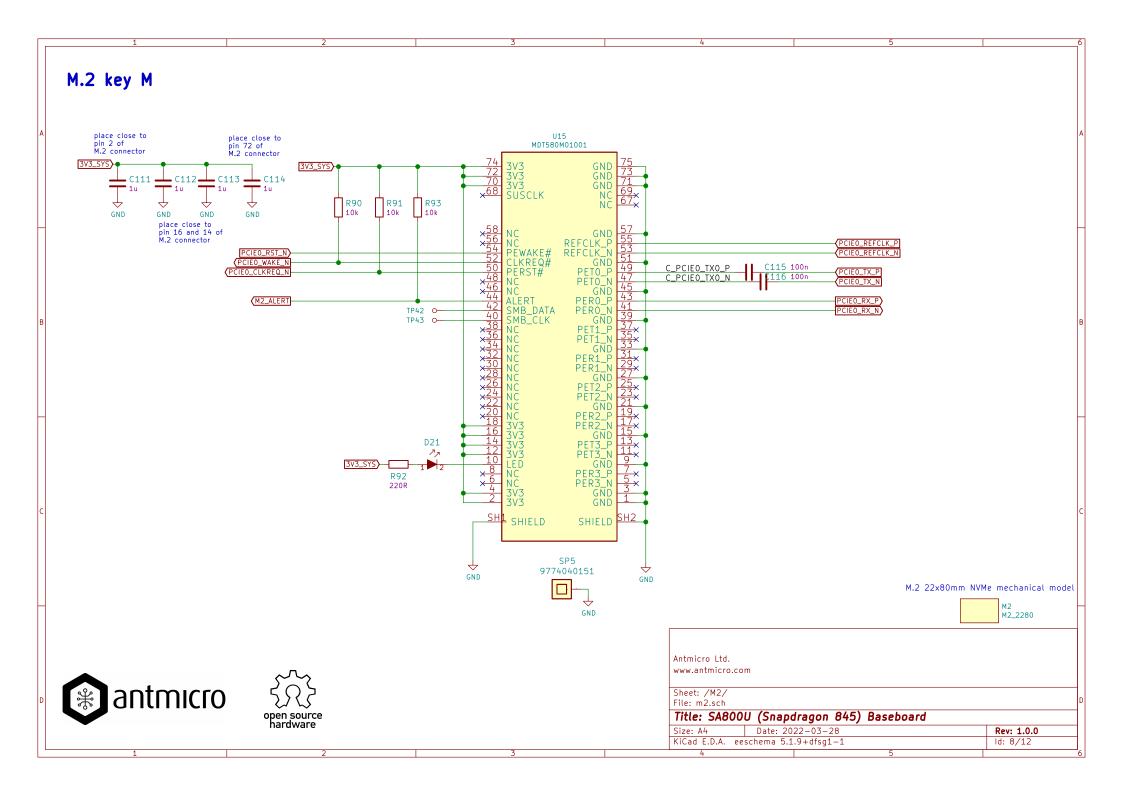
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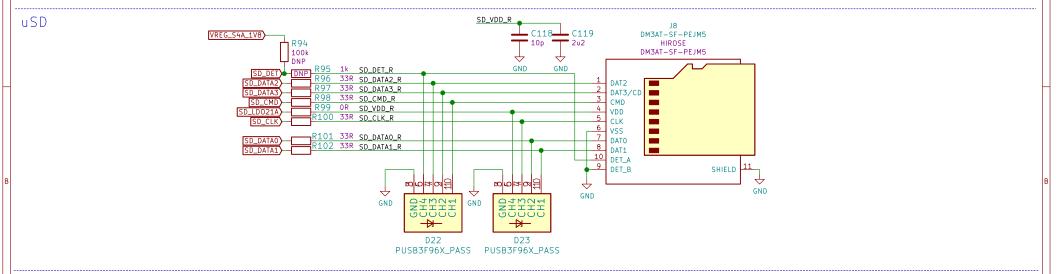
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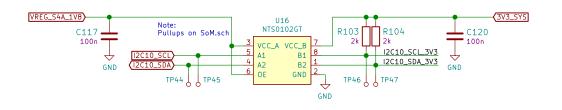


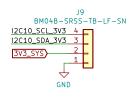


### Other interfaces



12C









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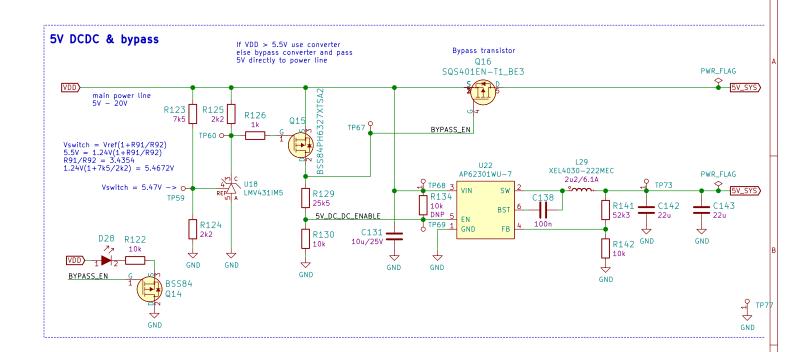
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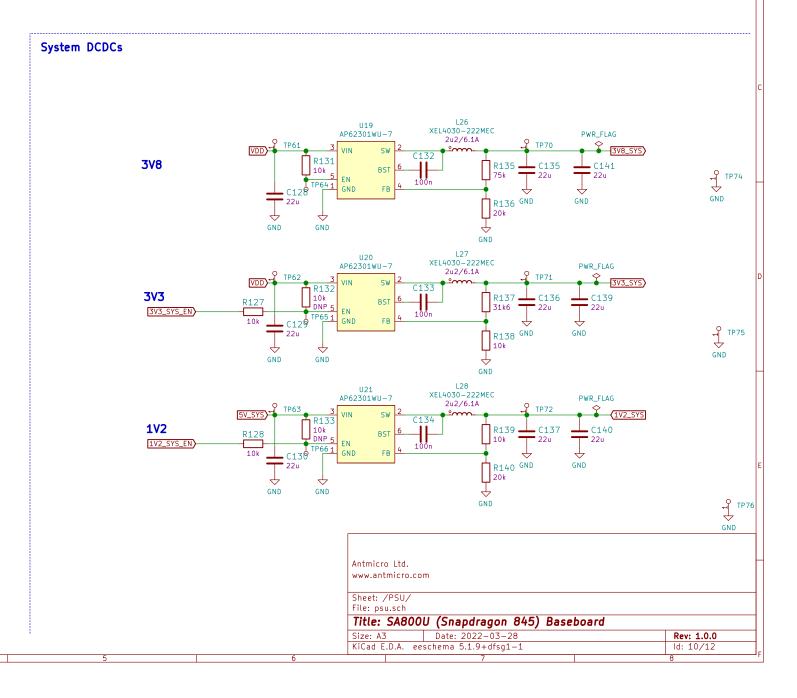
Size: A4	Date: 2022-03-28			Rev: 1.0.0
KiCad E.D.A. eeschema 5.1.9+dfsg1-1				ld: 9/12
1.			E	•

# Power section Source selector Auxiliary Power Connector J10 1053131202 PWR\_FLAG PWR\_FLAG AUX\_VDD VDD) R121 C121 • 10u/25V GND D27 GND GND TP49 5-15V, middle priority TP50 5V\_POE 5V0, lowest priority TP51 GND GND VS2 GND VS3 G3 R112 200k Fall: UV 4.8V OV 18.4V TP54 TP55 TP57 R118 R119 R113 Rise: UV 4.85V OV 18.45V POE\_VALID USB\_PD\_VALID R114 13k7 AUX\_VALID R109 39k Rise: UV 4.85V OV 18.45V 11 × 24 × 23 × R11 13k7 Fall: UV 4.8V OV 18.4V Rise: UV 4.85V OV 18.45V EPAD GND R107 13k7 Indicator leds D24 R111 D25 R115 D26 R116 VDD 1 2 VDD 1 2 AUX\_VALID USB\_PD\_VALID G POE\_VALID B550 Q13

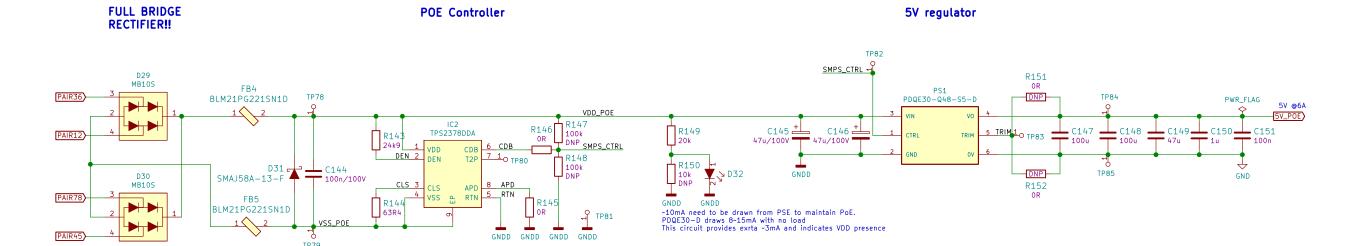
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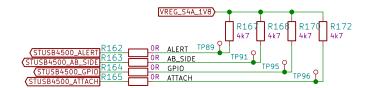
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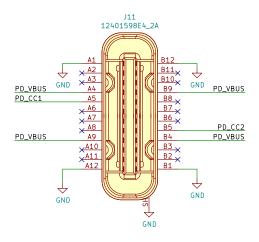
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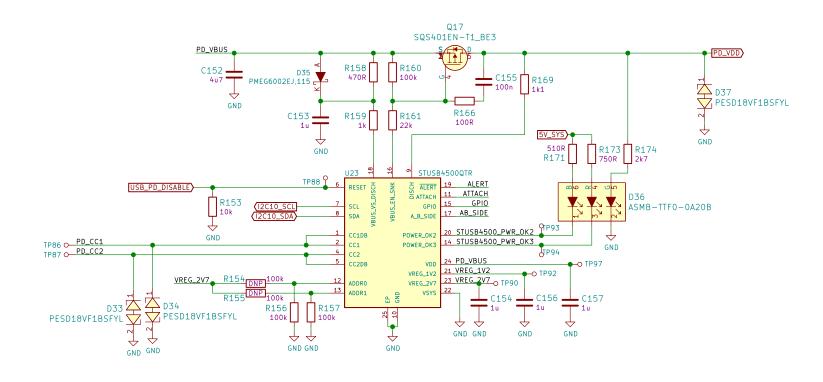
Title: SA800U (Snapdragon 845) Baseboard

Size: A3 Date: 2022-03-28 KiCad E.D.A. eeschema 5.1.9+dfsg1-1

# USB-C PD controller











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Title: SA800U (Snapdragon 845) Baseboard

 Size: A3
 Date: 2022-03-28
 Rev: 1.0.0

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