

[illegible]

Power supply

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The schematic diagram illustrates the electrical connections for the MCU board. It includes the following components and connections:

- MCU (U6):** EFM32HG321. Pin connections include:
 - PA0: VDD_RG
 - PA1: AVDD_0
 - PA2: AVDD_1
 - PA8: OVDD_0
 - PA9: OVDD_3
 - PA10: OVDD_5
 - PC0: RX_SRC_SDA_3V
 - PC1: RX_SRC_SCL_3V
 - PC2: RX_SINK_SDA_3V
 - PC3: RX_SINK_SCL_3V
 - PC4: RX_HP0_3V
 - PC8: TX_HP0_3V
 - PC9: TX_HP0_3V
 - PC10: TX_HP0_3V
 - PE10: PWR_CTL_D0
 - PE11: PWR_CTL_D1
 - PE12: TX_SINK_SDA_3V
 - PE13: TX_SINK_SCL_3V
 - PF0: DBG_SWCLK
 - PF1: DBG_SWIO
 - PF2: TX_SRC_CEC
 - PF3: TX_SINK_CEC
 - PF4: RX_SRC_CEC
 - PF5: RX_SINK_CEC
 - PF6: RESET
- Power and Ground:**
 - Two +3V3 power sources are connected to the MCU pins.
 - Capacitors C5, C37, C1, C26, and C6 are used for decoupling.
 - Resistors R1, R2, and R66 are used for current limiting.
- Debug and Status:**
 - SWD Debug pins are connected to the MCU.
 - DBG_LED_0 and DBG_LED_1 are connected to the MCU pins.
- Other Components:**
 - J1: 0473460001 (Header).
 - D9: LED.
 - D10: RCLAMP0524PATCT (Relay).

Config

The diagram shows a 4-pin header labeled SW1 (1571983-5). The pins are connected to TX_EQ0 1.1, TX_EQ1 2.1, RX_EQ0 3.1, and RX_EQ1 4.1. The header also has a GND connection.

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Title: Video Booster Board			
Size: A2	Date:	Rev: 1.0	
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