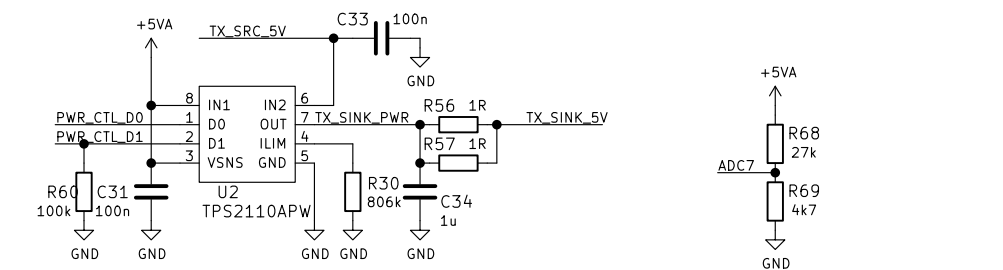


[illegible][illegible]

The figure contains two circuit diagrams for the NTS0102GT component. The left diagram is the TX section, showing pins TX_SRC_SCL_V (pin 3), TX_SRC_SDA_V (pin 6), and TX_SRC_VCC_B (pin 4) connected to a +3V3 supply. A 100nF capacitor (C32) is connected between the supply and the TX_SRC_VCC_B pin. The right diagram is the RX section, showing pins RX_SINK_SCL_V (pin 3), RX_SINK_SDA_V (pin 6), and RX_SINK_VCC_B (pin 4) connected to a +3V3 supply. A 100nF capacitor (C39) is connected between the supply and the RX_SINK_VCC_B pin. Both sections also show pins TX_SRC_VCC_B (pin 7) and RX_SINK_VCC_B (pin 7) connected to a +3V3 supply through a 100nF capacitor (C38).



The diagram illustrates the internal circuitry of an HDMI receiver module. It features an HDMI connector (J3) on the left, which is connected to a PTN3363 receiver IC (U1). The PTN3363 is powered by a +3V3 supply and has various pins for RX_OUT, RX_IN, and RX_SRC. The RX_IN pins are connected to an HDMI connector (J9) on the right. The PTN3363 is also connected to an EFM32HG321 MCU (U6) via a USB connector (J1). The MCU is powered by a +3V3 supply and has various pins for TX_SRC, TX_SINK, and TX_EQ. The MCU is also connected to a USB connector (J1) and a USB connector (J1). The layout includes various components like capacitors, resistors, and a USB connector, with a clear pinout for the HDMI connector and the MCU.

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