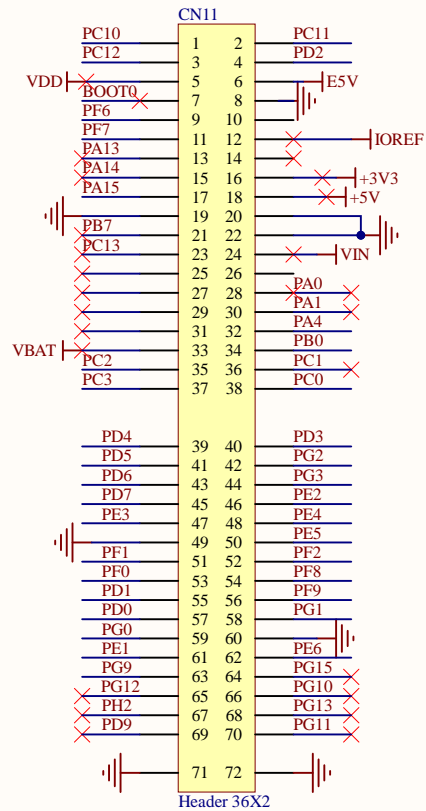
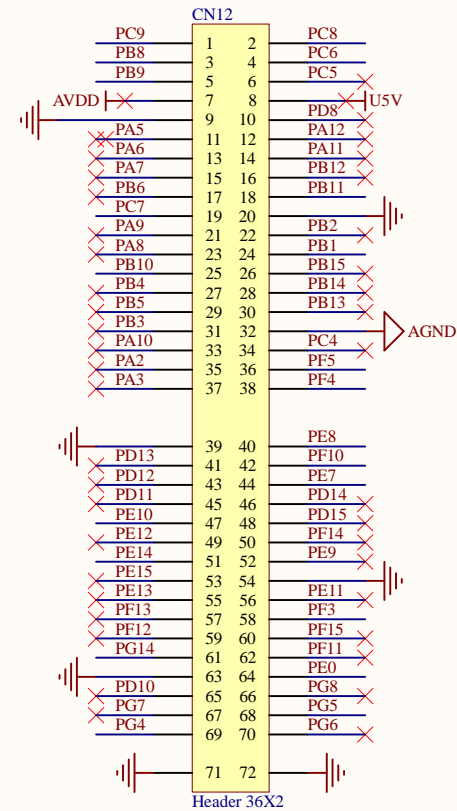


Morpho connector



Morpho connector



PA[0..15] PA[0..15]
PB[0..15] PB[0..15]
PC[0..15] PC[0..15]
PD[0..15] PD[0..15]

PE[0..15] PE[0..15]
PF[0..15] PF[0..15]
PG[0..15] PG[0..15]
PH[0..2] PH[0..2]

Title: Nucleo 144 Connections

Projekt: ITS-BRD

Size: A4

Number: 2

Revision: 02

Date: 16.12.2020

Time: 14:16:33

Sheet 2 of 5

File: Nucleo-144-connection.SchDoc

Dokument VCS Rev: 111277ad951877c88840ddc74e87a05f36626231 [Locally Modified]

Projekt-Folder VCS Rev: 111277ad951877c88840ddc74e87a05f36626231



Labor für technische Informatik

Author: Tobias Jähnichen

A

B

C

D

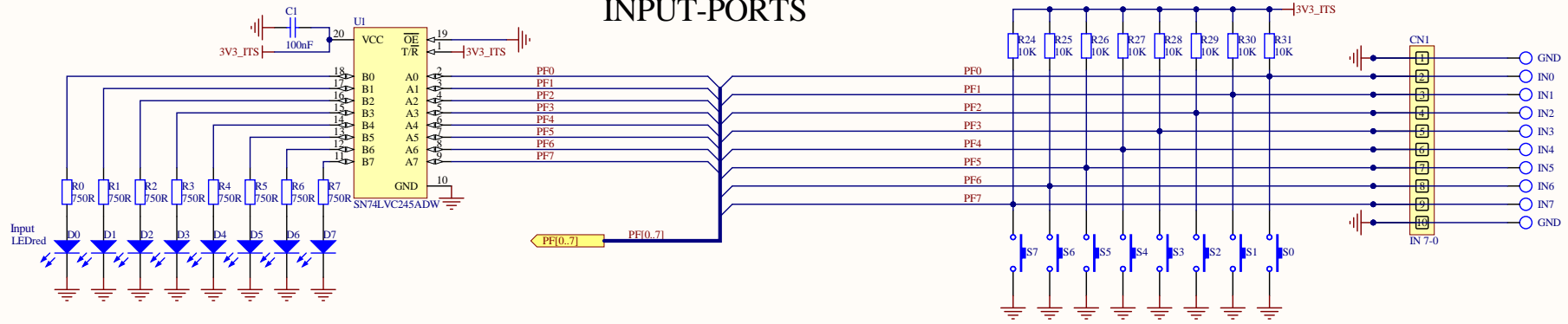
A

B

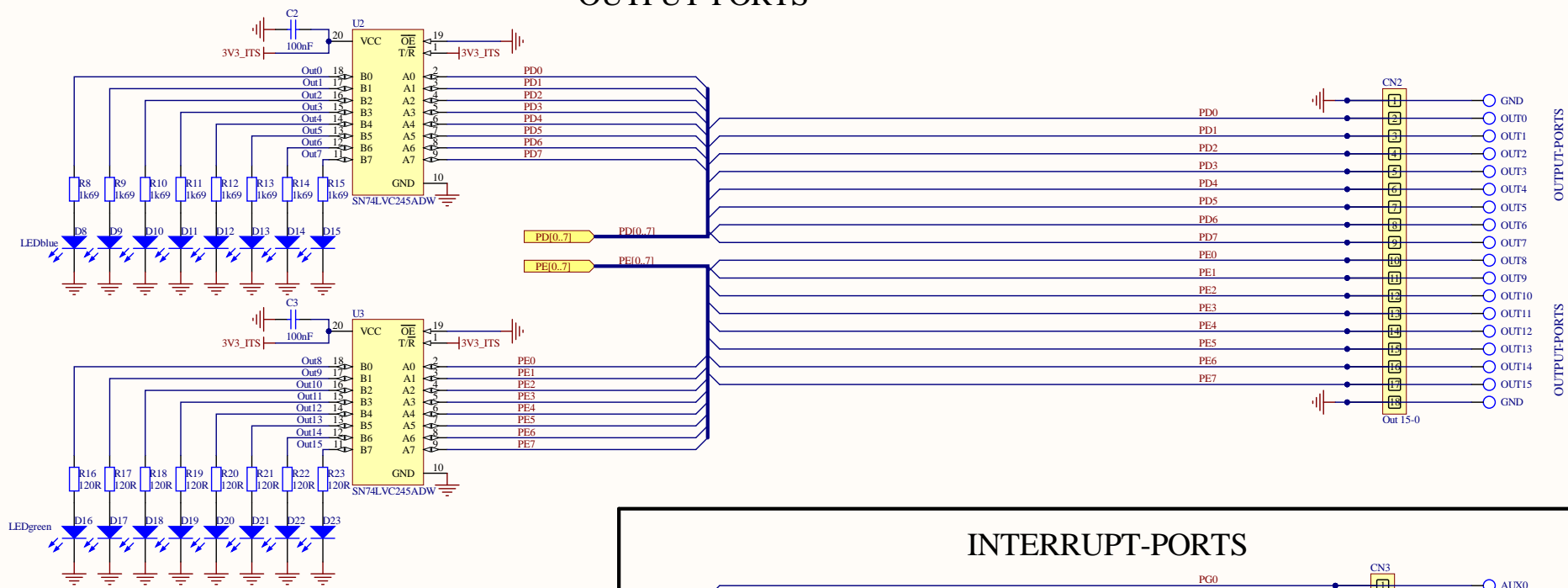
C

D

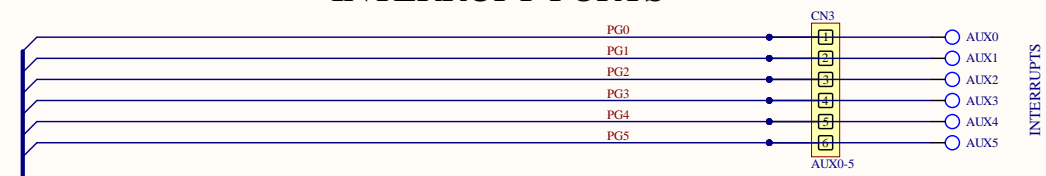
INPUT-PORTS



OUTPUT-PORTS



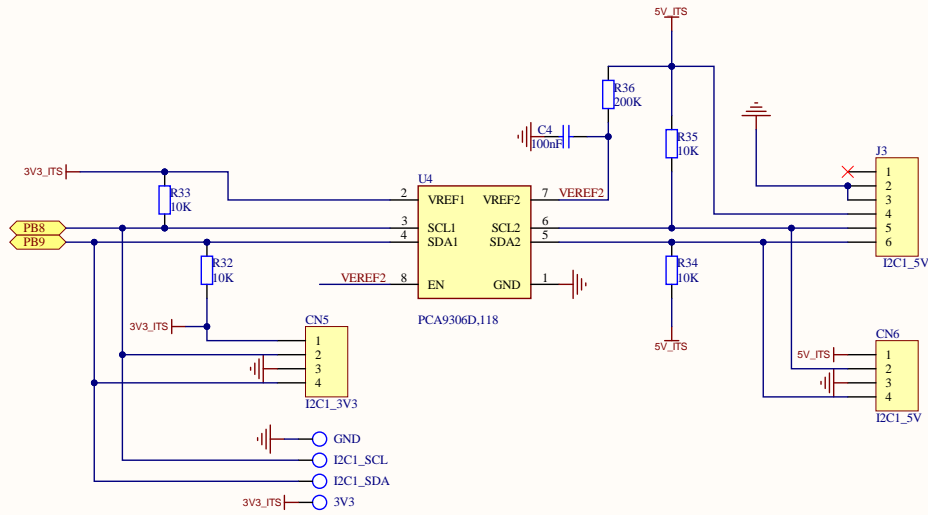
INTERRUPT-PORTS



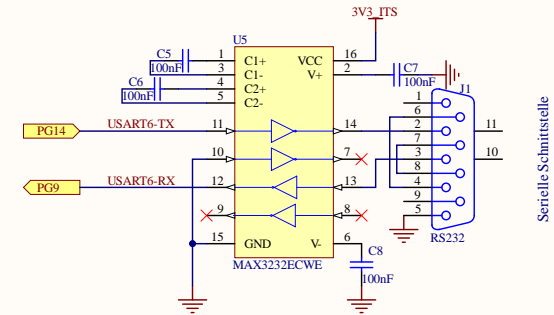
Title: GPIOs			
Projekt: ITS-BRD			
Size: A3	Number: 3	Revision: 02	
Date: 16.12.2020	Time: 14:16:33	Sheet 3 of 5	Author: Tobias Jähnichen
File: GPIOs_SchDoc			
Dokument VCS Rev: 111277ad951877c88840ddc74e87a05f36626231 [Locally Modified]			
Projekt-Folder VCS Rev: 111277ad951877c88840ddc74e87a05f36626231			

Labor für technische Informatik

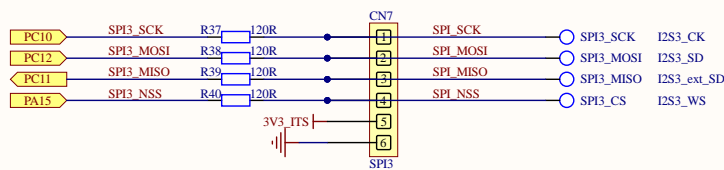
I2C



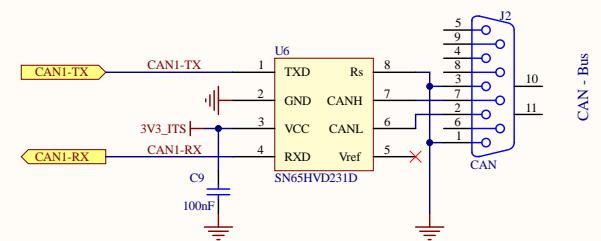
RS232



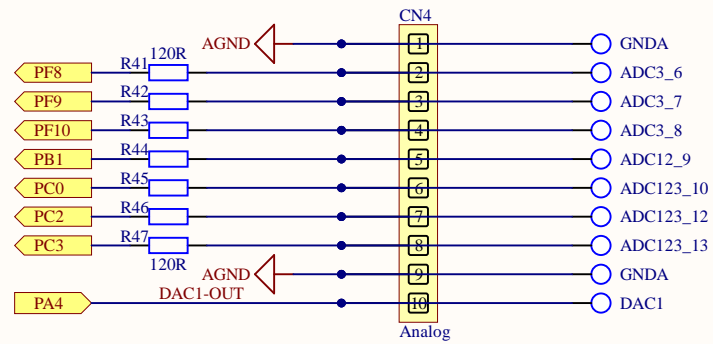
SPI/I2S



CAN

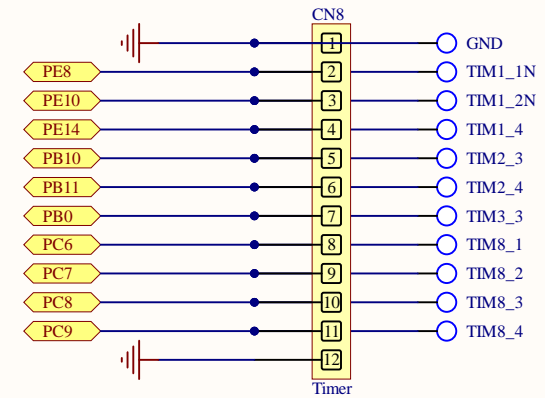


Analog




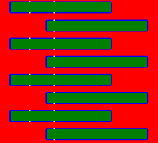
Analog / Digital

Timer



Timer

Title: Analog and Timers			 Labor für technische Informatik
Projekt: ITS-BRD			
Size: A4	Number: 5	Revision: 02	
Date: 16.12.2020	Time: 14:16:34	Sheet 5 of 5	
File: Analog-Timers.SchDoc			Author: Tobias Jähnichen
Dokument VCS Rev: cfee652982c78c48293699dc6e877e158f4d69ec [Locally Modified]			
Projekt-Folder VCS Rev: 111277ad951877c88840ddc74e87a05f36626231			



HAW
HAMBURG

Department Informatik
Lehrstuhl Technische Informatik

ITS-BRD

Rev. 02.15.12.10.20.1A

Timer

Aux

Analog

SPI3

I2C1

Out

In

