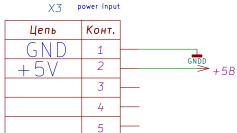


X4 Цепь

Конт.

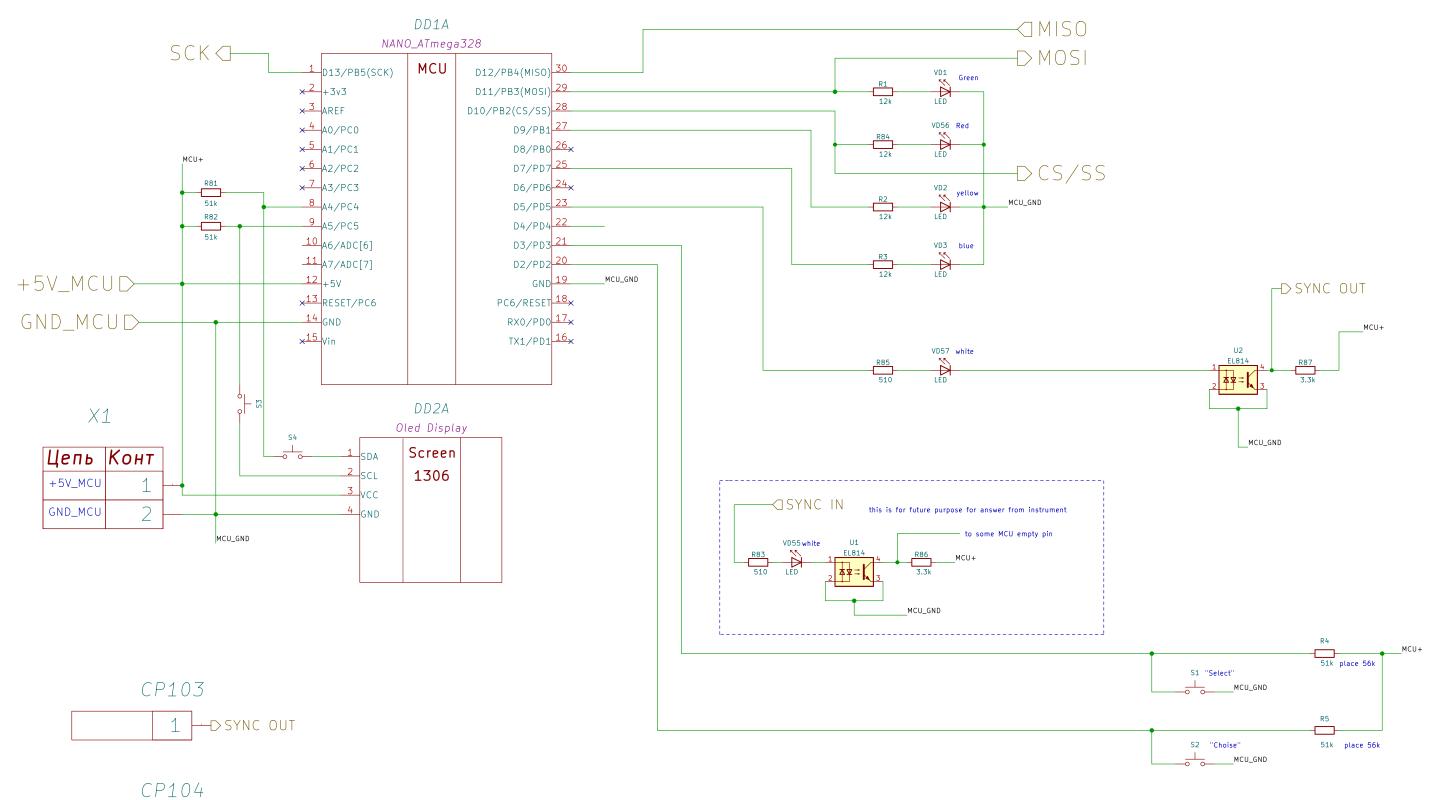


Вилка JST B5B-XH-A

pinsA1-D12 are arDuino names of pins

— SYNC IN

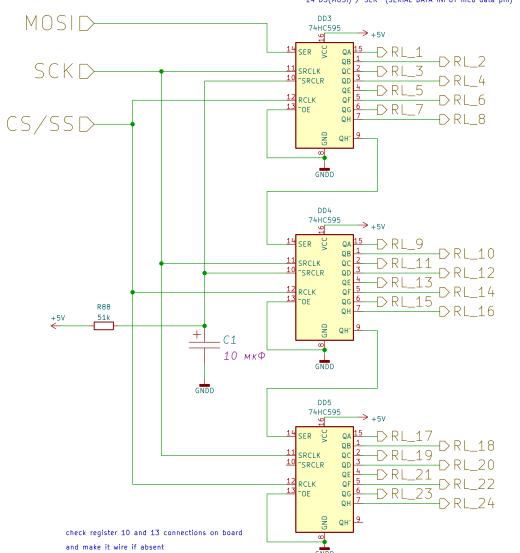
PCO-PC5 PBO-PB4 PDO-PD7 are the ports numbers

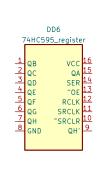


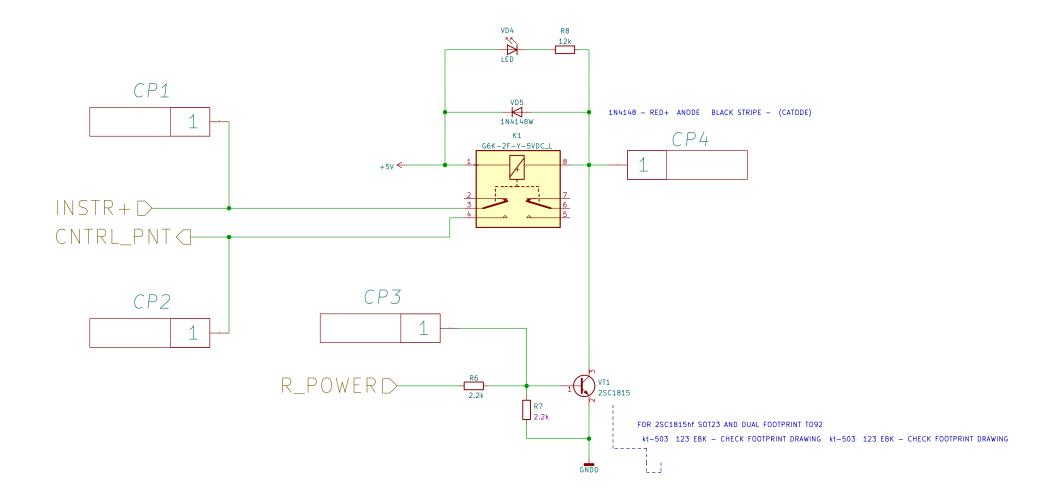
QA-QH 8 output GP

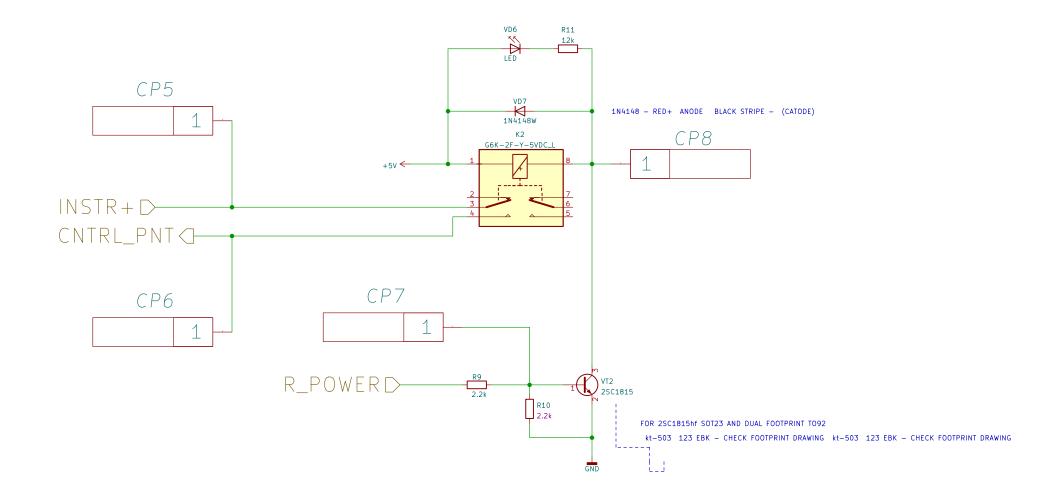
9 Q7' SER DATA OUT(MISO)
10 MR(LOW-ON) (SRCLR shift register cleak)
11 SH-CP (CHIP SELECT)mcu clock pin /SRCLK shift register clock
12 ST-CP(CLK) mcu latch pin /RCLC storage register clock
13 OE (LOW-ON)

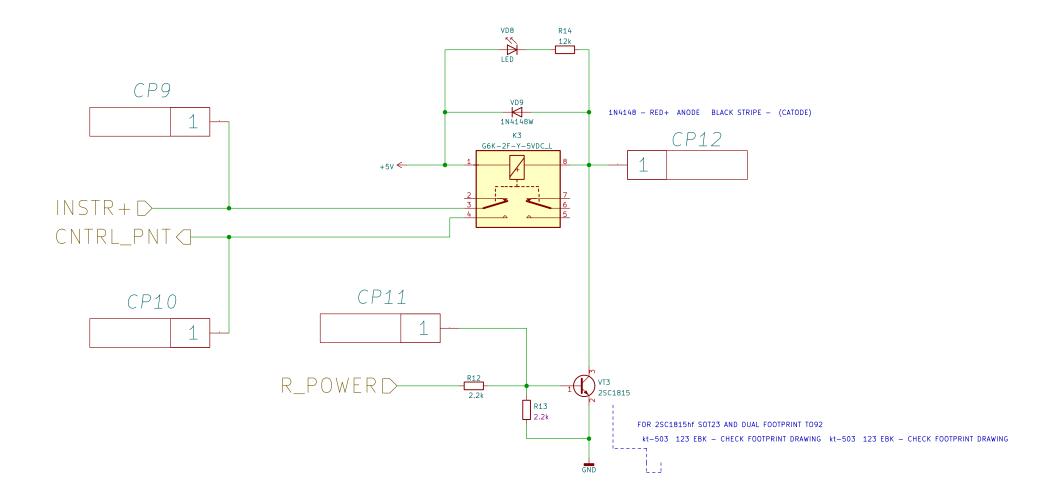
14 DS(MOSI) / SER (SERIAL DATA INPUT mcu data pin)

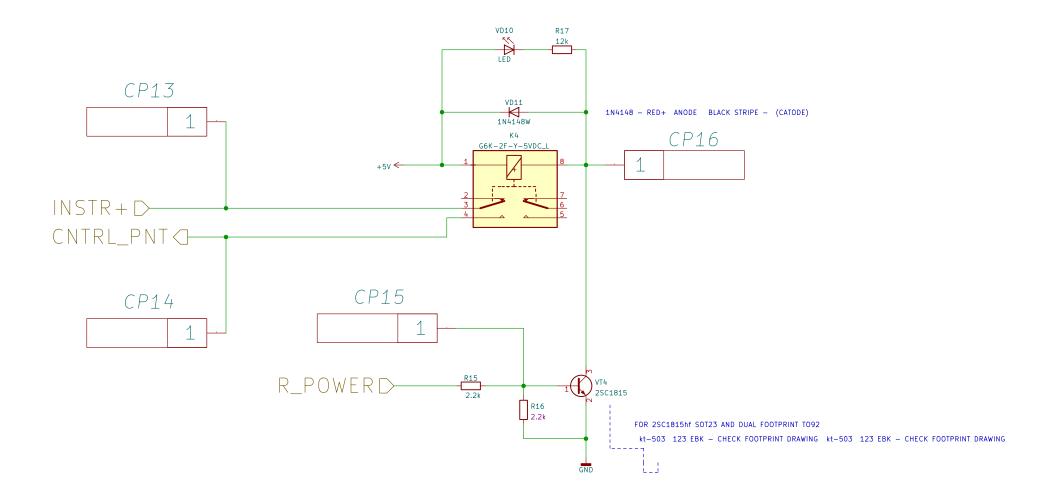


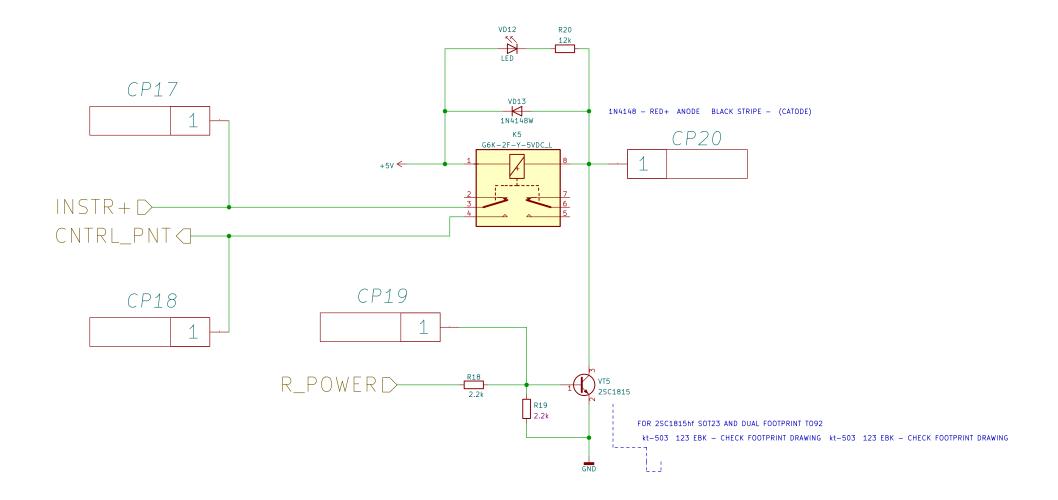


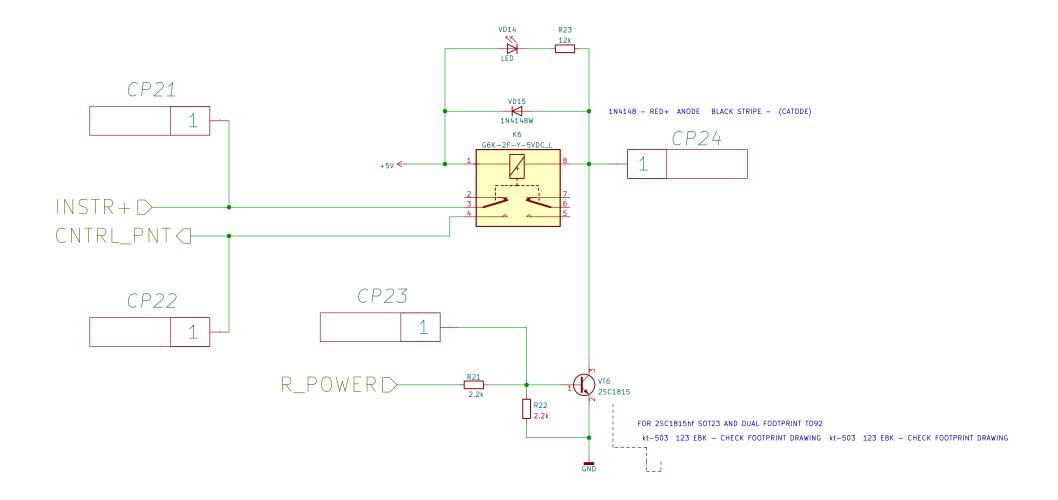


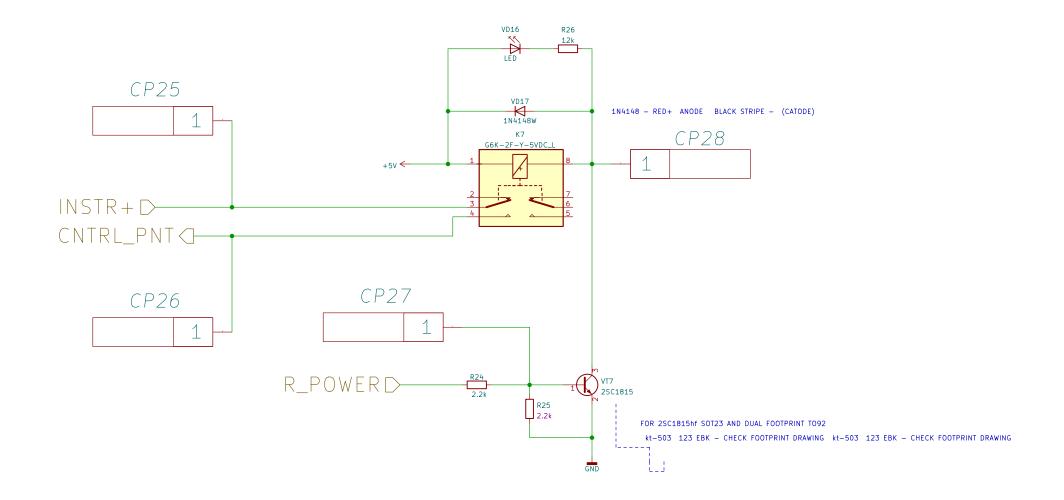


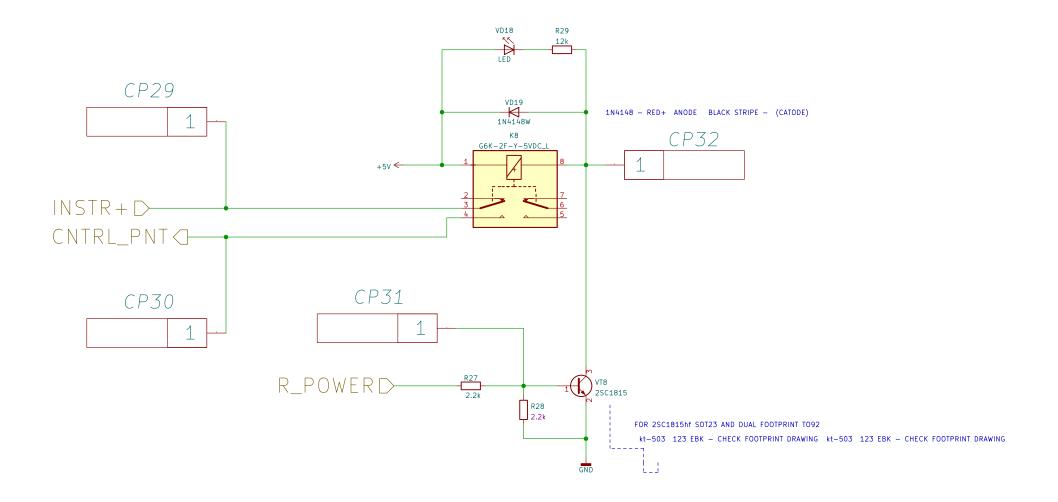


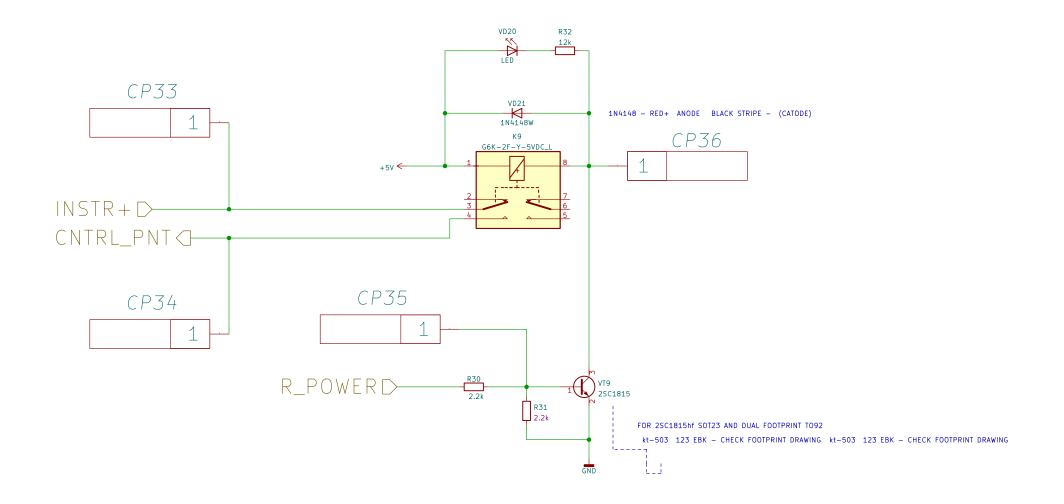


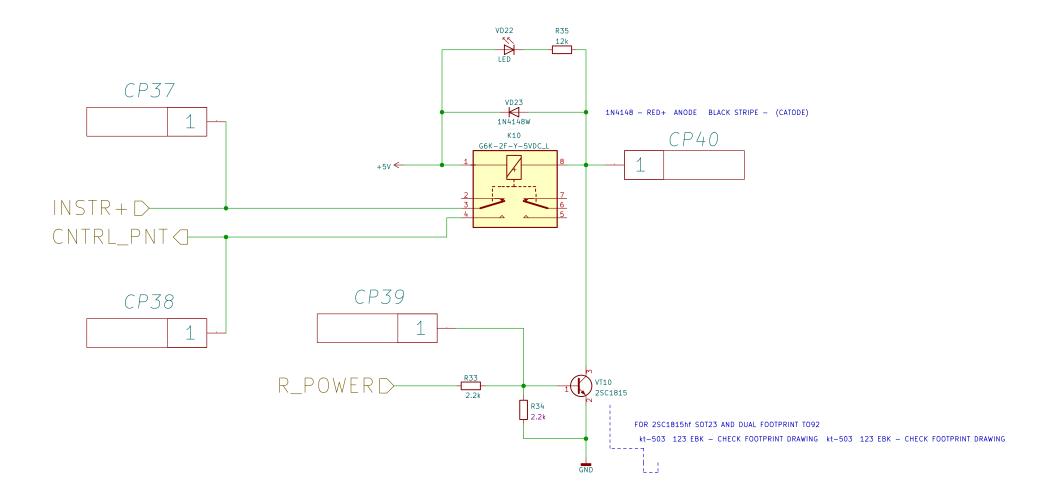


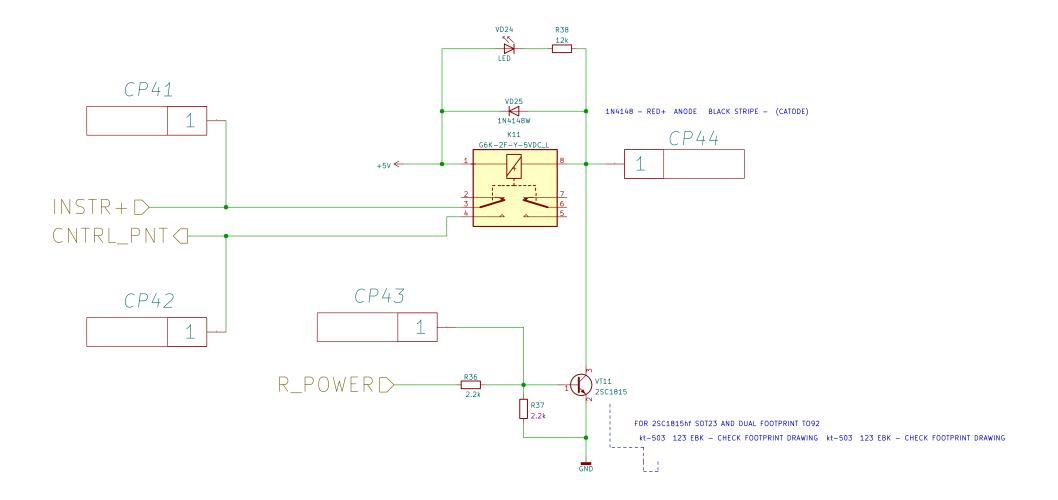


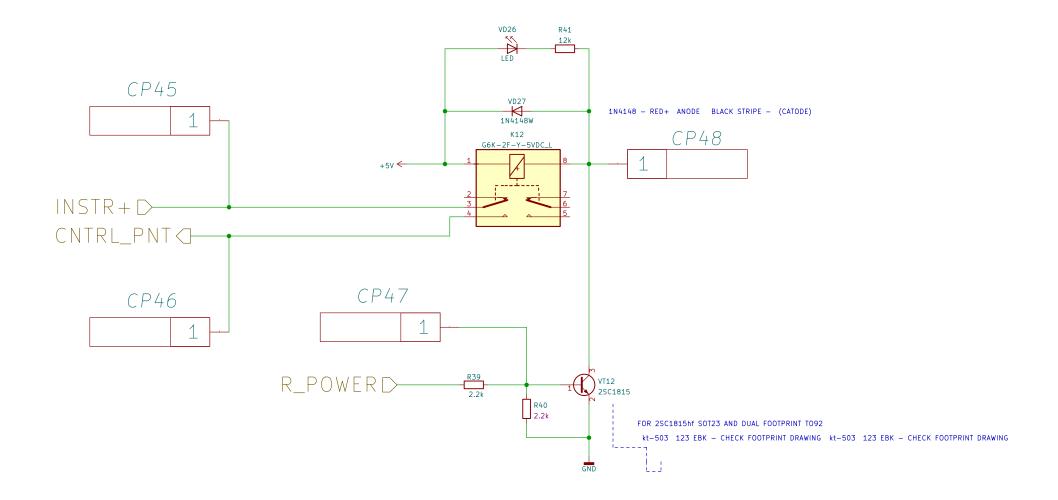


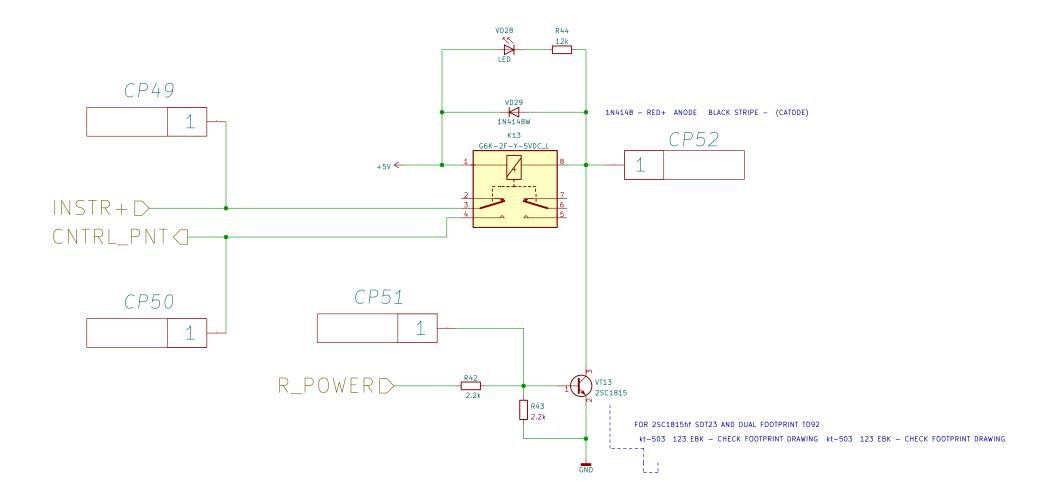


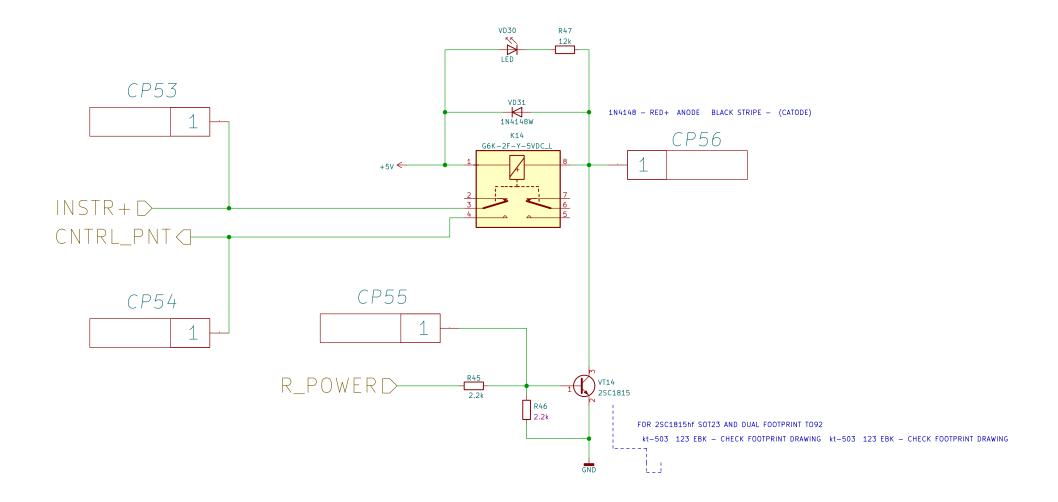


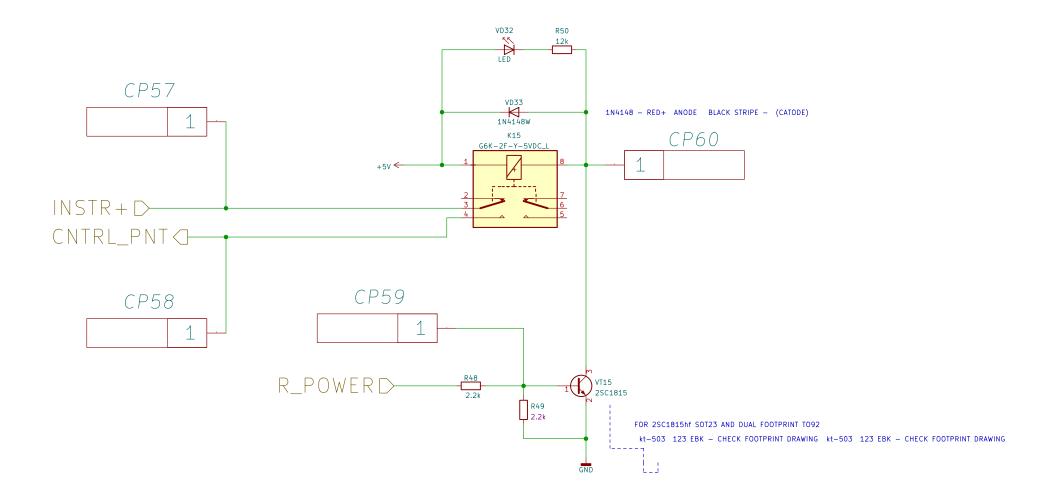


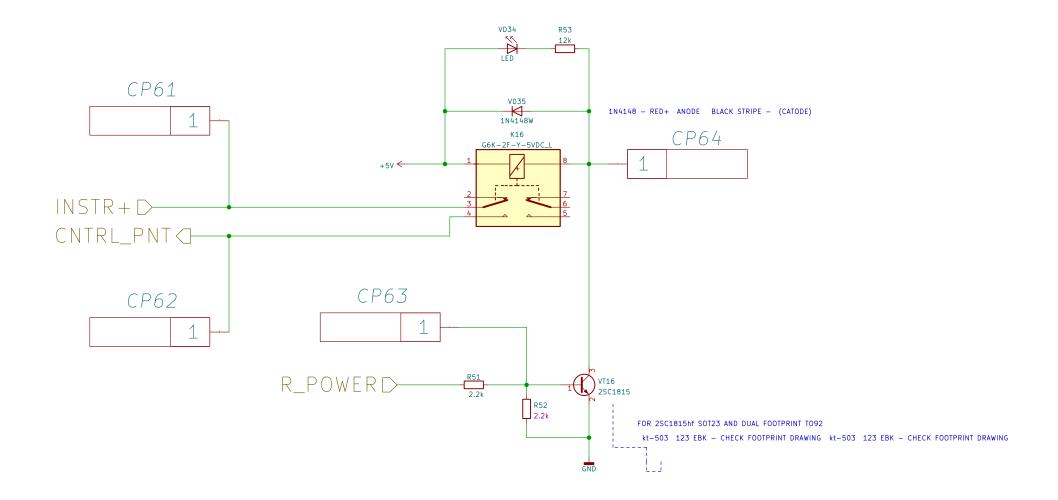


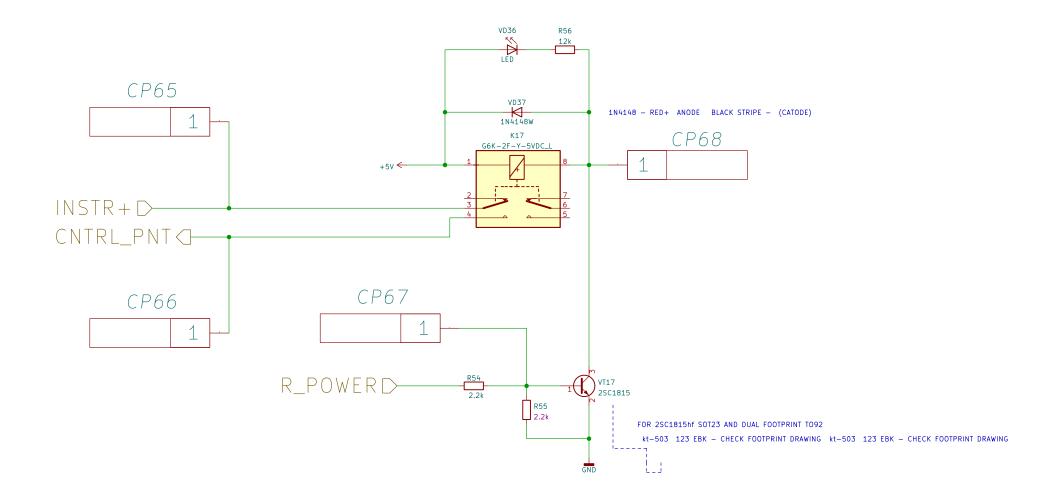


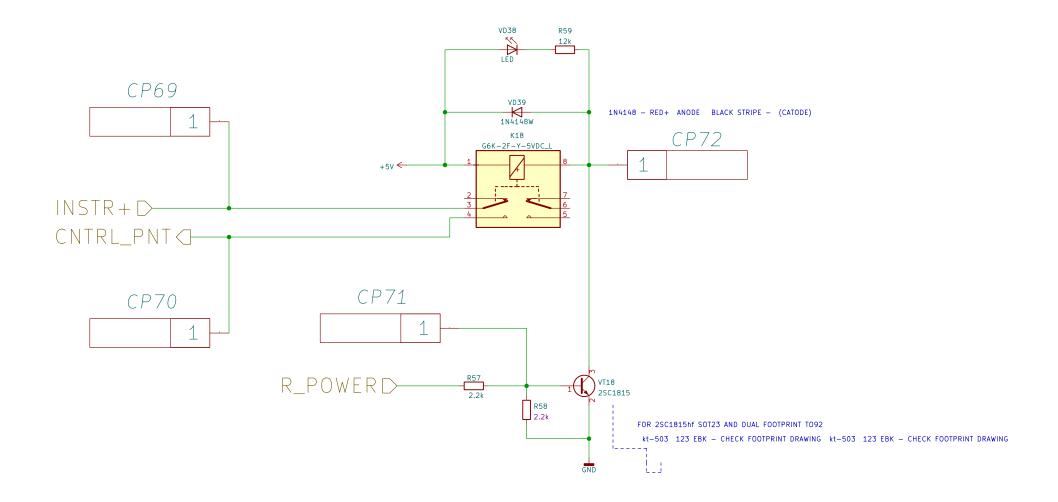


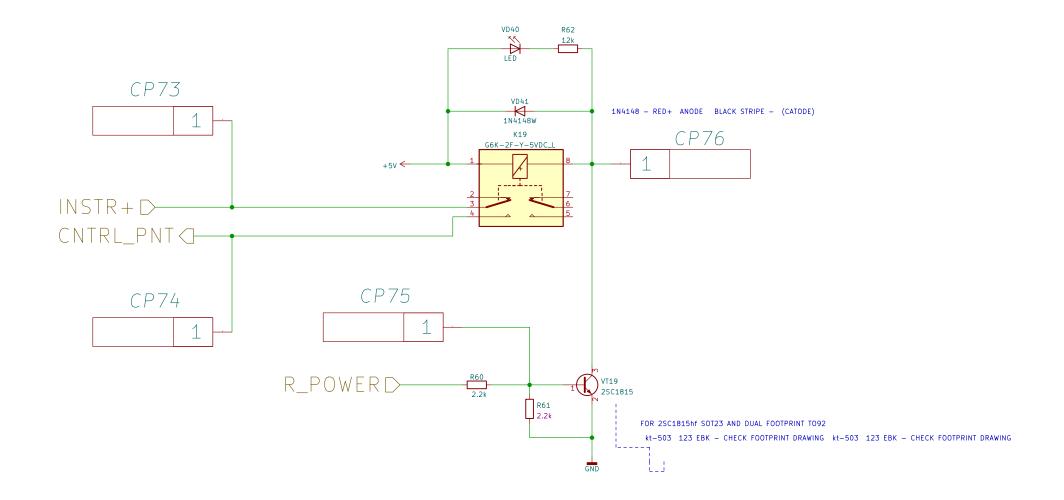


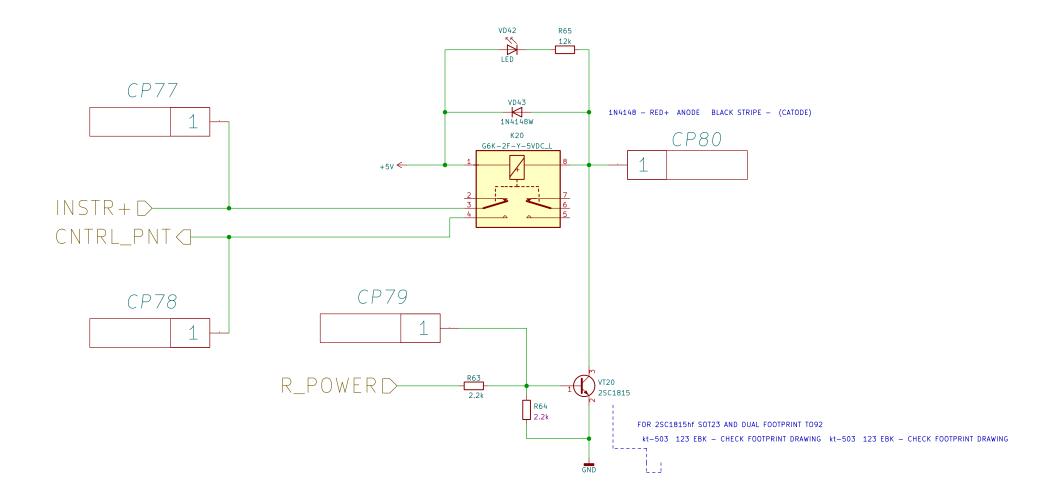


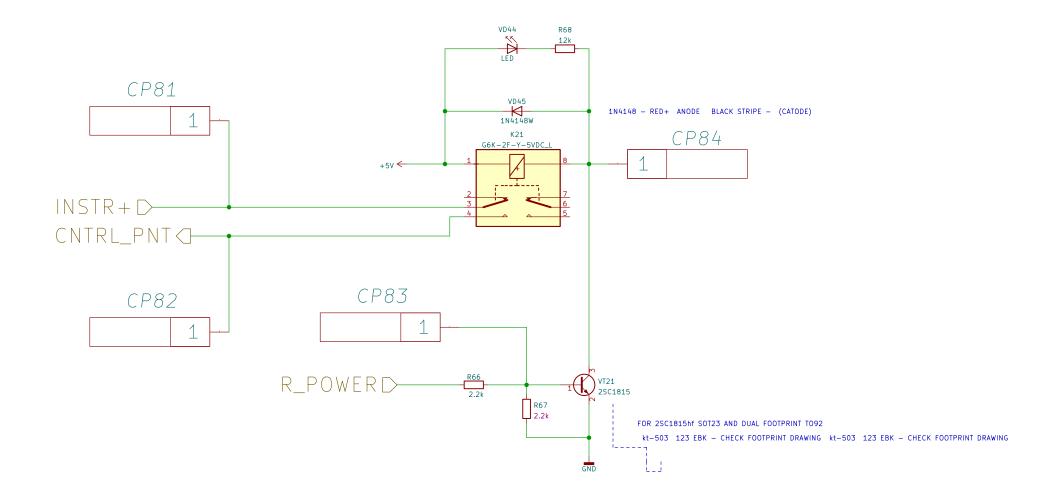


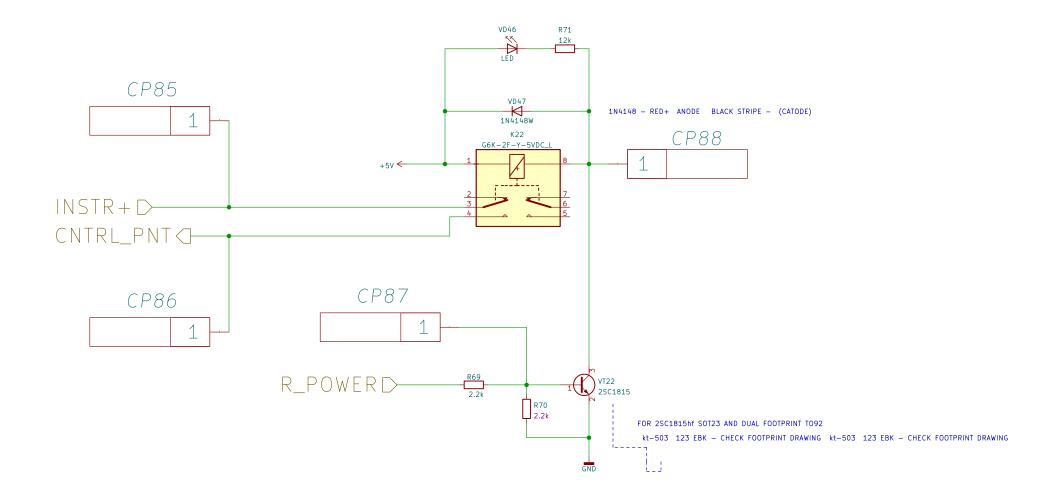


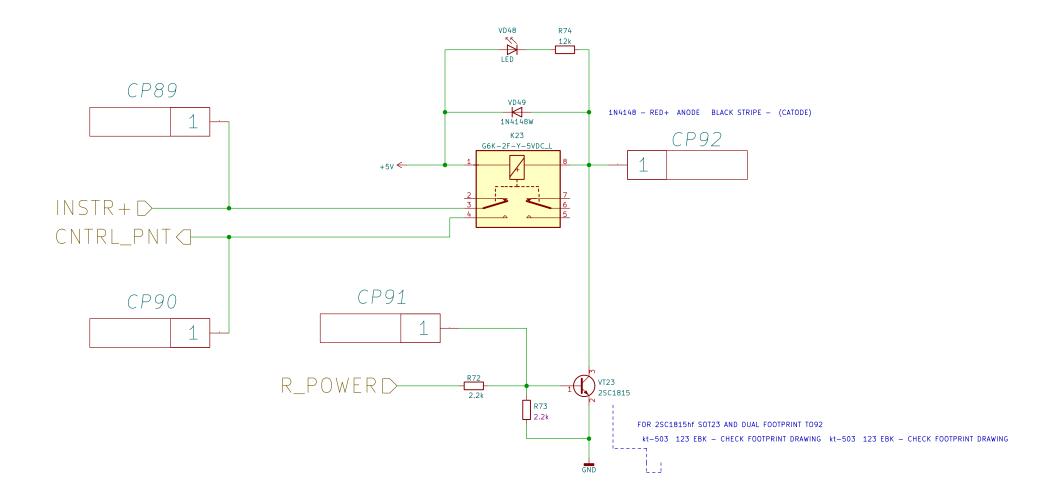


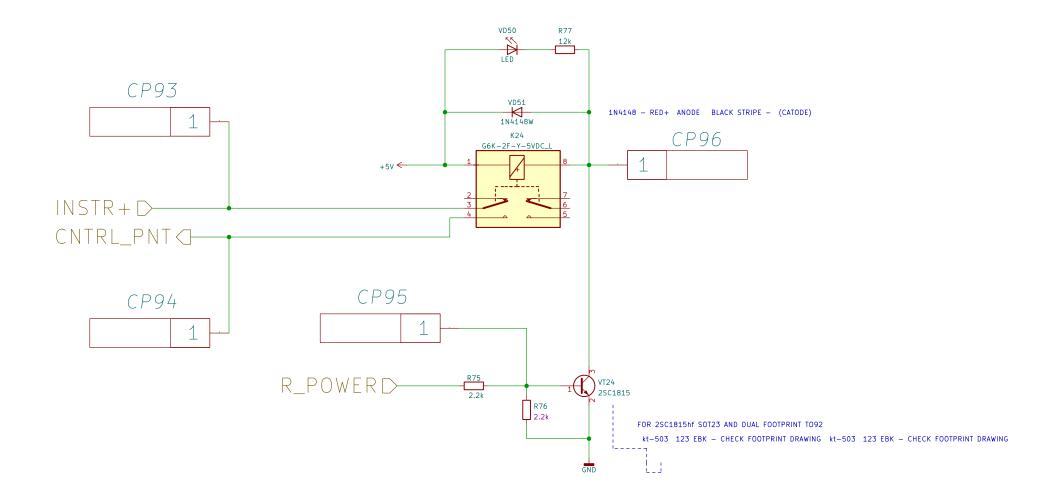


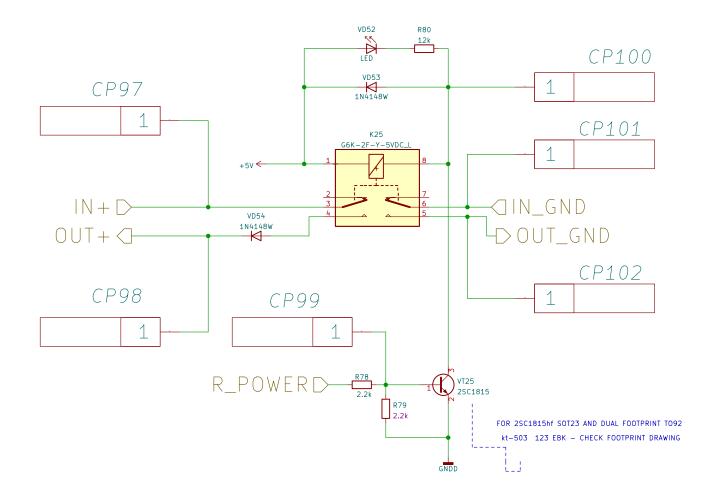


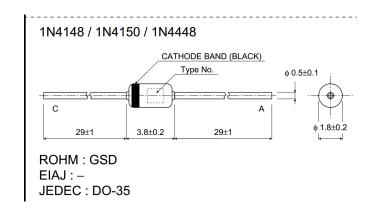


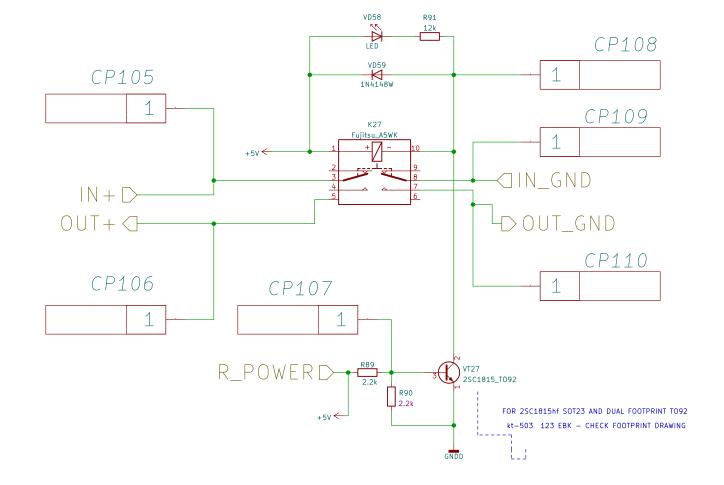


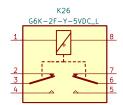


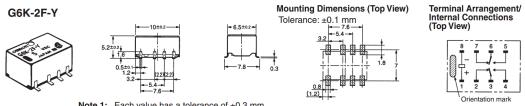












Note 1: Each value has a tolerance of ± 0.3 mm. Note 2: The coplanarity of the terminals is 0.1 mm max.

