

Moschos Antoniadis

Electrical, Electronic & Computer Engineer

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🐙 github/antoniadm

🎯 OBJECTIVE

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🎓 EDUCATION

Dpl in Electrical & Computer Engineering

2021

Aristotle University of Thessaloniki

- ◆ Thesis: Design of analog/mixed signal cells for high speed PLLs in 22nm technology

BSc in Electronic Engineering

2014

Alexander T.E.I. of Thessaloniki

- ◆ Thesis: Design and construction of distance measuring device using ultrasonic waves

🏢 EXPERIENCE

Computer Analyst & Programmer

12/2021-09/2022

Office of Research and Computer Science, Hellenic Army, XVI Mechanized Infantry Division

Field Installation & Support

04/2013-09/2013

Intracom Telecom (Internship)

⚙️ SKILLS & INTERESTS

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|------------------------------|---|
| Design/Simulation | Cadence tools PSpice LTSpice Multisim |
| Programming/Scripting | C C++ Python Java Powershell Bash |
| Miscellaneous | MS Office L ^A T _E X Adobe Photoshop/Premiere UNIX/Linux-based tools |
| Languages | Greek (Native) English (Fluent) German (Beginner) |
| Driving Licences | A B |

⚡ PERSONAL QUALITIES

- | | |
|---------------------------------|---------------------------------------|
| ◆ Morbi auctor lorem non justo. | ◆ Vivamus viverra fermentum felis. |
| ◆ Pellentesque eget lectus. | ◆ Donec eu purus. |
| ◆ Morbi ut dui. | ◆ Pellentesque tristique sodales est. |

📄 CERTIFICATES & COURSES

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|--|------|
| ◆ Multiple Badges - Virtuoso Layout Pro Series, Quantus Extraction, ADE XL etc. | 2022 |
| <i>Cadence Design Systems</i> | |
| ◆ Fundamentals of Accelerated Computing with CUDA C/C++ | 2019 |
| <i>NVIDIA Deep Learning Institute</i> | |
| ◆ 6.002x Circuits and Electronics | 2013 |
| <i>Massachusetts Institute of Technology, edX</i> | |