

2. Combinational Circuit Format

1	2	3	4	5	6 ...
0 GATE	outline	0 IPT	#_of_fout	#_of_fin	inlines
		1 BRCH			
		2 XOR			
		3 OR			
		4 NOR			
		5 NOT			
		6 NAND			
		7 AND			
1 PI	outline	0	#_of_fout	0	
2 FB	outline	1 BRCH	inline		
3 PO	outlin				

Example:

```

1 1 0 1 0
1 2 0 2 0
2 3 1 2
2 4 1 2
1 5 0 1 0
0 6 7 2 2 1 3
2 7 1 6
2 8 1 6
0 9 6 2 2 4 5
2 10 1 9
2 11 1 9
0 12 6 1 2 7 10
0 13 3 1 2 8 11
3 14 7 0 2 12 13
  
```

