## HOW CPU WORKS

In

CPU-central trocessing and

\* brain of the computer.

\* Consist of register, control unit, arithemetic logic unit (ALU).

\* ( Address bus for transmitting address carrying

Data bus for data.

CPU to sgm

· Every action in CPU synach by a clock.

\* clock produce a clock signed in range (MHb-CIHZ)

\* Each cycle herjonns simple oposation

\* CPU alless data from RAM.

\* RAM- Random Access Memory.

Normall data can be accerted only in a particular order from a memory. But in RAM - it can be accessed in any order.

of CPU takes data from the for aument operation.

\* EP How CPU take clater from RAM:

request cuilty an address)

Note:

only if enabled wire is activated.

return data in data bus. is activated.

Dosta in RAM: numbers, addresses, addresses, onstruction for CPU, encoded letters.

Instruction Sets for CPU.

1) Local

2) Add

3) Compare

1) Compare

1) Control Jumps

torrous actions

coorking of CU and ALU.

\* cution the deat a is fetched, it is processed by
the AUL based on the commands or in a

instructions from w.

\* There are also Flags.

These flags are just cuires that become low or high based on some condition. They do not hansed any outful but to show any ament state.

Hard distress

\* when the power is twoord off the entire clocka
in RAA RAM is lost since it is an valable
memory.

\* But so a hard distre is used for clus.

\* The mount of the own in above the
magnetic dish is very fast but mot as fail
as computation done in CPU.

\* 50 just duta is loaded to RAM from these CPU access it.