# $\begin{array}{c} {\rm ECE~2200L} \\ {\rm Introduction~to~Microelectronics~Circuits} \\ {\rm Laboratory} \end{array}$

Experiment 2
Diode I-V Characteristics

Report

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# Objective

To study the current-voltage relationship of semiconductor PN diodes, and to determine the reverse saturation current and the diode ideality factor from the I-V characteristic plot of the diodes.

# Result

### 1N4001 Silicon PN diode

The following is the experimental data obtained from 1N4001 Silicon PN diode.

$V_d$ (V)	$I_d$ (A)	$\frac{V_d}{V_T}$	$ln(I_d)$ (A)
-10	$-2.000 \times 10^{-9}$		
-8	$-2.000 \times 10^{-9}$		
-6	$-2.000 \times 10^{-9}$	N/A	
-4	$-2.000 \times 10^{-9}$		
-2	$-1.000 \times 10^{-9}$		
0.3	$9.000 \times 10^{-7}$	11.53846	-13.9209
0.4	$1.440 \times 10^{-5}$	15.38462	-11.1483
0.5	$1.910 \times 10^{-4}$	19.23077	-8.56324
0.6	$1.660 \times 10^{-3}$	23.07692	-6.40094
0.7	$1.407 \times 10^{-2}$	26.92308	-4.26371



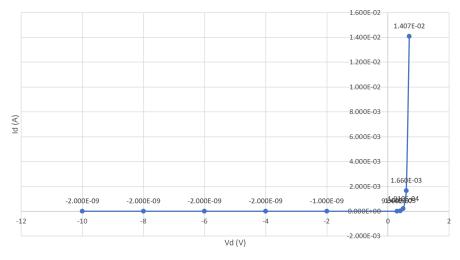


Figure 1: IV chart of 1N4001 Silicon PN diode

# Vd/VT vs. In(Id) for Silicon Diode

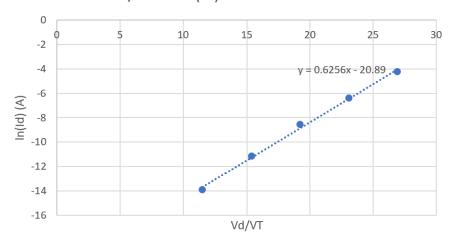


Figure 2:  $\frac{V_d}{V_T}$  vs  $\ln(I_d)$  chart of 1N4001 Silicon PN diode

From the above chart, the ideality of the diode  $\eta$  can be derived by taking the reciprocal of the slope of the line of best fit and the saturation current  $I_s$  can be derived by taking the inverse natural logarithm of the intercept of the line of best fit.

 $\begin{array}{ccc} {\rm Slope} & 0.6256 \\ {\rm Intercept} & -20.89 \, {\rm A} \\ & \eta & 1.598465 \\ & I_s & 0.846 \, {\rm nA} \end{array}$ 

# 1N270 Germanium PN diode

The following is the experimental data obtained from 1N270 Germanium PN diode.

$V_d$ (V)	$I_d$ (A)	$\frac{V_d}{V_T}$	$ln(I_d)$ (A)
-10	$-1.270 \times 10^{-6}$		
-8	$-1.093 \times 10^{-6}$		
-6	$-9.750 \times 10^{-7}$	N/A	
-4	$-8.740 \times 10^{-7}$		
-2	$-7.260 \times 10^{-7}$		
0.1	$1.670 \times 10^{-5}$	3.846154	-11.0001
0.2	$2.220 \times 10^{-4}$	7.692308	-8.41283
0.3	$1.570 \times 10^{-3}$	11.53846	-6.45668
0.4	$7.550 \times 10^{-3}$	15.38462	-4.88621



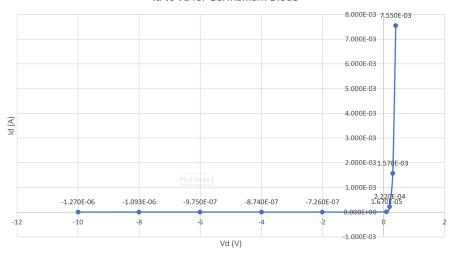


Figure 3: IV chart of 1N270 Germanium PN diode

# Vd/VT vs. In(Id) for Germanium Diode

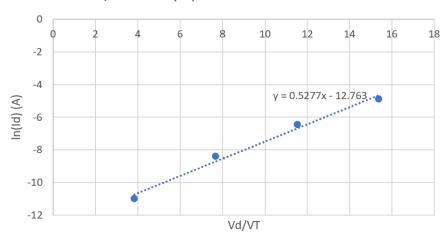


Figure 4:  $\frac{V_d}{V_T}$  vs  $ln(I_d)$  chart of 1N270 Germanium PN diode

From the above chart, the ideality of the diode  $\eta$  can be derived by taking the reciprocal of the slope of the line of best fit and the saturation current  $I_s$  can be derived by taking the inverse natural logarithm of the intercept of the line of best fit.

 $\begin{array}{ccc} {\rm Slope} & 0.5277 \\ {\rm Intercept} & -12.763 \, {\rm A} \\ & \eta & 1.8950161 \\ & I_s & 2.86 \, {\rm \mu A} \end{array}$ 

### Conclusion

The ideality factor of both Si and Ge diode derived from the  $\frac{V_d}{V_T}$  vs  $ln(I_d)$  plots are higher than expected. The increased temperature of diode when operating at relatively high voltage may have contributed to the higher value by increasing the thermal voltage, which is directly proportional to the calculation of ideality factor as it decreases the coefficient of the independant variable of the line derived from the  $\frac{V_d}{V_T}$  vs  $ln(I_d)$  plot, which is used to derive the ideality factor by taking the reciprocal of the slope, thus the decrease in slope resulted in an increase of ideality value derived.