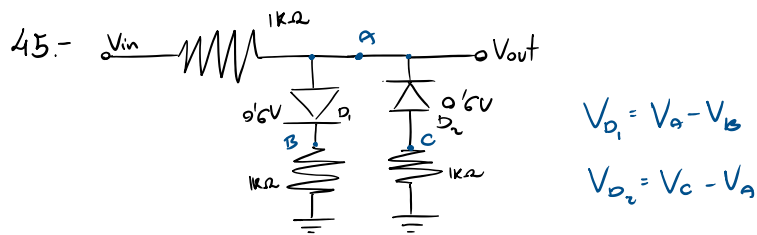
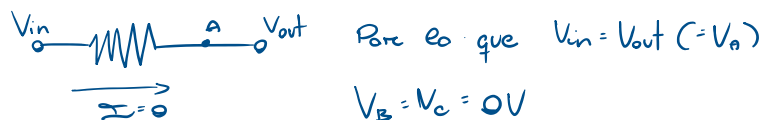


Problemas

domingo, 6 de febrero de 2022 17:38



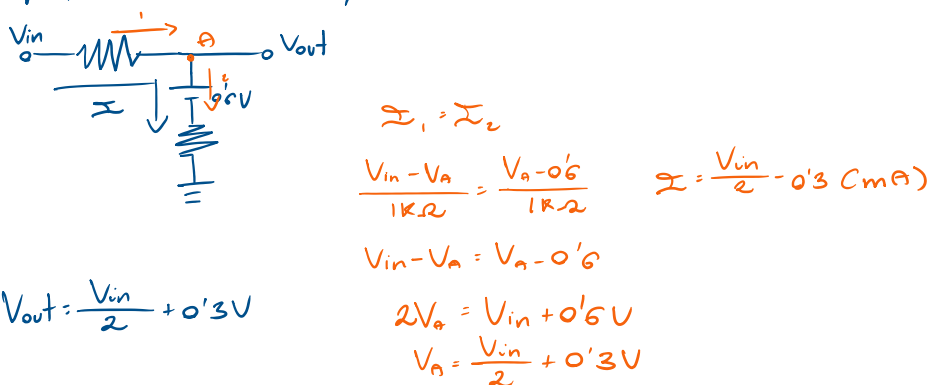
Supongamos ambos diodos en OFF:



Esto ocurrirá cuando:

- $V_A - 0 = V_A < 0.6V$
- $0 - V_A = -V_A < 0.6V \Rightarrow V_A > -0.6V$

Supongamos D_1 ON y D_2 OFF



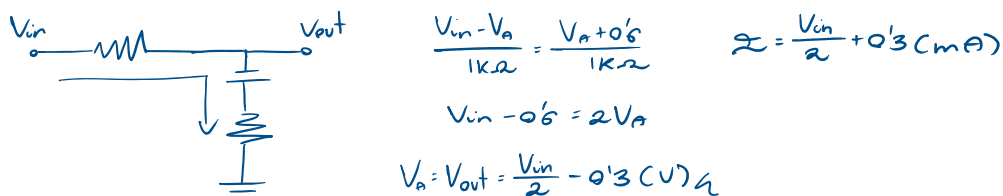
Esto ocurrirá si $(V_C = I \cdot R = \frac{V_{in}}{2} - 0.3)$

$$I > 0 \Rightarrow \frac{V_{in}}{2} - 0.3 > 0 \Rightarrow \boxed{V_{in} > 0.6}$$

$$V_{D2} = V_C - V_A = -\frac{V_{in}}{2} - 0.3 < 0.6$$

$$V_{in} > -1.8V$$

Si D_1 OFF y D_2 ON

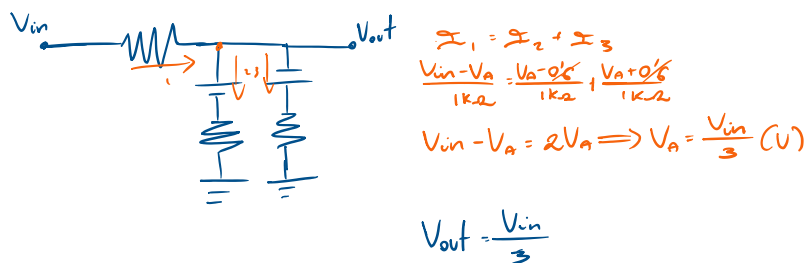


Esto ocurre si:

$$V_{D1} = V_A - V_C = \frac{V_{in}}{2} - 0.3 < 0.6 \Rightarrow V_{in} < 1.8V$$

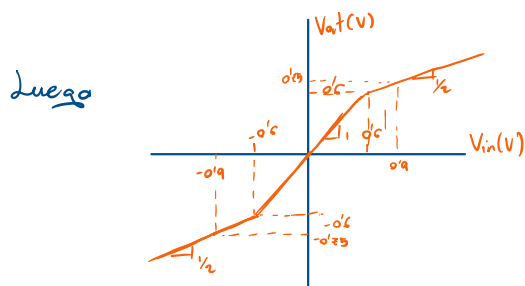
$$I < 0 \Rightarrow \frac{V_{in}}{2} + 0.3 < 0 \Rightarrow \boxed{V_{in} < -0.6V}$$

Suponemos ambos en ON:



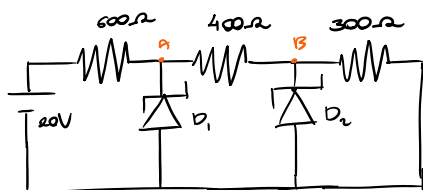
Esto ocurrirá si

$I_2 > 0 \Rightarrow \frac{V_{in}}{3} - 0.6 > 0 \Rightarrow \boxed{V_{in} > 1.8}$
 $I_3 < 0 \Rightarrow \frac{V_{in}}{3} + 0.6 < 0 \Rightarrow \boxed{V_{in} < -1.8}$



$$V_{out} = \begin{cases} \frac{V_{in}}{2} - 0.3 & V_{in} < -0.6 (V) \\ V_{in} & -0.6 \leq V_{in} \leq 0.6 (V) \\ \frac{V_{in}}{2} + 0.3 & 0.6 < V_{in} (V) \end{cases}$$

46.-

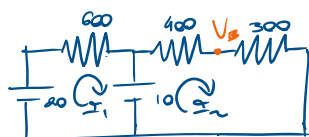


$V_{D1} = V_{D2} = 0.6V$
 $V_{Z1} = 10V \quad V_{Z2} = 8V$

Supongamos D_1 y D_2 en OFF

$I = \frac{20V}{1.3k\Omega} = 15.38 \text{ mA} \Rightarrow V_{D1} = -V_A = -(20 - IR_1) = IR_1 - 20 = -13.85V < -10V = -V_{Z1} \Rightarrow \text{Contradicción}$

Supongo D_1 ON (Invertido) y D_2 OFF



$I_1: 20 - 10 = I_1(600)$
 $10 = 600I_1 \Rightarrow I_1 = 16.67 \text{ mA}$

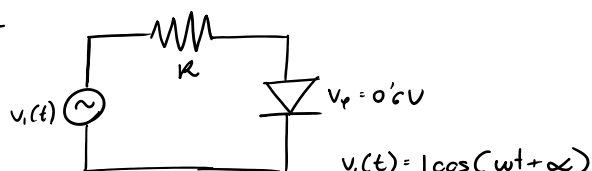
$I_2: 10 = 300I_2 \Rightarrow I_2 = 14.28 \text{ mA}$

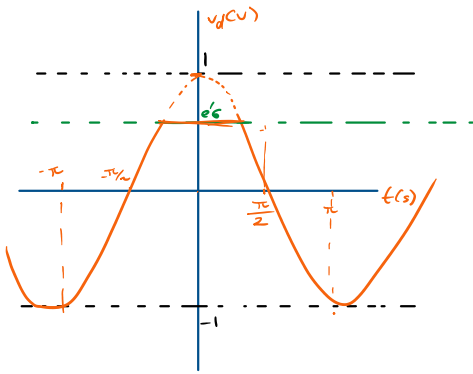
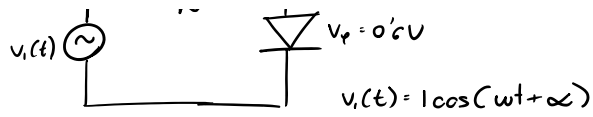
Comprobamos que: $V_{D2} = -V_B = -(20 - I_1 600 - I_2 400) = -4.29V > -8 = -V_{Z2} \Rightarrow \text{Correcto}$

$I_{D1} < 0 \Rightarrow I_2 - I_1 = -2.39 \text{ mA} < 0 \Rightarrow \text{Correcto}$

$I_{R1} = I_1 = 16.67 \text{ mA}, I_{R2} = I_{R3} = I_2 = 14.28 \text{ mA}$

47.-



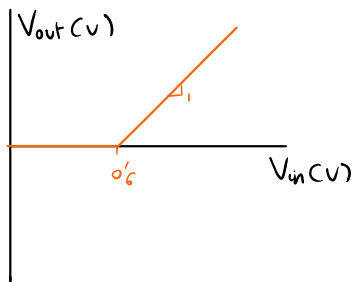


b) Característica de transferencia:

$$V_{out} = V_K$$

$$\text{Si } V_{in} < 0.6V \Rightarrow V_{out} = \overset{0}{I} \cdot Z_K = 0V \Rightarrow v_{out}(t) = 0V$$

$$V_{in} \geq 0.6V \Rightarrow V_{out} = I \cdot Z_K = \frac{V_{in} - 0.6}{Z_R} \cdot Z_K = V_{in} - 0.6V \Rightarrow v_{out}(t) = v_{in}(t) - 0.6V$$



$$V_{out} = V_D$$

$$\text{Si } V_{in} < 0.6 \Rightarrow V_D = V_{in} = V_{out}$$

$$V_{in} \geq 0.6 \Rightarrow V_D = V_{out} = 0.6V$$

