



IEEE SIT SB

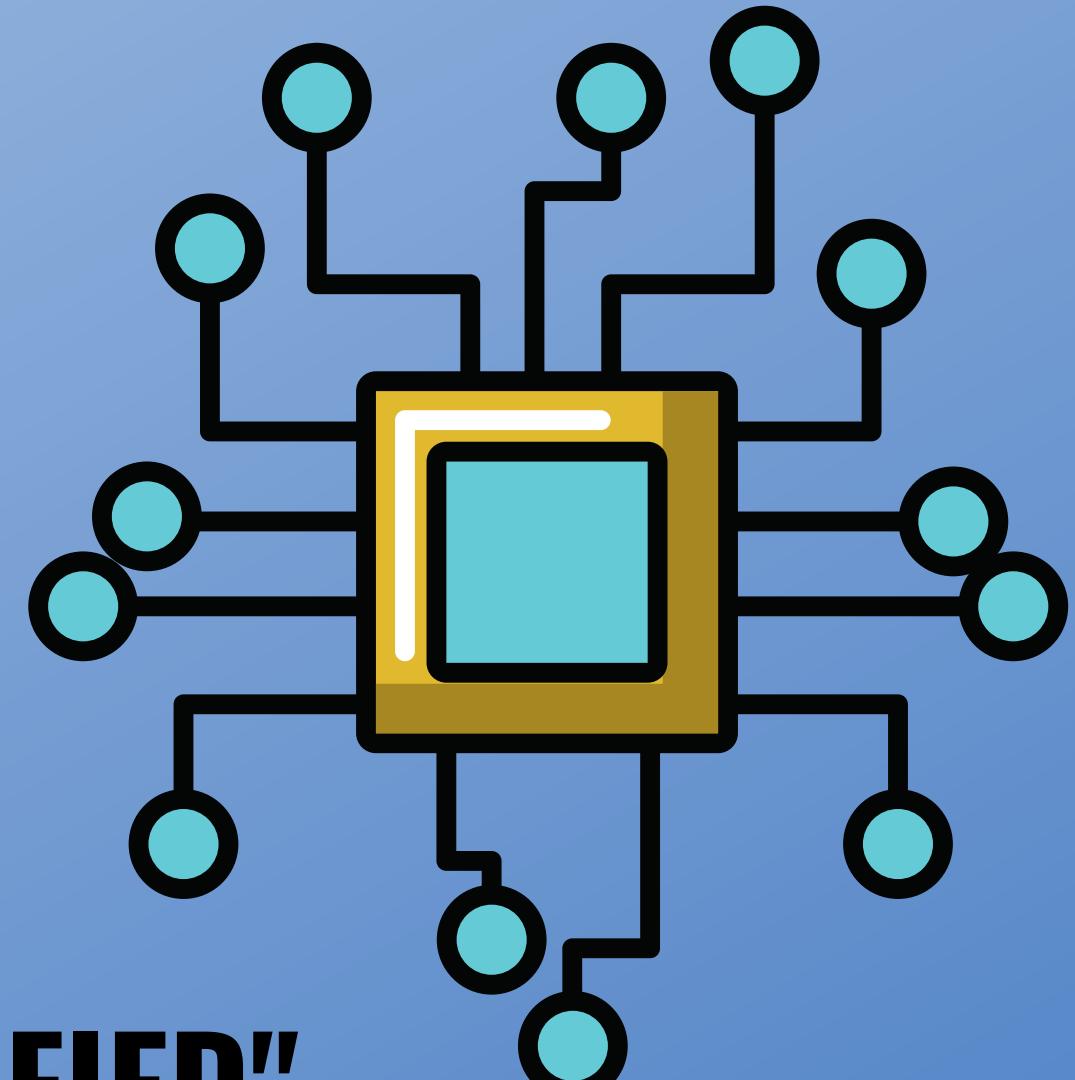
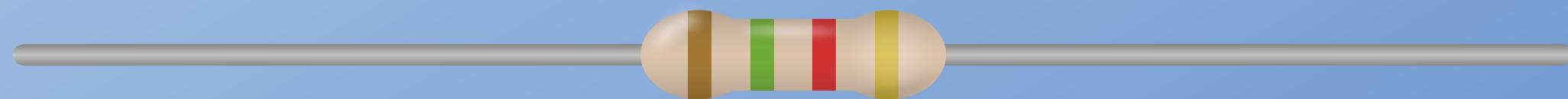
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" LET PEACE BE AMPLIFIED, WORLD BE RECTIFIED"

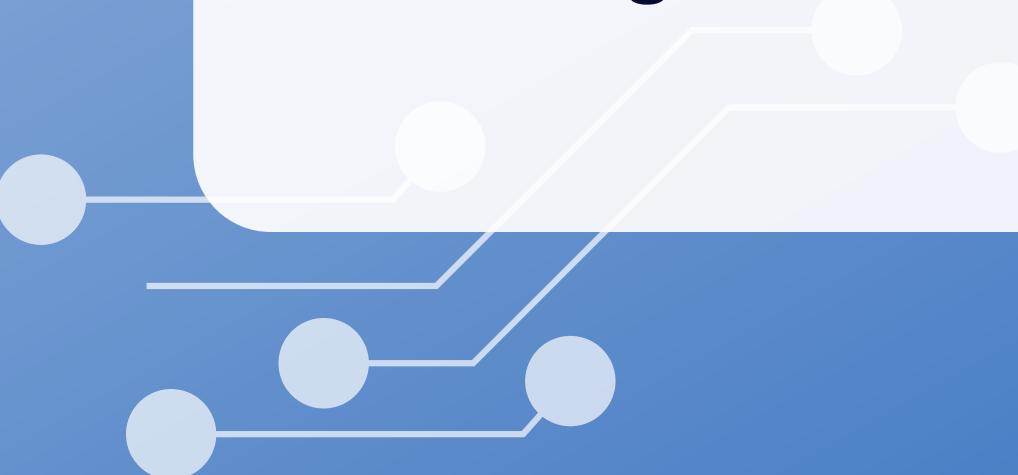
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RECTIFIER ?!!

Creation, demonstration, and execution of the critical attenuation component of emerging skills are termed Engineering. The quest of finding the bug is zeal for core people, here is the third dimension of infinite possibilities. Crack the code to fix the bug.



The FLOW!!

Round 1: Written test

The event consists of two categories:

- 1st and 2nd years(group one).
- 3rd and 4th years(group two).
- The team size should be **two** of the same year of study.

Round 1 continued...

- In the preliminary round, participants are given an MCQ test that contains **25 questions** regarding basic concepts of electronics, electrical, and aptitude concepts that have to be solved in **30 minutes**.
- The top 5-7 participants will be selected for the next round. (which will be decided on the day of the event).

Final round: Circuit debugging

- Final round will involve circuit debugging for 1st and 2nd years. Those who debug the given circuit and obtain the desired output **within less time** will be declared winners.
- For the 3rd and 4th years, participants should design a circuit and get the **desired results** and the winners will be declared based on the time taken to finish the round.

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Judging Criteria: Ability to get the desired output within minimum time and to answer the questions which will be asked by the judges.

Eligibility:

Open to all the branches.

Maximum team size: 2

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Thank You

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