PMod Interface

Single Width PMod				Double Width PMod				I2C Interface			
Host		**Peripheral**		**Host**		**Peripheral**					
1		1		1	7	7	1				
2		2		2	8	8	2	1	2	SCL	SCL
3		3		3	9	9	3	3	4	SDA	SDA
4		4		4	10	10	4	5	6	GND	GND
5		5		5	11	11	5	7	8	VCC	VCC
6		6		6	12	12	6				

Host

1
2
3
4
5
6

Peripheral

1
2
3
4
5
6

0.40"

0.15"

Right angle female connector

Right angle male connector

Fig 1: Standard Six-Pin Connector Placement

Host

1 7
2 8
3 9
4 10
5 11
6 12

Peripheral

7 1
8 2
9 3
10 4
11 5
12 6

0.45"

0.20"

Right angle female connector

Right angle male connector

Fig 2: Standard Twelve-Pin Connector Placement

I^2C Module PCB

0.80"

0.30"

1 2
3 4
5 6
7 8

0.20"

Right angle male connector

Fig 3: Standard Module Dimensions

Pmod Interface Type 1 (GPIO)			Pmod Interface Type 2 (SPI)			Pmod Interface Type 2A (expanded SPI)			Pmod Interface Type 3 (UART)			Pmod Interface Type 4 (UART)			Pmod Interface Type 4A (expanded UART)			Pmod Interface Type 5 (H-Bridge)			Pmod Interface Type 6 (dual H-Bridge)		
Pin	Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction	Pin	Signal	Direction
1	IO1	In/Out	1	SS	Out	1	SS	Out	1	CTS	Out	1	CTS	In	1	CTS	In	1	DIR	Out	1	DIR1	Out
2	IO2	In/Out	2	MOSI	Out	2	MOSI	Out	2	RTS	In	2	TXD	Out	2	TXD	Out	2	EN	Out	2	EN1	Out
3	IO3	In/Out	3	MISO	In	3	MISO	In	3	RXD	In	3	RXD	In	3	RXD	In	3	SA	In	3	DIR2	Out
4	IO4	In/Out	4	SCK	Out	4	SCK	Out	4	TXD	Out	4	RTS	Out	4	RTS	Out	4	SB	In	4	EN2	Out
5	GND		5	GND		5	GND		5	GND		5	GND		5	GND		5	GND		5	GND	
6	VCC		6	VCC		6	VCC		6	VCC		6	VCC		6	VCC		6	VCC		6	VCC	
						7	INT	In							7	INT	In						
						8	RESET	Out							8	RESET	Out						
						9	N/S	N/S							9	N/S	N/S						
						10	N/S	N/S							10	N/S	N/S						
						11	GND								11	GND							
						12	VCC								12	VCC							

			SS - Slave Select. Active low to enable slave device						CTS - Permission for peripheral to send data to host			CTS - Device will only transmit when this signal is asserted			CTS - Device will only transmit when this signal is asserted			DIR - Motor direction			DIR1 - Motor/Phase 1 direction, active high		
			MOSI - Master Out Slave In. Data from master to slave						RTS - Request from peripheral to send data to host			RTS - Device is ready to receive data			RTS - Device is ready to receive data			EN - Motor enable, active high			EN1 - Motor/Phase 1 enable		
			MISO - Master In Slave Out. Data from slave to master						RXD - Data from peripheral to host			RXD - Data from peripheral to host			RXD - Data from peripheral to host			SA - feedback sense A			DIR2 - Motor/Phase 2 direction, active high		
			SCK - Serial clock. Data clock from master to slave									TXD - Data from host to peripheral			TXD - Data from host to peripheral			SB - feedback sense B			EN2 - Motor/Phase 2 enable		
															INT - Interrupt signal from peripheral to host								
															RESET - Reset signal for host to reset peripheral								
															N/S - Not-specified depending on the module these pins may be unconnected or additional module specific inputs or outputs.								