ANUJA SHAH

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EDUCATION

University of California, Los Angeles, School of Law — Los Angeles, CA
J.D. Candidate — May 2027
Specialization: Technology Law

University of Southern California, Viterbi School of Engineering, — Los Angeles, CA
M.S. in Electrical Engineering — Machine Learning and Computer Architecture — August 2024

B.S. in Electrical & Computer Engineering, Minor in Legal Studies — May 2023
Honors: Cum Laude
Viterbi Dean's List '20 – '23
Awards: IEEE AP-S Research Grant (USRS)
USC IEEE Hack-IoT Prize (1st Place)
Activities: The Trevor Project Suicide Hotline for LGBTQ+ Youth, *Volunteer*
USC Solar Car Team, *Member*

EXPERIENCE

UltraSense Systems — San Jose, CA
Patent Intern — Summer 2024
Craft independent and dependent claim set out of ideas. Draft provisional patents to speed up patent filing.

USC Institute for Technology & Medical Systems Innovation — Los Angeles, CA
Student Researcher — Aug 2022 – Apr 2024
Formulated algorithm for region-specific multiresolution voxelization to improve efficiency in terms of computational resources and to minimize interpolation error. Automated modification of .model files with Python scripting to reduce process time by 30%. Generated hippocampal models with differing stimulating electrode positions on suprapyramidal, crest, and infrapyramidal regions for simulation. Investigated, using a computational model, desired characteristics of magneto-electric nanoparticles, such as size, quantity, and density, needed to create magnetic field which generated the necessary electric field for stimulating optic nerve to induce regeneration wirelessly and non-invasively.

USC Viterbi School of Engineering — Los Angeles, CA
Freshman Academy Coach — Fall 2022
Facilitated and supported faculty with class projects and lectures as a teaching assistant. Mentored first-year engineering students and fostered a community among students.

UltraSense Systems — San Jose, CA
Electrical Engineering Intern — Summer 2022
Designed and 3D printed demo case for new sensor chip with Fusion360. Constructed GUI for demo using Bootstrap library, so users could customize haptic, LED, and speaker feedback. Tested logistic regression binary classification on registering a touch as press or non-press and found few Type II errors and no Type I errors by partitioning various training and test sets with scikit-learn.

zSpace — Sunnyvale, CA
Electrical Engineering Intern — Summer 2020
Created a circuit schematic, through reverse-engineering, of device employed in testing durability and longevity of different materials for passive 3D glasses. Improved circuit schematic by adding safety features, such as an emergency stop button, and utilized schematic to build additional devices for testing products.

SKILLS

C/C++ | Python | JavaScript | MATLAB | TensorFlow | PyTorch | Azure | Verilog | Bootstrap | CSS | HTML | KiCad | Fusion360 | Soldering | Microsoft Office | Conversational Gujarati