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Anuj Kalia

Education

2013–present **Ph.D. student in Computer Science**, *Carnegie Mellon University*.
Adviser: Prof. David Andersen

2009–2013 **B.Tech. in Computer Science and Engineering**, *IIT-Delhi*.
GPA – 9.75, 2nd in class

Conference publications

OSDI 2016 **Anuj Kalia**, Michael Kaminsky, and David G. Andersen. FaSST: Fast, Scalable, and Simple Distributed Transactions with Two-Sided (RDMA) Datagram RPCs. *Operating Systems Design and Implementation, November 2016*.

ATC 2016 **Anuj Kalia**, Michael Kaminsky, David G. Andersen. Design Guidelines for High Performance RDMA Systems. *Annual Technical Conference, June 2016*. **Best Student Paper Award**. Appears as invited article in USENIX ;login:.

ISCA 2015 Sheng Li, Hyeontaek Lim, Victor Lee, Jung Ho Ahn, **Anuj Kalia**, Michael Kaminsky, David Andersen, Seongil O, Sukhan Lee, Pradeep Dubey. Architecting to Achieve a Billion Requests Per Second Throughput on a Single Key-Value Store Server Platform. *International Synposium on Computer Architecture, June 2015*.

NSDI 2015 **Anuj Kalia**, Dong Zhou, Michael Kaminsky, David G. Andersen. Raising the Bar for Using GPUs in Software Packet Processing. *Networked Systems Design and Implementation, May 2015*.

SIGCOMM 2014 **Anuj Kalia**, Michael Kaminsky, David G. Andersen. Using RDMA Efficiently for Key-Value Services. *ACM SIGCOMM Conference on Data Communication, August 2014*.

Journal publications

Micro Top Picks, 2016 Sheng Li, Hyeontaek Lim, Victor Lee, Jung Ho Ahn, **Anuj Kalia**, Michael Kaminsky, David Andersen, Seongil O, Sukhan Lee, Pradeep Dubey. Achieving One Billion Key-Value Requests per Second on a Single Server. *Micro's Top Picks from the Computer Architecture Conferences, 2016*.

TOCS, 2016 Sheng Li, Hyeontaek Lim, Victor Lee, Jung Ho Ahn, **Anuj Kalia**, Michael Kaminsky, David Andersen, Seongil O, Sukhan Lee, Pradeep Dubey. Full-Stack Architecting to Achieve a Billion-Requests-Per-Second Throughput on a Single Key-Value Store Server Platform. *Transactions on Computer Systems, 2016.*

Work Experience

Fall 2015 **Research Intern**, Microsoft Research, Cambridge, UK
Worked with Dushyanth Narayanan on designing a physical time–based failure recovery protocol for an erasure-coded distributed transactional object store (FaRM).

Summer 2012 **Software Engineering Intern**, Google India, Hyderabad
Created a framework to generate runtime-configurable MapReduce pipelines to collect custom statistics.

Summer 2011 **Engineering Intern**, Imperial College London and Maxeler Technologies
Worked with Prof. Wayne Luk on developing an acceleration pipeline for AES encryption for Maxeler's FPGA-based dataflow engines.

Summer 2010 **Teacher**, Vidyamandir Classes, New Delhi
Taught chemistry to high school students.

Awards and Achievements

2017–2019 Facebook Fellowship

2016 Best Student Paper award at USENIX ATC, 2016

2009–2013	Dean's award for academic performance ($\sim$3 in class), in every semester at IIT-Delhi
2010, 2012	OP Jindal Engg. and Management Scholarship, awarded to 1 student from each year at IIT-D
2009	Rank 24 in Indian Institute of Technology Joint Entrance Exam, among around 400,000 students