

ANUPAM SOBTI



GET IN TOUCH

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ABOUT ME

- ▶ Technology Enthusiast
- ▶ Excellent Communicator
- ▶ Quick Learner
- ▶ Web Developer
- ▶ Hardware Hacker
- ▶ Passionate Teacher

OBJECTIVE

To develop a deep understanding of a problem and solving it with utmost dedication and perseverance so as to reach the best possible solution.

EDUCATION

2016	M.TECH. (Microelectronics)	WORK INTEGRATED LEARNING PROGRAMME (BITS Pilani)	8.93
2013	B.TECH. (ECE)	NETAJI SUBHAS INSTITUTE OF TECHNOLOGY (Delhi University)	79.6%
2009	CLASS XII	ST. FRANCIS DE SALES SCHOOL, NEW DELHI (Central Board Of Secondary Education)	90.2%
2007	CLASS X	ST. FRANCIS DE SALES SCHOOL, NEW DELHI (Central Board Of Secondary Education)	89.4%

WORK EXPERIENCE



Worked as DFT Engineer at Qualcomm India Pvt. Ltd. (July 2013- March 2016)

Worked on Memory BIST and ATPG Activities for Snapdragon 615, 610, 210, MDM 9x35 and other SoCs

Lead Memory Failure Analysis Team for Bangalore

1 invention in process for US Patent Filing as primary author

Awarded 5 Qualstarts for significant contribution to projects

SKILLS

VLSI	Pre-silicon Gate Level Verification and Post-Silicon Bringup and Characterization for Memory BIST and ATPG Activities
	Failure Analysis for memory failures on Silicon
EMBEDDED SYSTEMS	A batteryless remote control system (at CEDT, NSIT)
	John Conway's Game of Life on Nokia LCD (AVR Workshop)
	Line Follower Robot (Winter Training Project)
	Digital Color Organ using Arduino (at CEDT, NSIT)
	Dice Game on CPLD using VHDL (at CEDT, NSIT)
	Electronic Ludo using 8085 (as Microprocessors Lab Project)
	Home Automation System Prototype (Winter Intern, Velmenni)
	Wireless Home Monitoring System (Final Year Project)
WEB DEVELOPMENT	IEEE NSIT Official Website (7th in Asia region competition)
	India International Conference on Power Electronics 2010
	An electronics startup, Learnmicros
	Innovision 2011, Technical Fest of NSIT
PROGRAMMING	Automation Initiatives appreciated across locations at Qualcomm
	▶ Python ▶ C ▶ Java Basics
	▶ Unix Shell ▶ VHDL ▶ Matlab (Octave)
TOOLS	▶ VCS ▶ Modelsim ▶ OrCAD
	▶ PSpice ▶ EAGLE ▶ AVR Studio
	▶ Xilinx ▶ Android Studio

EXTRA CURRICULAR

EVENT MANAGEMENT	Various workshops like CAN and Ethernet (TI), Seminar on Telecommunication, Joomla
	Represented NSIT in IEEE Quaterly Meets as Joint Secretary
KNOWLEDGE SHARING	Founder of Special Interest Group on FPGAs using Papilio
	Workshop on Joomla

HOBBIES/INTERESTS

Music, Cycling, Table Tennis

DIY Projects for Decoration/Sound