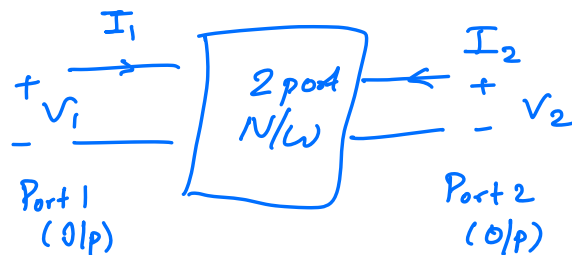
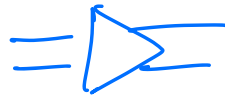


Two Port Network Parameters



Open circuit impedance parameters (Z-parameters)

Matrix notation

$$\begin{aligned} V_1 &= Z_{11} I_1 + Z_{12} I_2 \\ V_2 &= Z_{21} I_1 + Z_{22} I_2 \end{aligned} \quad \left| \quad \begin{pmatrix} V_1 \\ V_2 \end{pmatrix} = \begin{pmatrix} Z_{11} & Z_{12} \\ Z_{21} & Z_{22} \end{pmatrix} \begin{pmatrix} I_1 \\ I_2 \end{pmatrix} \right.$$

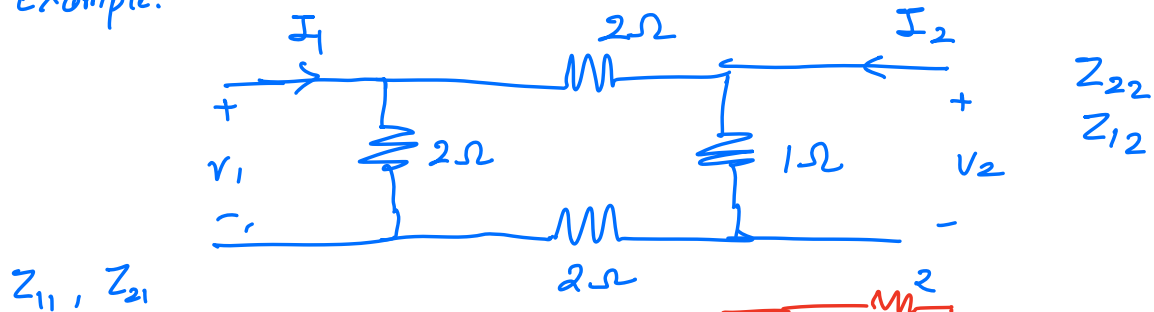
$$Z_{11} = \frac{V_1}{I_1} \quad \left| \quad I_2 = 0 \quad \text{Open Circuit}$$

$$Z_{21} = \frac{V_2}{I_1} \quad \left| \quad I_2 = 0 \quad \text{Impedance}$$

$$Z_{12} = \frac{V_1}{I_2} \quad \left| \quad I_1 = 0 \quad \text{Impedance}$$

$$Z_{22} = \frac{V_2}{I_2} \quad \left| \quad I_1 = 0 \quad \text{Impedance}$$

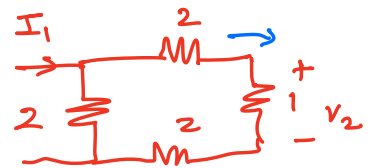
Example:



$$I_2 = 0$$

$$\begin{aligned} V_1 &= (2 \parallel (2 + 1 + 2)) I_1 \\ &= (2 \parallel 5) I_1 \\ &= 10/7 I_1 \end{aligned}$$

$$Z_{11} = \frac{V_1}{I_1} = 10/7$$



$$Z_{21} = \left. \frac{V_2}{I_1} \right|_{I_2 = 0}$$

$$V_2 = I_1 \cdot \frac{2}{2 + (2 + 1 + 2)} \times 1$$

$$= I_1 \cdot \frac{2}{2 + 5}$$

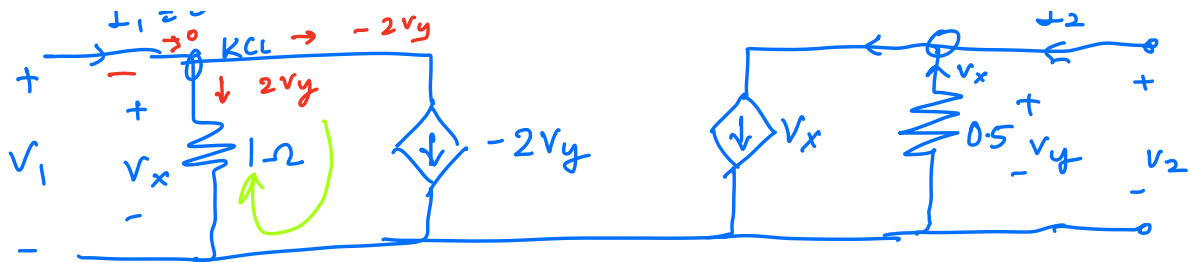
$$Z_{21} = V_2 / I_1 = 2/7$$

$$Z_{22} = 6/7 \quad Z_{12} = 2/7$$

Example 16.14 (SK)

τ = 0

KCL
τ



Z-parameters.

$$V_1 = V_x \quad V_2 = V_y$$

$$I_2 = 0, \quad Z_{11}, \quad Z_{21}$$

$$V_2 = V_y = -0.5 V_x$$

$$I_1 = \frac{V_x}{1} - 2 V_y$$

$$= -0.5 V_1$$

$$I_1 = V_1 - 2(-0.5 V_1)$$

$$I_1 = V_1 + V_1 = 2 V_1$$

$$Z_{11} = \frac{V_1}{I_1} = \frac{1}{2}$$

$$Z_{21} = \frac{V_2}{I_1} = \frac{-0.5 V_1}{2 V_1} = -\frac{1}{4}$$

$$I_2 = 0$$

$$Z_{22} = \frac{V_2}{I_2}$$

$$V_1 = V_x = -1(-2 V_y) = +2 V_2$$

KCL at end node.

$$I_2 = \frac{V_y}{0.5} + V_x$$

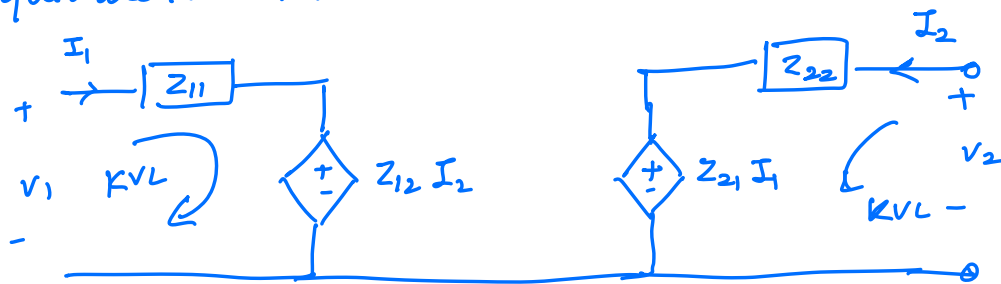
$$= 2 V_2 + 2 V_2$$

$$Z_{22} = \frac{V_2}{I_2} = \frac{1}{4}$$

Exercise: $Z_{12} = 0.5 \parallel$

$$\begin{cases} V_1 = Z_{11} I_1 + Z_{12} I_2 \\ V_2 = Z_{21} I_1 + Z_{22} I_2 \end{cases}$$

Equivalent ckt



Short Circuit Admittance Parameters (Y-parameters)

$$I_1 = Y_{11} V_1 + Y_{12} V_2$$

$$I_2 = Y_{21} V_1 + Y_{22} V_2$$

$$\begin{pmatrix} I_1 \\ I_2 \end{pmatrix} = \begin{pmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{pmatrix} \begin{pmatrix} V_1 \\ V_2 \end{pmatrix} \quad - (1)$$

$$Y_{11} = \frac{I_1}{V_1} \Big|_{V_2=0}$$

$$Y_{21} = \frac{I_2}{V_1} \Big|_{V_2=0}$$

$$Y_{12} = \frac{I_1}{V_2} \Big|_{V_1=0}$$

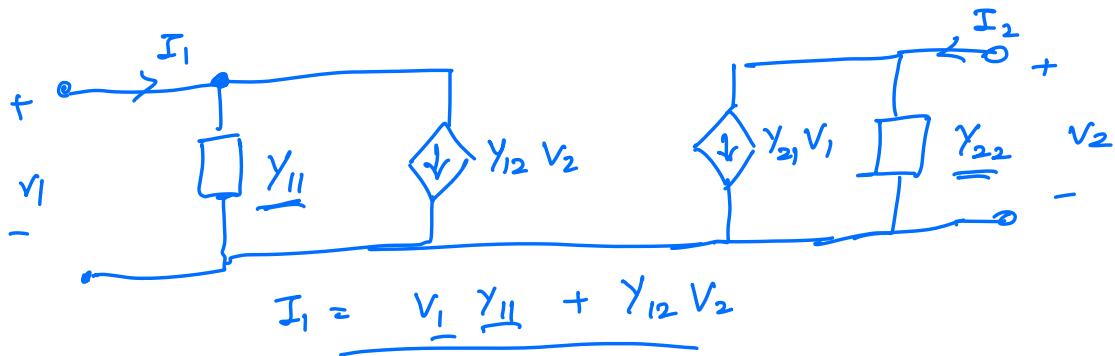
$$Y_{22} = \frac{I_2}{V_2} \Big|_{V_1=0}$$

Short Circuit
Admittance

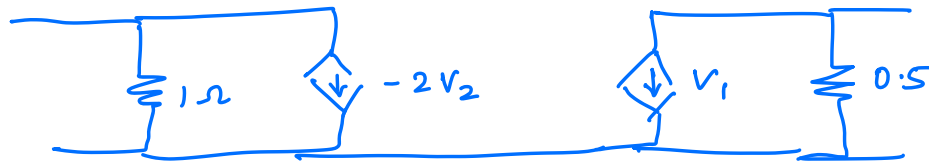
$$\begin{pmatrix} V_1 \\ V_2 \end{pmatrix} = \begin{pmatrix} Z_{11} & Z_{12} \\ Z_{21} & Z_{22} \end{pmatrix} \begin{pmatrix} I_1 \\ I_2 \end{pmatrix} \quad -(2)$$

$$\begin{pmatrix} Z_{11} & Z_{12} \\ Z_{21} & Z_{22} \end{pmatrix} = \begin{pmatrix} Y_{11} & Y_{12} \\ Y_{21} & Y_{22} \end{pmatrix}^{-1}$$

Equivalent circuit (Y-parameters)



(Example)



$$Z = \begin{pmatrix} 1 & -2 \\ 1 & 2 \end{pmatrix}^{-1}$$

email: anup.aprem@