Anuprabha R

Tiruppur, Tamil Nadu | kit27.ece006@gmail.com | 9025475280 | Anuprabha R | anuprabhar

EDUCATION

Kalaignarkarunanidhi Institute of Technology - Coimbatore , India 2023 – 2027
B.E – Electronics and Communication Engineering, Pre-Final year Current **CGPA: 7.96/10.0**

Coursework: Digital Communication, Microprocessors & Microcontrollers, Digital Signal Processing (DSP), Control Systems, Linear Integrated Circuits, Analog & Digital Circuits, Communication Networks, Probability & Random Processes.

EXPERIENCE

Web Development Intern | CodeBind Technologies – Coimbatore, India *(Offline)* Jul 2024

- Gained practical experience in core web technologies including HTML, CSS, JavaScript, and basic React.js components.
- Developed a fully responsive boutique website page, implementing clean design, product sections, and smooth navigation.
- Improved skills in building interactive UI features and understanding basic frontend–backend workflow connections

Embedded Systems Intern | Emglitz Technologies – Coimbatore, India *(Offline)* Dec 2024

- Gained hands-on experience in hardware concepts, including sensor integration, microcontroller basics, and hardware interfacing.
- Worked with Arduino boards to understand real-time data flow, actuator control, and basic hardware operations.
- Learned essential hardware debugging techniques such as checking connections, analysing circuit behaviour, and using serial communication tools

Web Development Intern | Brainery Spot Technology – Coimbatore, India *(Offline)* May 2025

- Developed full-stack modules using React.js and Node.js, contributing directly to production-level web features.
- Implemented API integrations, state management, and reusable UI components to enhance scalability and maintainability.
- Improved frontend performance and user experience through optimized rendering and clean code practices.

PROJECTS

Design and Validation of High-Performance Low-Power Parallel Prefix Adders | Cadence Virtuoso, Brent–Kung Architecture, CMOS VLSI Oct 2025

- Designed a low-power, high-speed Brent–Kung parallel prefix adder using CMOS logic to optimize delay, area, and power.
- Performed schematic entry, simulation, and validation in Cadence Virtuoso to evaluate timing characteristics and switching behaviour.
- Analysed critical path delays and optimized gate-level architecture to achieve improved performance over conventional adders.
- Verified functional correctness and power metrics using transient, DC, and post-layout simulations.

Automatic Dustbin System | Arduino Uno, Ultrasonic Sensor, Servo Motor Oct 2025

- Designed an automated waste-bin mechanism that detects human proximity using an ultrasonic sensor.
- Programmed the Arduino Uno to trigger a servo motor for hands-free lid opening, improving hygiene and user convenience.
- Implemented efficient sensor calibration to ensure accurate distance detection and smooth lid movement.
- Built a compact prototype with stable power management for continuous operation in indoor environments.

TECHNICAL SKILLS

C	HTML
SQL	CSS
Github	OOPS
SpringBoot	
Java	

CERTIFICATIONS & COURSES

Infosys Springboard - Java Foundation	2024
Infosys Springboard – C Programming	2024
Coursera – IoT Communication	2025
MathWorks – MATLAB Onramp	2025
Infosys Springboard - IoT Edge Computing and IoT Analytics	2025
Infosys Springboard - Database Management System Part 1	2025
Infosys Springboard - Database Management System Part 2	2025
MathWorks –Image Processing Onramp	2025

COMPETITIVE PROGRAMMING

Leetcode : Max rating – **1479**

Codeforces : Max rating - **377**

Codechef : Max rating - **1231** | Position : **1** Star