

OBJECTIVE OF THE CONFERENCE

Vidyalankar Institute of Technology aims to bring together academicians and experts from different parts of the country and abroad to exchange knowledge and ideas. With this vision, The Conference on Advances of Electronics and Computer Technology (ICAECT-2022) is being proposed, to be held on 07th and 08th April, 2022.

VISION OF CONFERENCE

Vidyalankar Institute of Technology aims to move a step further in its teaching endeavours and head towards research and promote interdisciplinary collaborations.

The International Conference on Advances of Electronics and Computer Technology (ICAECT-2022) is to be held on 07th and 08th April, 2022 is intended to bring together researchers and practitioners, from the academy and industry.

This conference shall be organized with parallel multi-track programmes, which will introduce participants to advances of electronics and computer technology. It will focus on the applications for the 'compliance to industry 4.0.'

ABOUT DEPARTMENT OF ELECTRONICS ENGINEERING

Electronics Engineering is an ever evolving field that has benefitted the progress of man and society. Electronic gadgets have become an integral part of our lifestyle. In fact, we begin our day with some electronic gadget and end the day with it. In today's fast-changing world, a new generation of electronics engineers needs to be developed to provide environment friendly solutions to the challenges faced due to limited resources. The department has a dedicated faculty to develop all round electronics engineering graduates with highly professional and ethical values. The department has well equipped laboratories where students can perform experiments and work on real life problems.

PATRON

- Ms. Rashmi Deshpande Chairperson, VDT
- Mr. Vishwas Deshpande Trustee, VDT
- Mr. Milind Tadvalkar Director, VDT
- Dr. Sunil Patekar Principal, VIT
- Prof. Varsha Bhosale Vice Principal, VIT
- Dr. Saurabh Mehta CAO, VIT
- Dr. Amit Oak COO, VIT

Program Chair	Program Co-Chair
Dr. Arun Chavan	Dr. Sheetal Mapare
	Dr. Nayana Mahajan

Technical Committee	Publication Chair
Dr. Sangeeta Joshi	Prof. Pravin Annadate
Prof. Shrikant Velankar	Prof. Rakshak Sood
Dr. Girish Gidaye	Prof. Anuradha Joshi

Registration and Enquiry

Prof. Rakshak Sood – 9004649055

Prof. Akhil Masurkar – 9819063308

Advisory Board

Dr. S.Mukhopadhyay (Macquarie University, Australia)
Prof. Mondher Frikha (Director, Sfax, Tunisia)
Dr. Faruk Kazi (Dean R&D, VJTI, Mumbai)
Dr. Milind Joshi (SAMEER IIT, Mumbai)
Dr. S.N. Mali (Principal, D Y Patil, Pune)
Dr. Atul Phadke (Professor, COEP, Pune)
Dr. Ravishankar Dudhe (Manipal University, Dubai)
Dr.Sadanand Deshpande (Dean R&D, PIET, Nagpur)
Dr. S. G. Kadwane (HOD, YCCE, Nagpur)
Dr. R. M. Moharil (Professor,YCCE, Nagpur)

Schedule Day 1: 07th April 2022

Time	Day 1	
09:30 a.m. To 10:00 a.m.	Inauguration of Conference	
10:00 a.m. To 11:00 a.m.	Keynote Speech	
11:00 a.m. To 12:00 p.m.	Guest of Honour Speech	
12:00 p.m. To 01:15 p.m.	Lunch Break	
01:15 p.m. To 02:45 p.m.	Session 1	Session 2
02:45 p.m. To 03:00 p.m.	Tea Break/Poster Presentation	
03:00 p.m. To 04:30 p.m.	Session 3	Session 4

Schedule Day 2: 08th April 2022

Time	Day 2	
09:30 a.m. To 10:30 a.m.	Tutorial Session	
10:30 a.m. To 11:00 a.m.	Tea Break/Poster Presentation	
11:00 a.m. To 12:30 p.m.	Session 5	Session 6
12:30 p.m. To 01:35 p.m.	Lunch Break	
01:30 p.m. To 03:00 p.m.	Session 7	Session 8
03:00 p.m. To 03:15 p.m.	Tea/Coffee Break	
03:15 p.m. To 04:30 p.m.	Valedictory and closing	



ABOUT THE INSTITUTE

Vidyalankar Institute of Technology is an Engineering Degree and Management College, approved by AICTE and affiliated to the University of Mumbai. Incepted in 1999 and awarded an 'A' Grade by the Govt. of Maharashtra, with three departments of the college are NBA accredited and the institute is NAAC accredited with A+ Grade, the Institute generates quality technocrats with solid academic records and offers excellent placement opportunities. "Work is worship" is the guiding principle for everyone at Vidyalankar. Through our passion for work we incessantly seek progress and believe that individual and collective expertise blossom in an environment of symbiosis. Inspired by the philosophy of "Student Delight", every year VIT welcomes and nurtures around 800 engineering and management talents at the undergraduate, postgraduate and doctorate levels and makes them industry-ready.

Tracks

- Industry 4.0
- Virtual Reality and Artificial Intelligence
- Power Systems and Automation
- Communication and Network security

List of journal

Selected papers shall be published in any one of the Journal given below

- Gradiva Review Journal (UGC Care List Group 2, Scopus Active Journal)
- International Journal of Engineering and Management Research (The journal is Abstracted/Indexed in: CrossRef, SSRN(Elsevier), Scilit, Google Scholar, CNKI-Scholar, Publons, Microsoft Academic, Open J-Gate, Research Bib, Mendely, etc.)
- Journal of Emerging Technologies and Innovative Research (UGC Care List)
- Web-of-science Indexed Journal.



International Conference on

ADVANCES OF ELECTRONICS AND COMPUTER TECHNOLOGY



(ICAECT-2022)

07th - 08th APRIL 2022

Organized By

Department of Electronics Engineering
Vidyalankar Institute of Technology,
Mumbai 400 037
Telefax: 022-24161126
Website: vit.edu.in

Conference Website
<https://icaect22.cf/>

Email: ICAECT@vit.edu.in

IMPORTANT DATES OF CONFERENCE

Call for Paper	06 January 2022
Acceptance Deadline	10 February 2022
Acceptance Notification	10 March 2022
Registration	20 March 2022
Submission of Camera-Ready Paper	28 March 2022
Conference Dates	7 & 8 April 2022

Mode of Conduction : Online
Platform : MS-Teams

REGISTRATION DETAILS:

Author/ Participant	Academia Participant	Industry Participant
Indian Author	Rs.2500	Rs.3000
Foreign Author	\$50	\$100
Participants	Rs.500	Rs.1000

PAPER SUBMISSION LINK

<https://easychair.org/cfp/ICAECT-2022>