

Enrollment No.....



Faculty of Engineering
End Sem (Odd) Examination Dec-2019
EC3CO10/EE3CO08/EI3CO10/EX3CO08

Microprocessor and Microcontroller

Programme: B.Tech.

Branch/Specialisation: EC/EE/EI/EX

Duration: 3 Hrs.**Maximum Marks: 60**

Note: All questions are compulsory. Internal choices, if any, are indicated. Answers of Q.1 (MCQs) should be written in full instead of only a, b, c or d.

- Q.1 i. 8085 is a: 1
 (a) 8 bit microprocessor (b) 16 bit microprocessor
 (c) 32 bit microprocessor (d) None of these.
- ii. Which is not part of execution unit? 1
 (a) ALU (b) Address conversion mechanism
 (c) Flag register (d) General purpose registers
- iii. Address bus size of the 8086 microprocessor is 1
 (a) 16 bits (b) 20 bits (c) 24 bits (d) 32 bits
- iv. Physical address calculated in 8086 microprocessors is of 1
 (a) 16 bits (b) 20 bits (c) 24 bits (d) 32 bits
- v. DMA controllers stands for 1
 (a) Direct Memory Alternation Controller
 (b) Direct Memory Access Controller
 (c) Direct Multi Access Controller
 (d) Double Memory Access Controller
- vi. For interrupt controller, the following chip is used- 1
 (a) 8255 (b) 8259 (c) 8257 (d) 8212
- vii. When the microcontroller executes some arithmetic operations, then 1
 the flag bits of which register are affected?
 (a) PSW (b) SP (c) DPTR (d) PC
- viii. 8051 series have how many 16 bit registers? 1
 (a) 2 (b) 3 (c) 1 (d) 0

P.T.O.

[2]

[3]

- ix. ARM processors were basically designed for _____ **1**
 (a) Main frame systems (b) Distributed systems
 (c) Mobile systems (d) Super computers
- x. The main importance of ARM micro-processors is providing **1**
 operation with _____
 (a) Low cost and low power consumption
 (b) Higher degree of multi-tasking
 (c) Lower error or glitches
 (d) Efficient memory management

- Q.2 Attempt any two:
- i. Discuss the evolution of microprocessors in detail. **5**
 - ii. Draw the pin diagram of 8085 microprocessor and explain signals on **5**
 it in brief.
 - iii. What is Flag? Explain different types of flag provided in 8085. **5**

- Q.3 Attempt any two:
- i. With the help of block diagram explain the architecture of 8086 **5**
 microprocessor.
 - ii. Describe the instruction format of 8086. Also describe the MOV **5**
 instructions of 8086.
 - iii. Write a assembly language program for 8086 microprocessor to add **5**
 two 16-bit numbers.

- Q.4 Attempt any two:
- i. Draw the block diagram of 8251 USART and explain function of **5**
 each block.
 - ii. Draw the block diagram of programmable interrupt controller and **5**
 explain its working.
 - iii. Explain following for 8255: **5**
 (a) Mode control word format
 (b) Bit set/reset control word format.

- Q.5 Attempt any two:
- i. Explain the memory structure of 8051. **5**

- ii. Explain various addressing modes of 8051 microcontroller. **5**
 - iii. Describe the architecture and working of 8051 microcontrollers. **5**
- Q.6 Attempt any two:
- i. Differentiate between RISC and CISC. **5**
 - ii. Write the difference between 80286, 80386 and 80486. **5**
 - iii. Draw and explain the Harvard architecture of microprocessor. **5**

Marking Scheme
EC3CO10/EE3CO08/EI3CO10/EX3CO08
Microprocessor and Microcontroller

Q.1	i.	8085 is a:		1
		(a) 8 bit microprocessor		
	ii.	Which is not part of execution unit?		1
		(b) Address conversion mechanism		
	iii.	Address bus size of the 8086 microprocessor is		1
		(b) 20 bits		
	iv.	Physical address calculated in 8086 microprocessors is of		1
		(b) 20 bits		
	v.	DMA controllers stands for		1
		(b) Direct Memory Access Controller		
	vi.	For interrupt controller, the following chip is used-		1
		(b) 8259		
	vii.	When the microcontroller executes some arithmetic operations, then the flag bits of which register are affected?		1
		(a) PSW		
	viii.	8051 series have how many 16 bit registers?		1
		(a) 2		
	ix.	ARM processors were basically designed for _____		1
		(c) Mobile systems		
	x.	The main importance of ARM micro-processors is providing operation with _____		1
		(a) Low cost and low power consumption		

Q.2		Attempt any two:		
	i.	Evolution of microprocessors		5
		(1 mark for per generation/bit)	(1 mark*each)	
	ii.	Diagram of 8085 microprocessor	2 marks	5
		Explanation	3 marks	
	iii.	Flag format	2 marks	5
		Explanation	3 marks	

Q.3		Attempt any two:		
	i.	Block diagram	2.5 marks	5
		Explanation	2.5 marks	
	ii.	At three types(Instruction format)	2 marks	5
		MOV instructions	3 marks	

	iii.	Code	4 marks	5
		Comment	1 mark	
Q.4		Attempt any two:		
	i.	Block diagram	2.5 marks	5
		Explanation	2.5 marks	
	ii.	Block diagram	2.5 marks	5
		Explanation	2.5 marks	
	iii.	Explain following for 8255:		5
		(a) Mode control word format	2.5 marks	
		(b) Bit set/reset control word format.	2.5 marks	
Q.5		Attempt any two:		
	i.	Memory structure of 8051.		5
		Internal	3 marks	
		External	2 marks	
	ii.	Various addressing modes of 8051 microcontroller.	(1 mark*5)	5
	iii.	Architecture	2.5 marks	5
		Working	2.5 marks	
Q.6		Attempt any two:		
	i.	Minimum 5 Difference	(1 mark*5)	5
	ii.	Minimum Difference	(1 mark*5)	5
	iii.	Diagram	2.5 marks	5
		Explanation	2.5 marks	
