

Q.6

Attempt any two:

- i. Explain cycle redundancy check. Assume that data is 110101010 and the code generator is 10101. Calculate CRC codeword.
- ii. What is hamming distance? If the 7-bit Hamming codeword received by a receiver is 1011011. Assuming the even parity, state whether the received codeword is correct or wrong. If wrong, locate the bit in error.
- iii. Explain sliding window error control mechanism in data communication with suitable examples.

5	3	5	1,2 3	1,4
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*Total No. of Questions: 6**Total No. of Printed Pages: 4***Enrollment No.....**

Knowledge is Power

Faculty of Engineering / Science
End Sem Examination Dec 2024

CS3CO28 / BC3CO39 Data Communication

Programme: B.Tech./ B.Sc.

Branch/Specialisation: CSE All/
Computer Science**Maximum Marks: 60****Duration: 3 Hrs.**

Note: All questions are compulsory. Internal choices, if any, are indicated. Answers of Q.1 (MCQs) should be written in full instead of only a, b, c or d. Assume suitable data if necessary. Notations and symbols have their usual meaning.

Marks	BL	CO	PO	PSO
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Q.1 i. Which of the following is required for **1** 1 1 1,2
communication between two computers?

- (a) Communication hardware
- (b) Communications software
- (c) Protocol
- (d) All of these including access to transmission medium

ii. Which of the following is the fastest media of **1** 1 1 1,2
data transfer?

- (a) Fiber optic
- (b) Co-axial cable
- (c) Untwisted wire
- (d) Telephone lines

iii. Which modulation scheme is preferred for **1** 1 2 1,2
frequency hopping spread spectrum process?

- (a) FSK
- (b) BPSK
- (c) MFSK
- (d) MPSK

iv. An analog signal has a bit rate of 8000 bps and a **1** 2 2 1,2
baud rate of 1000 baud. How many data elements are carried by each signal element? How many signal elements we do need?

- (a) 4 bits/baud; 256
- (b) 8 bits/baud; 128
- (c) 8 bits/baud; 256
- (d) 4 bits/baud; 128

v. The Asymmetric Digital subscriber Line – lite **1** 1 3 1,2
uses carriers for

- (a) 64 DMT
- (b) 128 DMT
- (c) 512 DMT
- (d) 256 DMT

Marking Scheme

CS3CO28 Data Communication

Q.1					
i)	(d) All of above including access to transmission medium	1	Q.4	i.	2 marks
ii)	(a) Fiber optic	1		Classification	1 mark
iii)	(c) MFSK	1		Description(at least Mesh, Bus, Star, Ring) (0.5 mark*4)	2 marks
iv)	(c) 8 bits/baud; 256	1	OR	ii.	1 mark
v)	(b) 256 DMT	1		Circuit Switching	3 marks
vi)	(a) end offices	1		Characteristics	3 marks
vii)	(b) Does not need an expensive server as individual workstations are used to access files	1		Stages	3 marks
viii)	(c) 128 bits	1		Comparison	5 marks
ix)	(b) 100	1		Delay diagram	2 marks
x)	(d) Checksum	1	Q.5	i.	4
				Physical addressing	1 mark
				Logical addressing	1 mark
				Port addressing	1 mark
				Specific addressing	1 mark
			OR	ii.	6
				Physical layer responsibilities	2 marks
				Data link layer responsibilities	2 marks
				Transport layer responsibilities	2 marks
				TCP/IP Protocol Suite architecture	2 marks
				TCP/IP Description	4 marks
Q.2					
i.	Upper bit rate = 6 Mbps	1 mark	Q.6	i.	5
	Appropriate bit rate = 4 Mbps for better performance			Cycle Redundancy Check	2 marks
	No. of signal level, L = 4	1 mark		CRC codeword = 1101010101011	3 marks
ii.	Transmission Impairments	1 mark	ii.	Hamming distance	5
	Causes	2 marks		Numerical steps	1 mark
iii.	Name of Guided media	1 mark		Error detection and correction: Incorrect bit = bit 5	3 marks
	Two types description	4 marks		Correct codeword = 1001011	1 mark
OR					
iv.	Half Duplex and Full Duplex	1 mark	iii.	Sliding Window protocol Diagram	5
	Analog and Digital Signal	1 mark		Sliding Window protocol Description	2 marks
	Baseband and Broadband transmission	1 mark			3 marks
	Serial and Parallel transmission	1 mark			
	Synchronous and Asynchronous transmission	1 mark			
Q.3					
i.	Block coding	1 mark	*****		
	Representation of sequence 01001110 (0.5 mark*4)	2 marks			
ii.	Sampling	2 marks			
	Quantization	3 marks			
	Encoding	2 marks			
OR					
iii.	Synchronous TDM	3 marks			
	Data rate management strategies	2 marks			
	Differentiation statistical TDM and synchronous TDM	2			