Anuraj R.

M.Sc.(Tech.) Space Robotics and Automation
M.Sc. Product Management(On Leave)

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Embedded Systems and Robotics Professional with a decade of experience in building hardware and software for Automotive, Space and Consumer Electronics Sectors.

Experience

June-2020– Present	**SoC Software Engineer**, ***Nokia*** *via Unikie Oy*, Oulu, Finland. Consulting for Nokia in mMIMO SoC software
Nov-2015– May,2020	**Embedded Software Engineer**, ***TactoTek Group***, Oulu, Finland.
April,2014– June,2015	**Robotics Engineer**, ***Probot Ltd.***, Oulu, Finland.
Jan-Apr, 2011	**Sr. Project Assistant**, ***Bharti School., IIT-Delhi***, New-Delhi, India.
Jul-Dec,2010	**Project Assistant**, ***Bharti School., IIT-Delhi***, New-Delhi, India.

Education

2017– Present — **Master of Science in Product Management(On Hold)**, ***University of Oulu***, **Finland**.

2011 – 2013 — **Master of Science(Technology) in Space Robotics and Automation**, ***Aalto University***, **Finland**.
Master of Science in Space Technology, ***Luleå University of Technology***, **Sweden**.

Erasmus Mundus Double Degree Master's Thesis

Title: **Terrain mapping near the vehicle, SLAM[1] and global map building for lunar rover. Thesis Grade: 4/5**

2006 – 2010 — **Bachelor of Technology in Electronics Engineering**, ***Sardar Vallabhbhai National Institute of Technology (SVNIT)***, **India**.

Bachelor's Thesis

Title: **Design of Medical Ventilator. Thesis Grade: 9/10**

Technical abilities and skills

Languages	C/C++, Python, bash-scripting, C#, Matlab and VHDL	**Embedded Protocols**	SPI, I2C, CAN, USART/UART, LIN, Debug - SWD, JTAG
CPU Architectures	ARM Cortex-A, RISC-V, x86	**Micro-controller**	ARM Cortex-M0/M0+, Cortex-M3/M4, AVR, 8051
Electronics	Electronic System Design, PCB Designing	**Other Software**	Yocto, CMake, Git, RobotFramework

Awards and Recognitions

○ Received **full scholarship** for two years from the European Union to pursue Master's Degree Programme in Space Science and Technology.

○ Selected for the prestigious **national-level scholarship/internship** programme called Kishore Vaigyanik Protsahan Yojana (KVPY[4]) in 2008 by Indian Institute of Science, Bangalore, India and Indian Institute of Technology-Bombay(IIT-Bombay), which is given to encourage research in India.

○ **Won the first prize** in the event "Open-Hardware-Innovision" **(Competition for Innovative Ideas)** at TECHFEST-2008, IIT-Bombay's technical festival which is considered to be the largest technical festival in Asia.

○ **Won the first prize** in the event **Scientist for a Day** organized by Institute of Plasma Research, Ahmedabad, India, 2005.

○ Secured an **All India Rank of 3738** out of approximately 300,000 students (**top 1.5%**) in IIT-JEE[3]-2006 which is the most competitive Engineering Entrance Examination in India.

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[1]Simultaneous Localization And Mapping
[2]Brush-Less Direct Current
[3]Indian Institute of Technology-Joint Entrance Examination
[4]Kishore Vaigyanik Protsahan Yojana - Translates to Young Scientist Encouragement Programme