Chapter No:6 Function of combinational logic

TOPICS

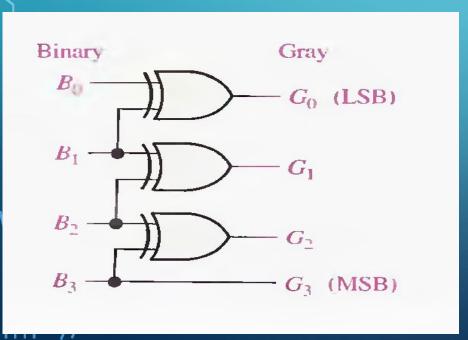
- 1.Half and Full Adders
- 2.Parallel Binary Adders
 - 3. Comparators
 - 4.Decoders
 - 5. Encoders
 - 6. Code Converters
 - 7. Multiplexers (Data Selectors)
 - 8.Demultiplexers

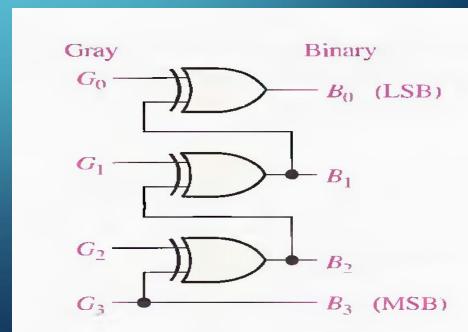
Binary-to-Gray and Gray-to-Binary Conversion

There are various code converters that change one code to another. Two examples are the four bit binary-to-Gray converter and the Gray-to-binary converter.

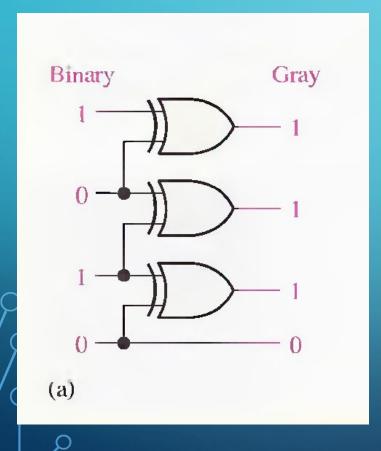
Four-bit binary-to-Gray conversion logic.

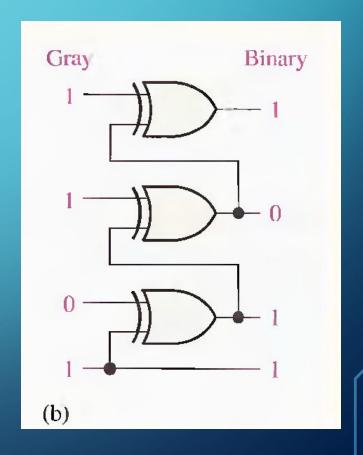
Four-bit Gray-to-binary conversion logic.





- (a) Convert the binary number 0101 to Gray code with exclusive-OR gates.
- (b) Convert the Gray code 1011 to binary with exclusive-OR gates.

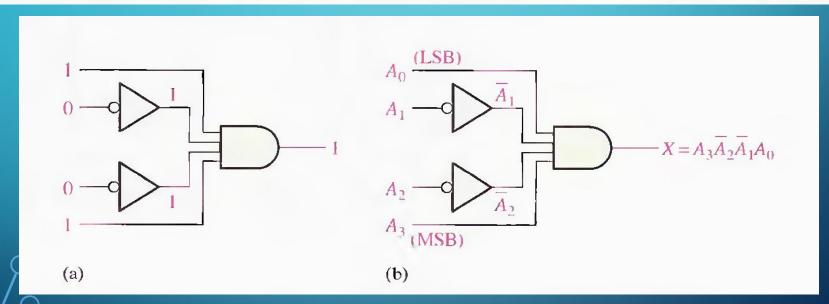




DECODERS

A decoder is a digital circuit that detects the presence of a specified combination of bits (code) on its inputs and indicates the presence of that code by a specified output level.

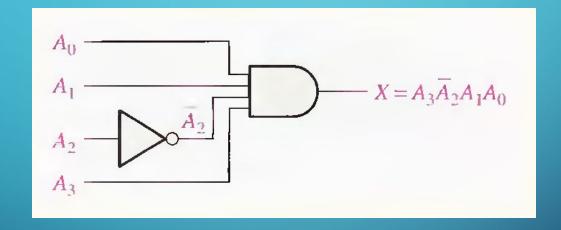
Suppose you need to determine when a binary 1001 occurs on the inputs of a digital circuit.



If a NAND gate is used in place of the AND gate in Figure, a LOW output will indicate the presence of the proper binary code, which is 100 I in this case.

Example

Determine the logic required to decode the binary number 1011 by producing a HIGH level on the output.



Develop the logic required to detect the binary code 10010 and produce an active-LOW output.

The 4-Bit Decoder

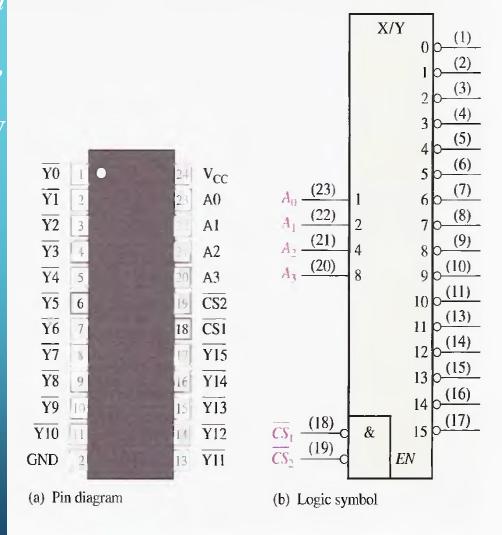
In order to decode all possible combinations of four bits, sixteen decoding gates are required (2^4 = 16). This type of decoder is commonly called either a 4-1ine-to-16-1ine decoder because there are four inputs and sixteen outputs.

Decoding functions and truth table for a 4-line-to-16-line (1-of-16) decoder with active-LOW outputs.

DECIMAL	BII	VARY	INP	UTS	DECODING			N.	1000	46			Q	UTI	UT	S					
DIGIT	A 3	Az	A_1	A_0	FUNCTION	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	0	0	0	0	$\overline{A}_3\overline{A}_2\overline{A}_1\overline{A}_0$	0	1	1	1	l	1	1	1	1	1	1	1	1	1	1	1
1	0	0	0 _	1	$\overline{A}_3\overline{A}_2\overline{A}_1A_0$	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1
2	0	0	- 1	0	$\overline{A}_3\overline{A}_2A_1\overline{A}_0$	1	1	0	1	1	1	1	1	1	1	1	1	1	1	1	1
3	0	0	1	1	$\overline{A}_3\overline{A}_2A_1A_0$	1	1	1	0	1	1-	1	1	1	1	1	1	1	1	1	1
4	0	1	0	0	$\overline{A}_3 A_2 \overline{A}_1 \overline{A}_0$	1	1	1	1	0	1	1	1	1	1	1	1	1	1	1	1
5	0	1	0	1	$\overline{A}_3 A_2 \overline{A}_1 A_0$	1	1 _	1 .	1	- 1	0	_ 1	. 1	1	1	1	1	1	1	1	1
6	0	1	1	0	$\overline{A}_3 A_2 A_1 \overline{A}_0$	1	1	1	1	1	1	0	1 -	1	1	1	1	1	1	1	1
7	0	1	1	1	$\overline{A}_3 A_2 A_1 A_0$	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	1
8	1	0	0	0	$A_3\overline{A}_2\overline{A}_1\overline{A}_0$	1	1	1	1	1	1	1	1	0	. 1	1	1	1	1	1	1
9	1	0	0	1	$A_3\overline{A}_2\overline{A}_1A_0$	1	1	1	1	1	1	1	1	1	0	1	1	1	1	1	1
10	1	0	1	0	$A_3\overline{A}_2A_1\overline{A}_0$	1	1	1	1	1	1	1	1	1	1	0	1	1	1	1	1
11	1	0	1	1	$A_3\overline{A}_2A_1A_0$	1	1	1	1	1	I	1	1	l	1	1	0	1	1	1	1
12	1	1	0	0	$A_3A_2\overline{A}_1\overline{A}_0$	1	1	1	1	1	i	1	1	1	l	1	1	0	1	1	1
13	1	1	0	1	$A_3A_2\overline{A}_1A_0$	1	1	1	1	1	1	1	1	1	1	1	1	1	0	1	1
14	1	1	1	0	$A_3A_2A_1\overline{A}_0$	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	1
15	1	1	1	1	$A_3A_2A_1A_0$	1	1	1	1	1	1	i	1	1	1	-1	Į	1	1	1	0

THE 74HC154 1-OF-16 DECODER

The 74HCl54 is a good example of an IC decoder. There is an enable function (EN) provided on this device, which is implemented with a NOR gate used as a negative-AND. A LOW level on each chip select input, CS1' and CS 2', is required in order to make the enable gate output (EN) HIGH. The enable gate output is connected to an input of each NAND gate in the decoder, so it must be HIGH for the NAND gates to be enabled. If the enable gate is not activated by a LOW on both inputs, then all sixteen decoder outputs (Y) will be HIGH regardless of the states of the four input variables.

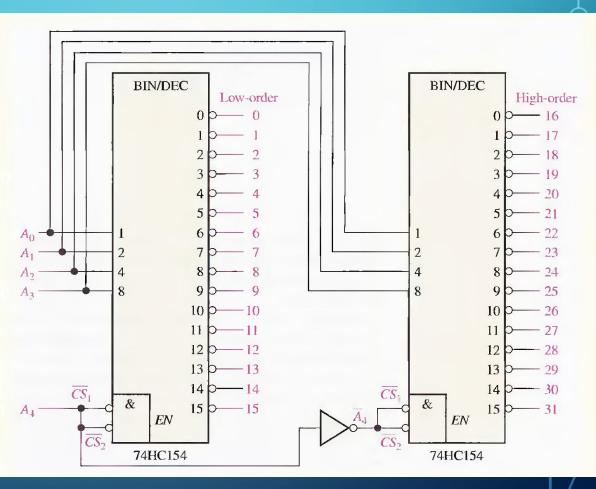


EXAMPLE

A certain application requires that a 5-bit number be decoded. Use 74HC154 decoders to implement the logic. The binary number is represented by the format $A_4A_3A_2A_1A_0$.

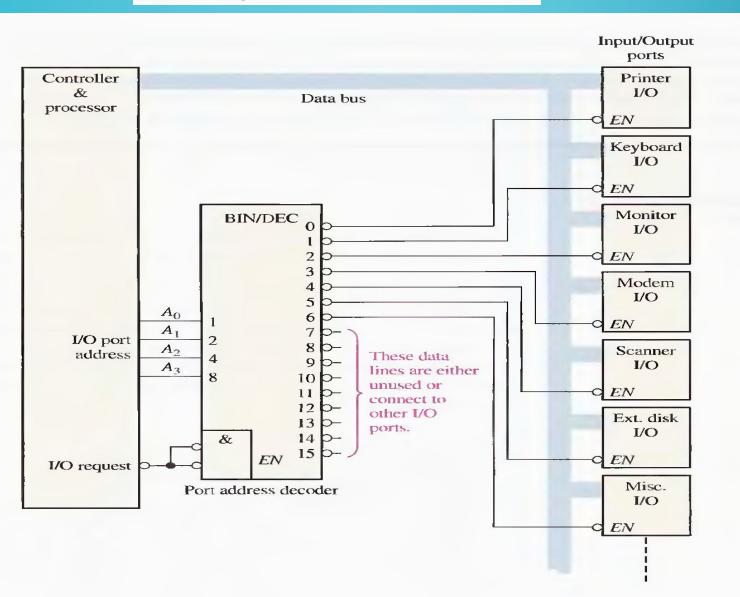
Since the 74HC154 can handle only four bits, two decoders must be use d to decode five bits. The fifth bit, A 4, is connected to the chip select inputs, CSl' and CS 2', of one decoder, and A4 is connected t the CS l'and CS 2' inputs of the other decoder, as shown in Figure . When the decimal number is 15 or less, A4 = 0, th Now-order decoder is enabled, and the high-order decoder is disabled. When the decimal number is greater than 15, A4 = I so A4' = 0, the 15high-order decoder is enabled. and the low -order decoder is

disabled.



An Application

A simplified computer I/O port system with a port address decoder with only four address lines shown.



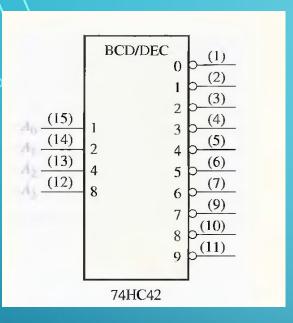
The BCD-to-Decimal Decoder

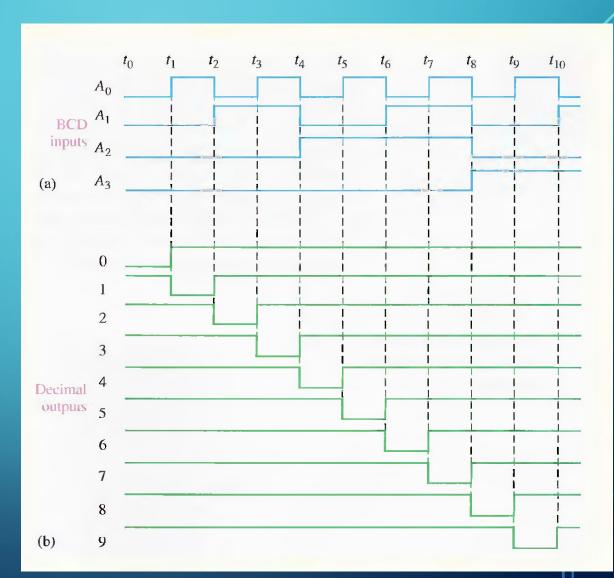
The BCD-to-decimal decoder converts each BCD code (8421 code) into one of ten possible decimal digit indications. It is frequently referred as a *4-line-to-10-line decoder* or a *1-of-10 decoder*.

BCD decoding functions.

DECIMAL		BCD C	DECODING			
DIGIT	A 3	A ₂	A_1	A_0	FUNCTION	
0	0	0	0	0	$\overline{A}_3\overline{A}_2\overline{A}_1\overline{A}_0$	
1	0	0	0	1	$\overline{A}_3\overline{A}_2\overline{A}_1A_0$	
2	0	0	1	0	$\overline{A}_3\overline{A}_2A_1\overline{A}_0$	
3	0	0	1	1	$\overline{A}_3\overline{A}_2A_1A_0$	
4	0	1	0	0	$\overline{A}_3 A_2 \overline{A}_1 \overline{A}_0$	
5	0	1	0	1	$\overline{A}_3 A_2 \overline{A}_1 A_0$	
6	0	1	1	0	$\overline{A}_3 A_2 A_1 \overline{A}_0$	
7	0	1	1	1	$\overline{A}_3 A_2 A_1 A_0$	
8	1	0	0	0	$A_3\overline{A}_2\overline{A}_1\overline{A}_0$	
9	1	0	0	1	$A_3\overline{A}_2\overline{A}_1A_0$	

The 74HC42 is an integrated circuit BCD-to-decimal decoder.



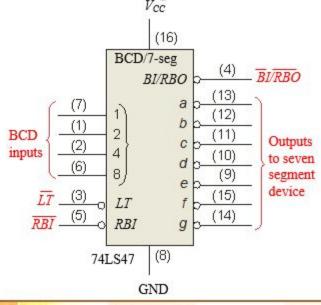


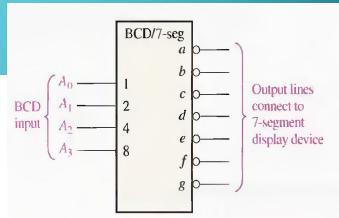
The BCD-to-7-Segment Decoder

The BCD-to-7-segment decoder accepts the BCD code on its inputs and provides outputs to drive 7-segment display devices to produce a decimal readout. The logic diagram for a basic 7-segment decoder is shown in Figure 6–34.

Another useful decoder is the 74LS47. This is a BCD-toseven segment display with active LOW outputs.

The *a-g* outputs are designed for much higher current than most devices (hence the word driver in the name).





BCD Decoder/Driver

Here the 7447A is an connected to an LED seven segment display. Notice the current limiting resistors, required to prevent overdriving the LED display.

