

TEAM :- TRIGUN
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Anushka Tripathi

PROBLEM STATEMENT 1

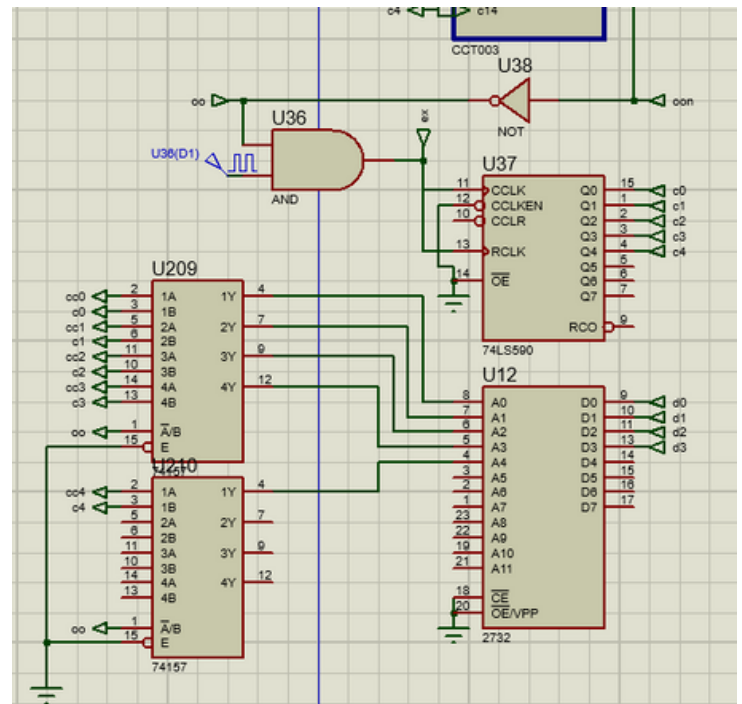
CONVEX HULL CALCULATION

In geometry, a convex hull is the smallest convex shape that contains a set of points. It's also known as the convex envelope or convex closure.

“Starting from a leftmost point of the data set, we keep the points in the convex hull by anti-clockwise rotation.”

We used only one **ROM**. For being able to use only one ROM we needed two counters, one for the calculation of left-most point and the other one for the other one for calculation of orientation and updation.

We used two MUXs to create a two **5-bit data selector** which selects which indices should be given to the ROM. the select condition for the MUXs were whether we were performing left-most point calculation or orientation calculation.



Firstly we tried to figure out how to find the leftmost point and we observed that it can be done by only comparing the x-coordinates of points unless the x-coordinates are equal, in that case , we compared the y-coordinates.

The next step was to construct a circuit that would calculate the orientation of the points, for that matter we choose 9 registers and assigned (p_2, x'', y'') , (p_0, x', y') and (i, x_0, y_0) to them with appropriate loading conditions.

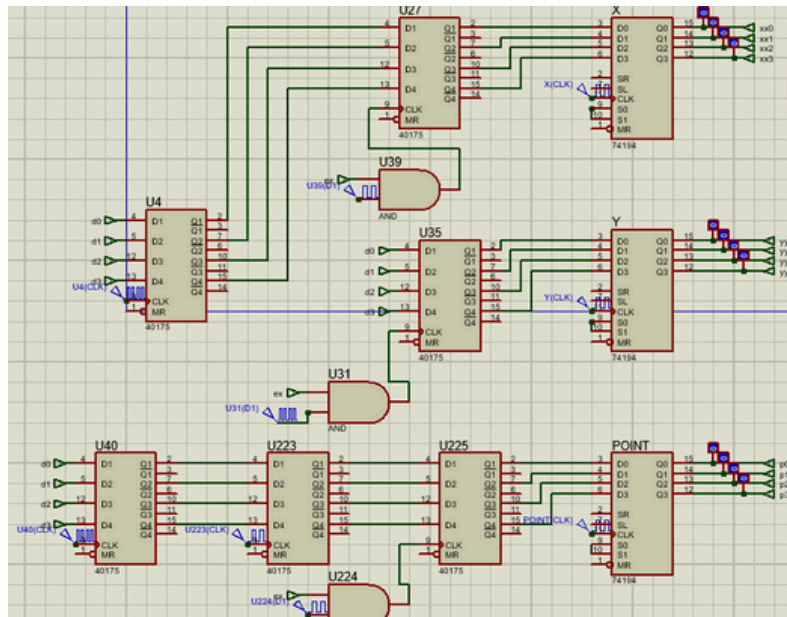
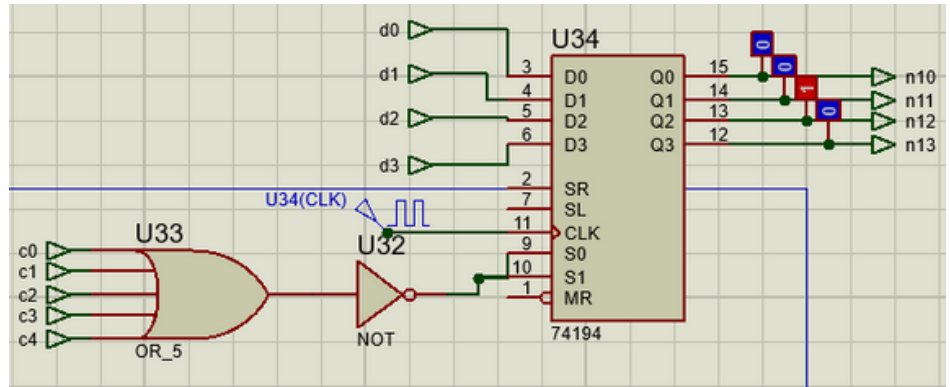
Our circuit consists of a subcircuit which deals with the collinear case as well, as in a strict convex hull, only the vertices are considered and not every point which lies on the edge of the hull. For creating this subcircuit, we again considered comparison of coordinates of points for deciding which one is farther.

In the end, for the circuit to stop, we considered the condition when the point number of the point on the convex hull is again equal to the point number of the leftmost point, the process is complete and the circuit stops.

SUBCIRCUIT MIN

We need the **number of coordinates** for any further action hence we created a circuit which stores the number of coordinates. The circuit consisted of a register which loads values only when the **count** is 0.

Here the number of coordinates were 4 hence the register stores value 4 (4'b0100).



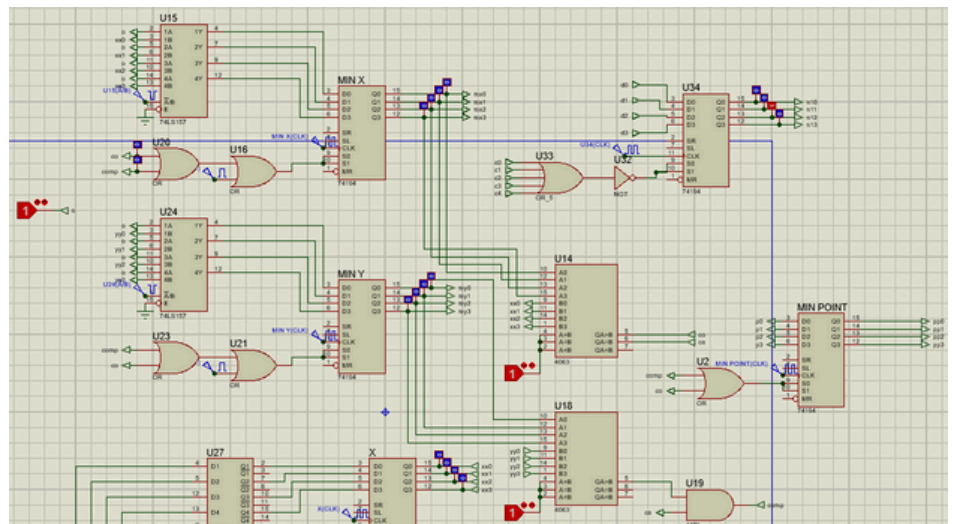
For a particular coordinate, first we obtain the point number, then the x-coordinate and then the y-coordinate, but we need all of them at once for any calculation hence we introduced a **delay of Two clock cycles** in point number and a **delay of one clock cycle** in x-coordinate.

For the registers to store only the entity they are supposed to store, we gave a pattern as clock to them which only loads the required values to a particular register. (point number, x-coordinate and y-coordinate to X,Y and POINT registers respectively).

For calculation of the left-most point firstly we stored value 15 (4'b1111, maximum possible value) in the MIN X, MIN Y registers so as when the comparison starts, the registers don't store any arbitrary trash value and the initial value in the registers are greater than any possible value the coordinates can take.

We compared the x-coordinates, if current x-coordinate is less than the x-coordinate stored in the MIN X register then MIN X, MIN Y and MIN POINT registers are updated with current values of (point number, x, y).

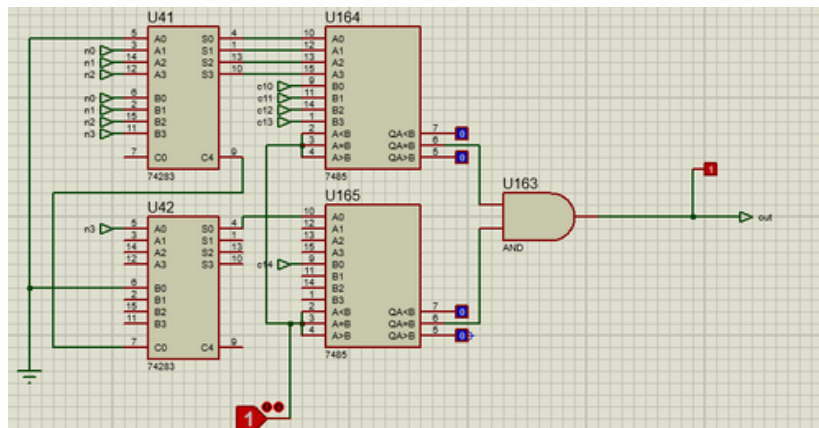
If the x-coordinates are equal then y-coordinates are compared and if current y is less than Min y then MIN X, MIN Y and MIN POINT are updated.



The process stops once the counter reaches the maximum index.

For the process to stop we need **maximum index**. Maximum possible is calculated in yet another sub-circuit **COUNT**.

SUBCIRCUIT COUNT AND COUNT2



We know that the maximum possible index will be three times the number of points hence we created a circuit to calculate **3*N**.

(N=number of points)

Firstly we left shifted N (obtained from min sub-circuit) so as to get 2*N and then added N to the result to get 3*N.

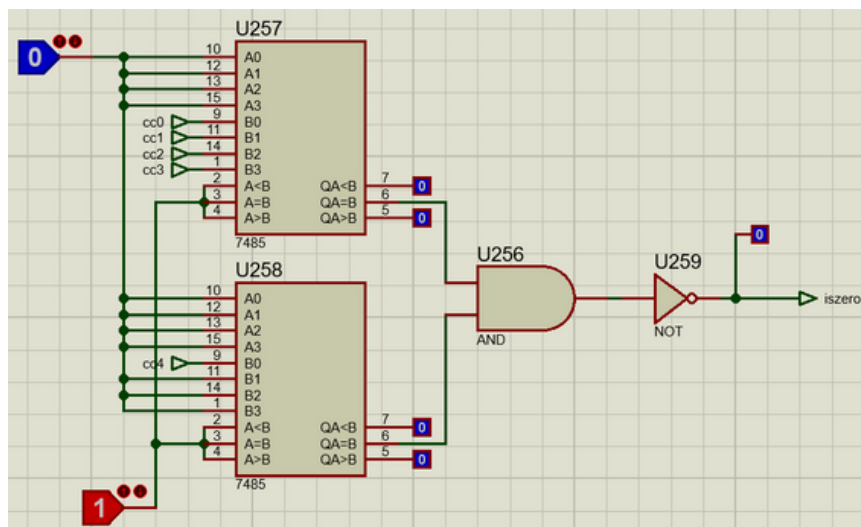
Then we compare the value of counter to

3*N, if the values are equal this means all the coordinates have been traversed and we got our bottom left point. now the counter for minimum point (left-most and bottom-most point) stops.

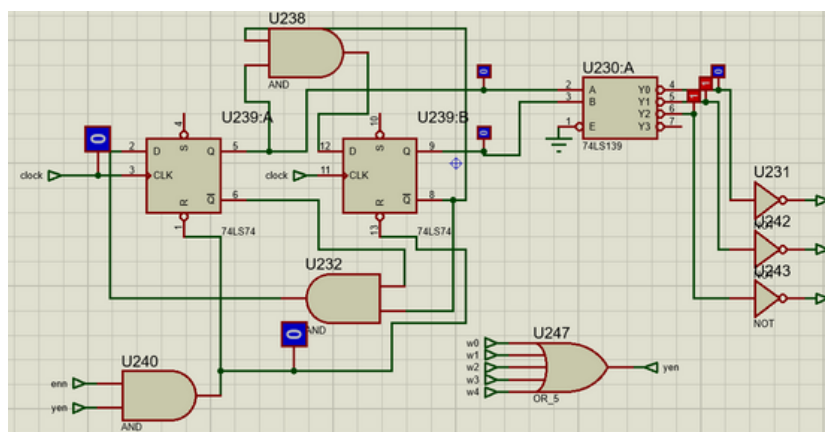
This circuit is present only in sub-circuit **COUNT2**, it compares the value of counter to **zero** and tells if the counter is zero or not.

We need to know if the counter is zero or not as we need to initialize the counter again and again (looping).

We didn't initialize the counter to 1 as it introduced unnecessary delay in the first traversal.



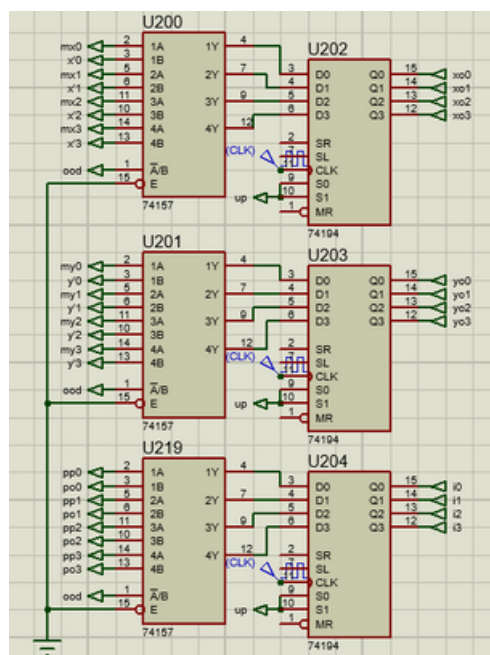
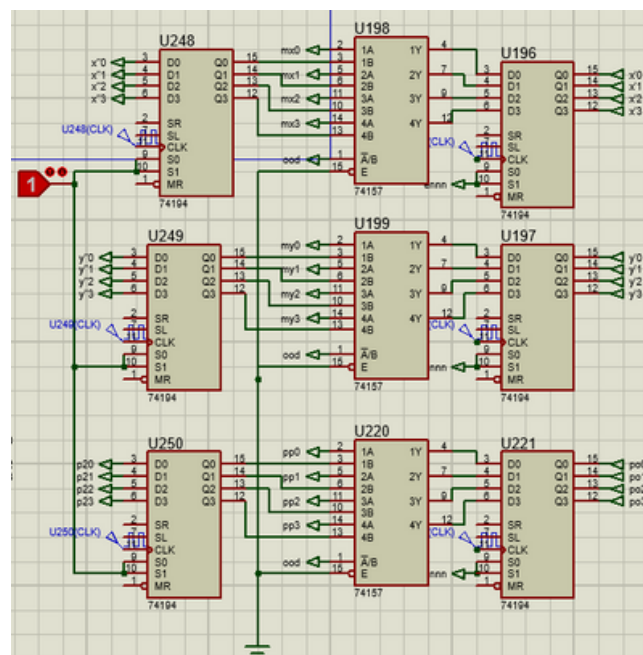
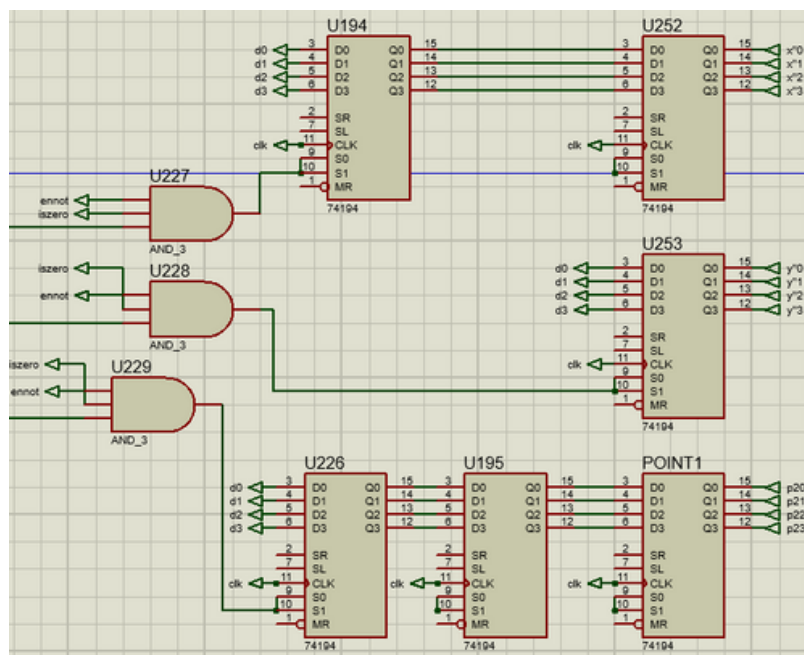
SUBCIRCUIT DECODE



This is the FSM of modulo 3.

This circuit is synchronized with the counter and outputs **mod0**, **mod1** or **mod2** in a cyclic manner. The circuit activates when the counter is active and the value is greater than zero, that is, it starts when the value of counter is 1 or more. These will be used to activate x,y and point registers

TERMS & NOMENCLATURE



(x_0, y_0) :- Reference point (Already on Hull).

(x', y') :- Point being checked to be on the vertex of convex hull.

(x'', y'') :- Every other point.

We included delays in the updation of x'' (one clock cycle) and point number **p2** (two clock cycles) so that (p_2, x'', y'') all are obtained at the same time.

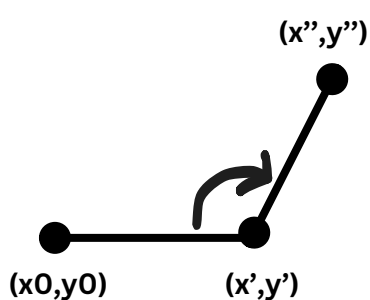
There is no need to include delays in loading of (p_0, x', y') and (i, x_0, y_0) as their updation depends on the orientation and various calculations being performed on (x'', y'') .

$$\text{Orientation} = (y' - y_0)(x'' - x') - (y'' - y')(x' - x_0)$$

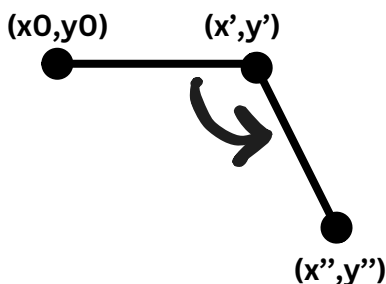
Orientation > 0 Clockwise

Orientation < 0 Counter clockwise

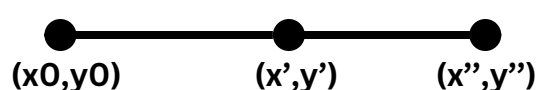
Orientation = 0 Collinear



Clockwise
Orientation > 0

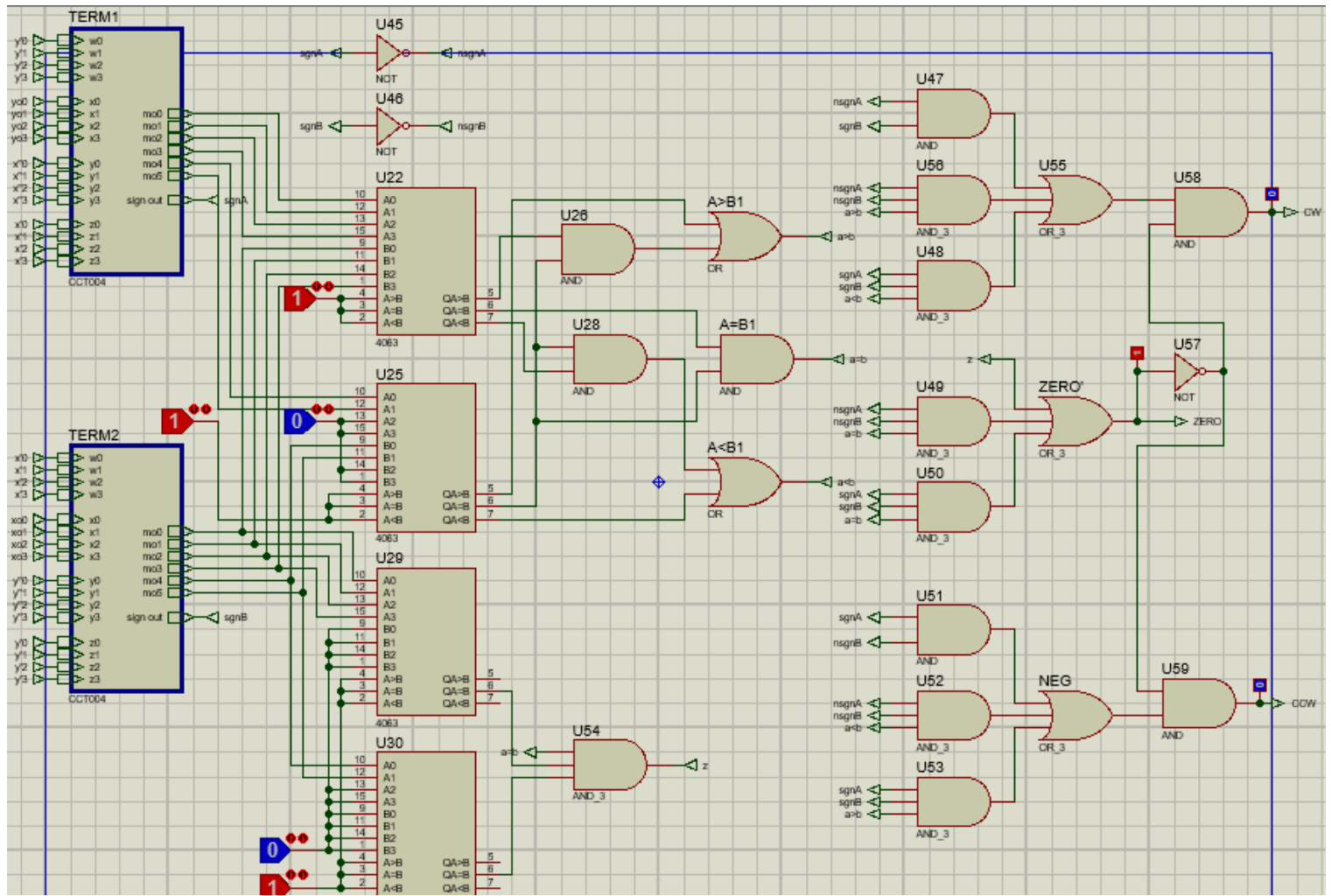


Counter Clockwise
Orientation < 0



Collinear
Orientation = 0

SUBCIRCUIT ORIENTATION



$$\text{TERM1} = (y' - y_0) * (x'' - x')$$

$$\text{TERM2} = (y'' - y') * (x' - x_0)$$

Let:-

$$a = \text{TERM1}$$

$$b = \text{TERM2}$$

$$\text{Orientation} = (y' - y_0) * (x'' - x') - (y'' - y') * (x' - x_0) = a - b$$

Now, if we consider the signs of **a** and **b** :-

a	b	a=b	a>b	a<b	Orientation
Positive	Negative	1	0	0	Positive
Positive	Negative	0	1	0	Positive
Positive	Negative	0	0	1	Positive
Negative	Positive	1	0	0	Negative
Negative	Positive	0	1	0	Negative
Negative	Positive	0	0	1	Negative
Positive	Positive	1	0	0	Zero (Positive)
Positive	Positive	0	1	0	Positive
Positive	Positive	0	0	1	Negative
Negative	Negative	1	0	0	Zero (Positive)
Negative	Negative	0	1	0	Negative
Negative	Negative	0	0	1	Positive

From the table, we observed that if **a** and **b** are of **opposite signs** then the orientation depends on the sign of **a** only, doesn't matter if **a** is greater than **b** or vice-versa, unless both are equal to zero, in that case we see whether **a** and **b** are equal or not and if **b** is equal to zero or not, here **Orientation** will become **ZERO**.

If **a** and **b** are of same signs then value of Orientation depends on whether **a** is greater than **b** or vice-versa. Orientation will be **ZERO** if both **a** and **b** are of same sign and are equal. If both are positive then orientation will be **Positive** if (**a>b**), and **Negative** if (**a<b**). If both are negative then orientation will be **Positive** if (**a<b**), and **Negative** if (**a>b**).

Hence, our circuit compares the signs of **a (TERM1)** and **b (TERM2)** and their magnitudes and computes whether **Orientation** will be **positive (Clockwise)** , **negative (Counterclockwise)** or **Zero**.

SUB-SUBCIRCUIT TERM1 & TERM2

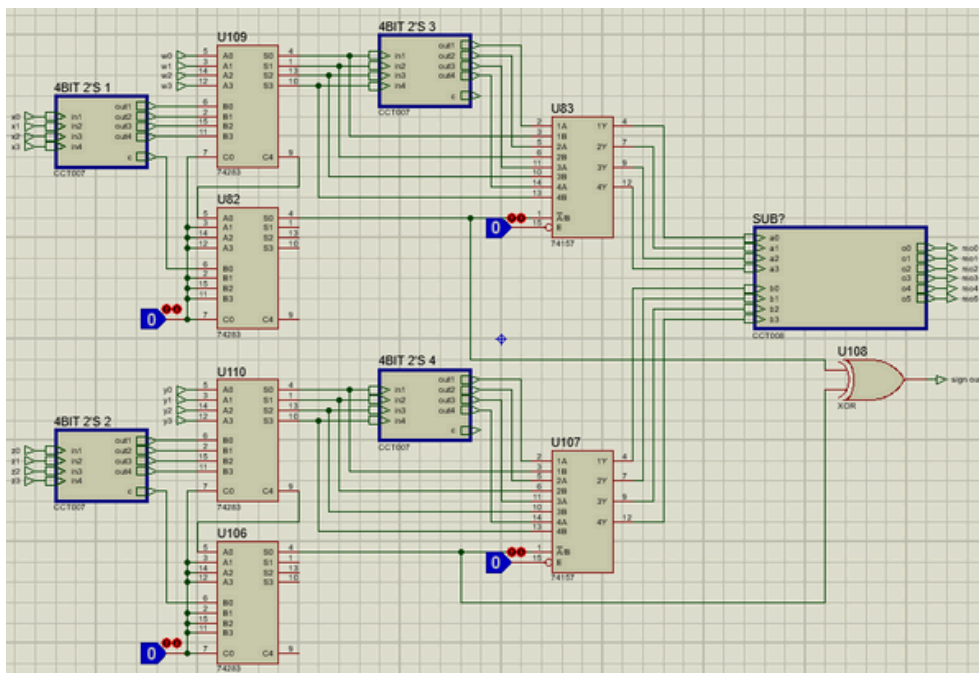
TERM1 = (y2-y1)(x3-x2) = c*d

If we consider calculation of **c (y2-y1)**:-

First we calculate the 2's complement of y1 (in the subcircuit **4BIT 2'S**) and then add it to y2.

Carry bit will tell us whether the subtraction resulted in positive value (in 2's complement form) or negative value.

We multiply the magnitude of **c** and **d** in the subcircuit **SUB?** and the sign of the product depends



on the signs of **c** and **d** , if both are of same sign then the product will be positive and if they are of opposite signs then the product will be negative.

We used XOR gate to obtain the sign of the product.

SIGN:-

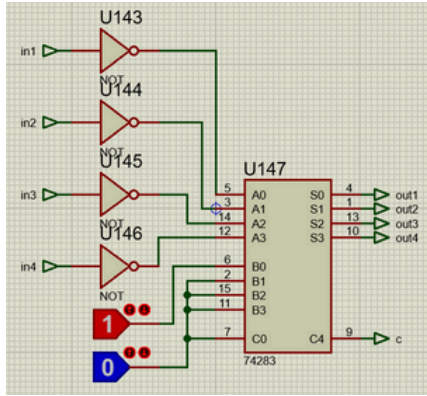
Positive = 0

Negative = 1

To calculate the magnitude of subtraction, when result is positive, we pass the result

to **4BIT 2'S** subcircuit to again take 2's complement. We pass the 2's complement of the result and the actual result to a MUX whose select line is the carry of the adder. The MUX outputs the magnitude of the result of subtraction to the **SUB?** subcircuit for multiplication.

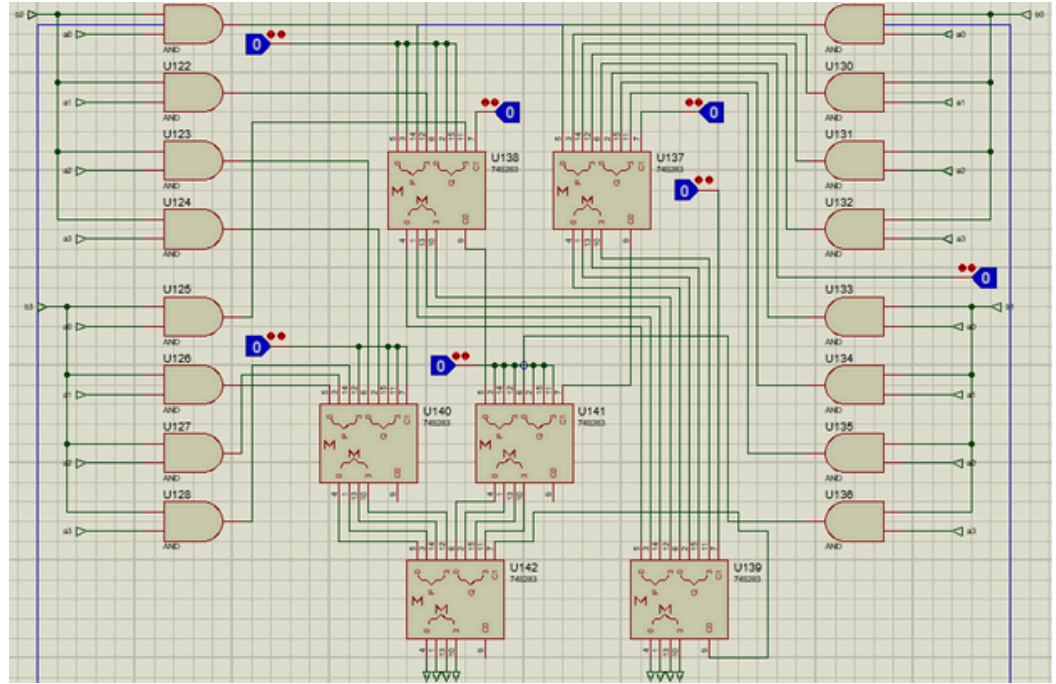
SUB-SUB-SUBCIRCUIT 4BIT 2'S



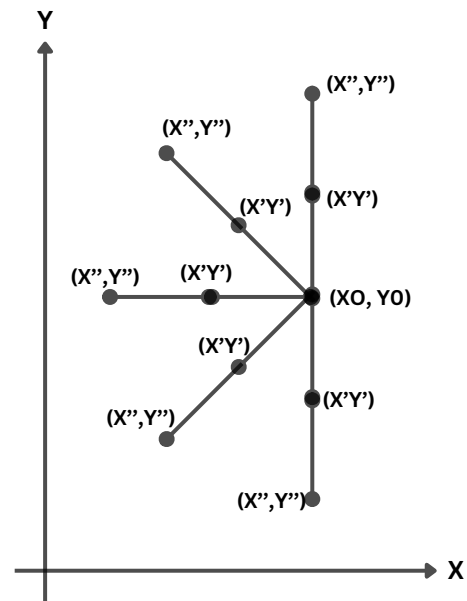
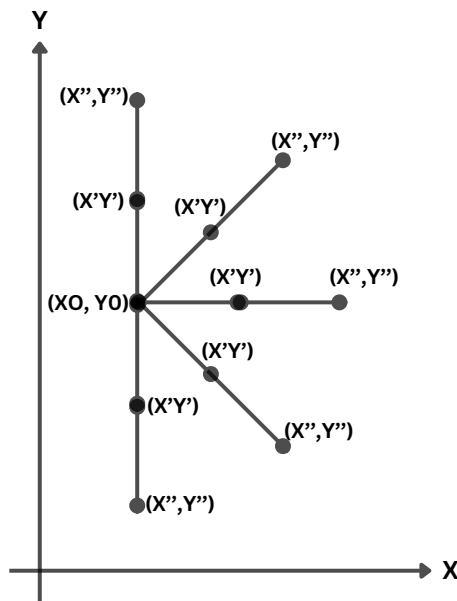
4BIT 2'S calculates 2's complement of a number by inverting it's bits and then adding 1 to it.

SUB? multiplies two 4 bit numbers.

SUB-SUB-SUBCIRCUIT SUB? (MULTIPLICATION)



SUBCIRCUIT COLLINEAR CASE

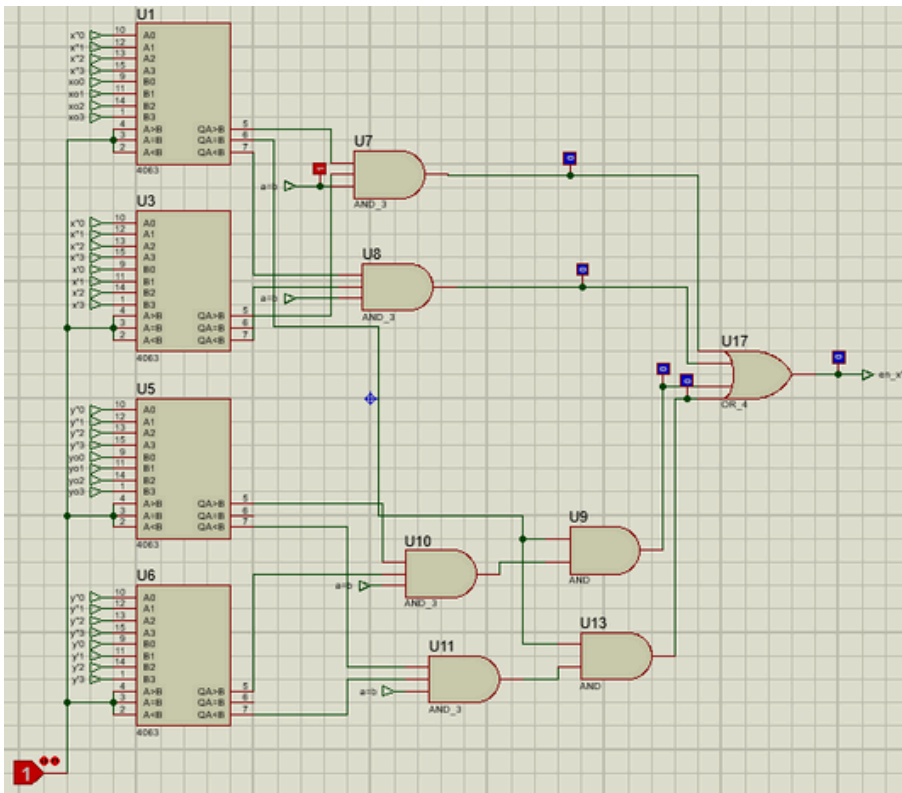


When **Orientation** is **ZERO**, that is, when points are collinear, then (x', y') should be updated only when (x'', y'') is farther from (x_0, y_0) than (x', y') , this is possible for cases shown above.

if $(x'' > x_0)$
if $(x'' > x')$
update (x', y')

if $(x'' < x_0)$
if $(x'' < x')$
update (x', y')

if $(x'' = x_0)$
if $(y'' < y_0)$
if $(y'' < y')$
update (x', y')
if $(y'' > y_0)$
if $(y'' > y')$
update (x', y')



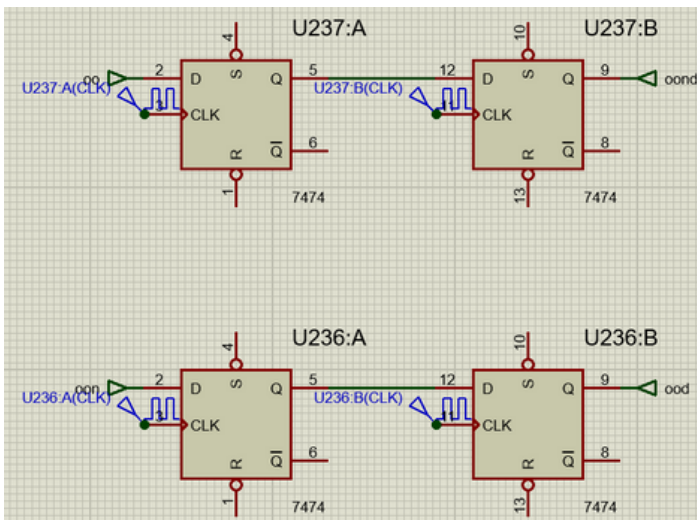
The subcircuit COLLINEAR CASE configures whether (x'', y'') is farther from (x_0, y_0) than (x', y') or not.

The subcircuit operates/is active only when **Orientation** is **ZERO**.

The circuit consists of four magnitude comparators and a combination of their outputs.

(x', y') will be updated if **Orientation** is either **clockwise** or if (x'', y'') is farther from (x_0, y_0) when **Orientation** is **ZERO**.

UPDATE CONDITIONS



oon = count (for min / leftmost) calculation is equal to **3*N**

oo = NOT(**oon**)

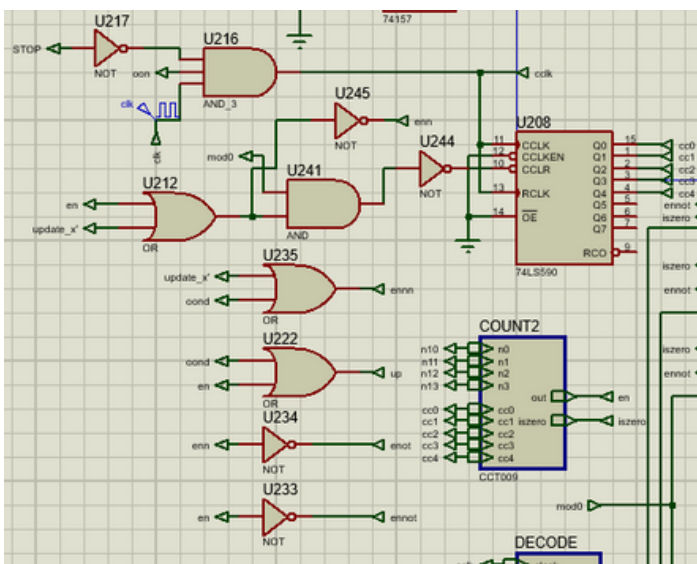
ood = **oon** delayed by one clock cycle

oond = **oo** delayed by one clock cycle

en = count2 (for Orientation calculation) is equal to **3*N**

iszero = is count2 equal to zero or not

update_x' = condition for updating (x', y')



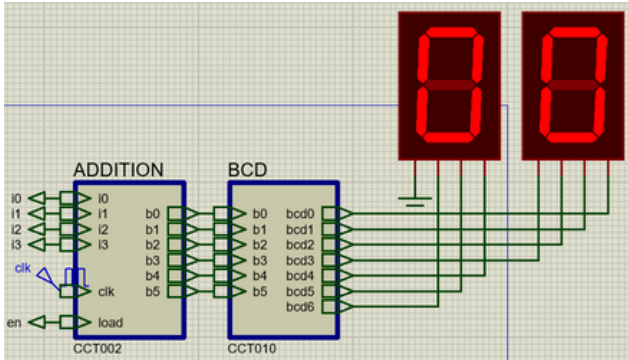
ennn = if min calculation is complete or not and should (x', y') be updated or not

up = condition for updating (x_0, y_0) , if count2 is equal to **3*N** or **oond** (to update initial value of (x_0, y_0))

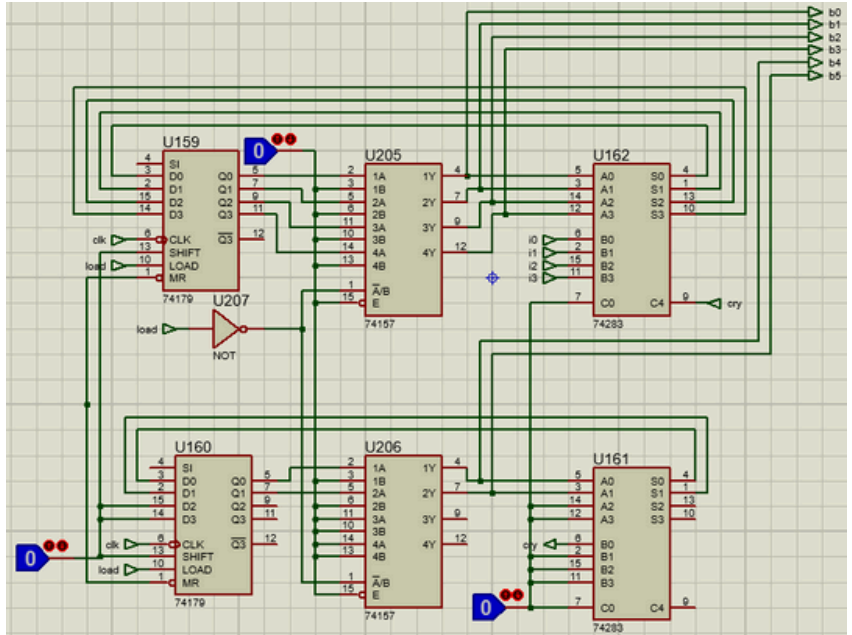
enot = NOT(**enn**)

ennot = NOT(**en**)

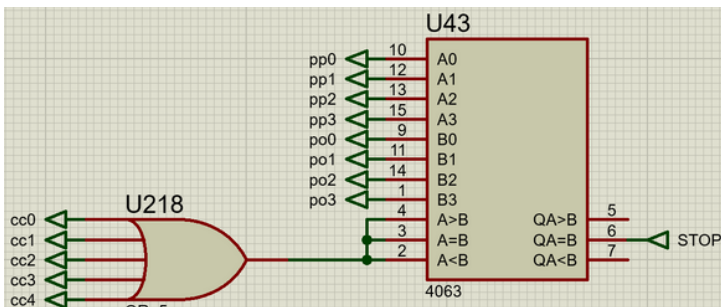
SUBCIRCUIT ADDITION



The subcircuit ADDITION consists of adders and registers. the circuit is active whenever (x_0, y_0) is updated. The circuit add the point number corresponding to the new (x_0, y_0) and the result is stored in a register. There is a feedback which adds the new point number to the sum of were on convex hull.



STOP



This part of the circuit decides when the whole circuit should stop.

This is a comparator which compares when the updated point number (which updates whenever (x_0, y_0) updates) is equal to the point number of the leftmost-bottommost point.

Bill Of Materials for WorkingJMA

Design Title WorkingJMA
Author
Document Number
Revision
Design Created 11 March 2025
Design Last Modified 11 March 2025
Total Parts In Design 267

255 Integrated Circuits

Quantity	References	Value	Stock Code	Unit Cost
12	U1, U3, U5-U6, U14, U18, U22, U25, U29-U30, U43, U244	4063		₹2.00
23	U2, U16, U20-U21, U23, U158, U167-U168, U171-U172, U175-U176, U179-U180, U183-U184, U187-U188, U191-U192, U212, U222, U235	OR		₹0.10
6	U4, U27, U35, U40, U223, U225	40175		₹4.00
15	U7-U8, U10-U11, U48-U50, U52-U54, U56, U216, U227-U229	AND_3		₹0.20
61	U9, U13, U19, U26, U28, U31, U36, U39, U47, U51, U58-U59, U84-U99, U121-U136, U163, U166, U170, U174, U178, U182, U186, U190, U213, U218, U224, U232, U238, U240-U241, U246, U256	AND		₹0.10
1	U12	2732		₹75.00
2	U15, U24	74LS157		₹2.00
1	U17	OR_4		₹0.30
45	U32, U38, U45-U46, U57, U67-U70, U72-U77, U79-U80, U111-U114, U116-U119, U143-U146, U148-U151, U153-U156, U207, U231, U233-U234, U242-U243, U245, U259	NOT		₹0.10
2	U33, U247	OR_5		₹0.40
12	U34, U194-U197, U202-U204, U221, U226, U252-U253	74194		₹2.00
2	U37, U208	74LS590		₹2.00
22	U41-U42, U44, U60, U62, U65-U66, U71, U78, U81-U82, U106, U109-U110, U115, U120, U147, U152, U157, U161-U162, U211	74283		₹2.00
1	U55	OR_3		₹0.20
14	U61, U63, U83, U107, U198-U201, U205-U206, U209-U210, U219-U220	74157		₹2.00
2	U64, U108	XOR		₹0.10
12	U100-U105, U137-U142	74S283		₹2.00
2	U159-U160	74179		₹2.00
6	U164-U165, U214-U215, U257-U258	7485		₹2.00
7	U169, U173, U177, U181, U185, U189, U193	74HC283		₹2.00
3	U217, U236-U237	7474		₹4.00
1	U230	74LS139		₹2.00
2	U239, U248	74LS74		₹2.00
1	U251	7475		₹0.20
Sub-totals:				₹316.60

12 Miscellaneous

Quantity	References	Value	Stock Code	Unit Cost
2	A<B1,A>B1	OR		₹0.10
1	A=B1	AND		₹0.10
7	MIN POINT,MIN X,MIN Y,POINT,POINT1,X,Y	74194		₹2.00
2	NEG,ZERO'	OR_3		₹0.20
Sub-totals:				₹14.70

Totals: ₹331.30

We use one clock (which is instantiated a few times but has same frequency).
We used a pulse as well, in the Subcircuit MIN.

Including their cost in the bill:-

BOM	Rs. 331.30
Clock	Rs. 80
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TOTAL	Rs. 411.30