Message from the General Chairs

January is VLSID season, and is that time of the year when the VLSI and Embedded community sets off on the annual pilgrimage to our beloved conference. It gives us great pleasure to welcome you to the 32nd International Conference on VLSI Design and 18th International Conference on Embedded Systems at New Delhi, India. The conference has a long and illustrious history, and it is our honour to bring back the conference to Delhi after a decade.

In addition to the traditional pillars of an exciting technical program, comprehensive tutorials, keynote addresses from technology luminaries, and several other forums that have come to define VLSID, we have several innovations this year. The second day of the main conference is dedicated as "*Women in Engineering Day"* featuring a series of presentations and a plenary panel discussion on a variety of topics ranging from contributions of women leaders in technology to challenges in gender equality. This is the *first Go Green* edition of the Conference, which has resulted in several innovations you would notice around you in the conference. The extremely popular fellowship program, where the conference provides financial support to academics for attending the conference, has drawn for the *first time ever* participation from *every state of India*. The *Startup Forum* focuses on the challenges and opportunities in the thriving startup ecosystem. We hope that these become annual fixtures in future conference editions.

This year's conference theme is *Autonomous Intelligence for a Secure, Safer, and Smarter World*, and we have several keynotes, panels, and technical sessions exploring different aspects of this important theme. Numerous tracks and threads weave their way into a rich tapestry called VLSID that offers something new to everyone - new open questions to the researcher, new products and concepts to the engineer, a glimpse into new strategic directions to the policy maker, new tutorials to the curious student, and new platforms for interaction to all of us.

We would like to acknowledge the huge contributions of a very large team of volunteers from various institutions and companies that made this conference possible, including the Advisory Board and Steering Committee, Organising Committee, Technical co-sponsor IEEE-CPS (Patrick Kellenberger and Lisa O'Conner), IEEE-CS (Jennifer Carruth), ACM (Diana Brantuas), and our dedicated student volunteers, with a special thanks to Steering Committee Chair Vishwani Agrawal, Executive Chair Sanjay Gupta, and VSI liaisons Satya Gupta and Jaswinder Ahuja.

The Manekshaw Centre is a unique convention facility in many ways; we hope you enjoy the artwork on display. New Delhi is a travellers' paradise, and offers a brilliant fusion of traditional and contemporary India. The rich Indian cultural heritage is showcased through a breathtaking array of sights for the visitor, ranging from ancient forts and monuments such as Red Fort and Qutb Minar to expansive modern parks, gardens, and shopping malls. New Delhi is one of the few cities in the world with three UNESCO World Heritage sites. Non-VLSI delights are embedded everywhere in the capital. Don't miss the food!

Preet Yadav   *Preeti Ranjan Panda*

Wipro Limited   *Indian Institute of Technology Delhi*
General Chairs, VLSID 2019
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