

Message from the Technical Program Chairs



Welcome to New Delhi and the 2019 edition of the flagship conference on VLSI Design and Embedded Systems held annually in India for over 3 decades now.

This is the age of artificial intelligence (AI) and internet-of-things (IoT). The exponential and disruptive growth of AI and machine learning is impacting development in a myriad of disciplines, VLSI being one of the major ones. The evolution of IoT systems illustrates how VLSI and intelligence can synergize to foster the growth of both autonomous intelligence and embedded systems for a wide variety of application domains. Increasingly, intelligent systems are rivalling and in some cases replacing human intelligence, and evolving towards autonomous intelligent systems to enable a safe, secure and connected communities.

The technical program this year reflects this trend, and include regular papers as well as tutorials that focus on autonomous intelligence, smart devices, secure systems and IoT. The first two days of tutorials are succeeded by a three-day technical program that features a number of keynotes, panel discussions, and three parallel paper tracks on each day. The papers span a very wide range of topics and reflect the breadth and depth of VLSI Design and Embedded Systems.

The conference received 316 regular paper submissions spread over 12 subject categories. More than 1200 reviews were submitted by our team of 169 technical program committee members from all across the world, led by our 24 track chairs. The papers were selected by a double-blind review process. After detailed discussion on each of the submissions, 84 high quality papers (26% acceptance rate) were selected for presentation at the conference. This year's program also includes 28 interactive (poster) presentations.

Welcome again to New Delhi, and hope you enjoy the Delhi winter and its wide range of cultural and culinary experiences, while you are here to savor the delights of the 32nd International Conference on VLSI Design and the 18th International Conference on Embedded Systems!

With warm regards,

Prabhat Mishra

Jayadeva

University of Florida, USA Indian Institute of Technology Delhi, India

Technical Program Co-Chairs, VLSID 2019