VLSI Design Conference Steering Committee (2018)

Vishwani D. Agrawal, Chair, Auburn University, USA

Jaswinder Ahuja (Past President, VSI), Cadence, India

Srimat T. Chakradhar, NEC, USA

Annajirao Garimella, Intel, USA

Manoj S. Gaur (VDAT), IIT, Jammu, India

Dasaradha Gude, INVECAS, Santa Clara, USA

Gangadhar Gude, INVECAS, Hyderabad, India

Satya Gupta (President, VSI), SenzOpt, Bangalore, India

Chitra Hariharan (Secretary, VSI), SenzOpt, Bangalore, India

Ritesh Jain (Treasurer), Seagate, Pune, India

Nagi Naganathan (ACM and IEEE), Broadcom, USA

Preeti Ranjan Panda (Embedded Systems), IIT Delhi, India

Niranjan Pol, Seagate, Pune, India

Nagarajan (Ranga) Ranganathan, University of South Florida, USA

Sathya Sargunesan (Past Treasurer, VSI), ARM, India

Virendra Singh (RASDAT), IIT Bombay, India

Susmita Sur-Kolay, Indian Statistical Institute, India