Message from the Executive Chair

Today Intelligent autonomous systems are playing an increasing role in both engineering and day-to-day applications. For example, assembly lines, construction sites and even homes are employing autonomous robots, which is resulting in a big impact to our daily life. As another example, a computer program has beaten humans at the long-standing games of chess and GO. This only illustrates how new technologies and application domains are pushing forward the need for research and development, resulting in new challenges to be overcome so that Intelligent Autonomous Systems can be applied in a reliable and user-independent way. Recent advances in the areas of Artificial Intelligence, Machine Learning and Adaptive Control have created unprecedented new possibilities across the spectrum of Autonomous Systems to perform intelligently in a safe, skilful and robust manner.

That is what makes the 32nd International Conference on VLSI Design and 18th International Conference on Embedded Systems particularly exciting. This edition of the conference will provide an interdisciplinary platform for engineers, scientists, students and researchers to present and discuss the most recent trends and innovations as well as challenges encountered and solutions adopted in the field of Autonomous Systems. The backbone of the conference remains the visionary keynotes, technical paper tracks, women in engineering forum, panel discussions and tutorials, among others.

As you may be aware, VLSID is not only the oldest and longest running industry conference, but it also attracts the most exhibitors in the industry. Needless to say, an international conference of this scale cannot be successfully organized without the support and great voluntary efforts of many people and organizations. And considering that 2018 has been a busy and fun year for the entire organizing committee of VLSID Conference, I would like to sincerely thank my fellow Executive Committee Members, Mr. Vishwani Agrawal from Auburn University; Preet Yadav from Wipro Limited and Professor Preeti Ranjan Panda from IIT Delhi and all other organizing committee members, industry partners and others, for their contributions to the conference, our collaboration partner IESA and technical co-sponsors, IEEE and ACM. I would also like to extend a special thanks to Jaswinder Ahuja for guiding us with his vast industry knowledge.

On behalf of the VLSID Executive Committee, I am pleased to welcome you to the historically and culturally rich city of New Delhi. Mark your calendar and be sure to connect with industry experts and leaders from January 5-9, 2019. I look forward to seeing you there!

With warm regards,

Sanjay Gupta

Sanjay Gupta,
Chair, VLSI Design Conference Executive Committee
VP & India Country Manager, NXP Semiconductors
December 11, 2018