

Message from the Organizing Chair



At the outset wish you a very happy and prosperous new year 2019.

It is an honor and privilege for me to welcome you on behalf of the VLSID & ES 2019 Organizing Committee to the 32nd International Conference on VLSI Design and the 18th International Conference on Embedded Systems being held from 5th – 9th January 2019 at the Manekshaw Centre, New Delhi.

The VLSID & ES conference has come a long way since its modest inception in 1985, and over the years, emerged as the premier conference of its kind on Indian soil. We, the Organizing Committee members of VLSID & ES 2019, are grateful to the VLSI Society of India and the organizers, participants and sponsors of every preceding year for passing on a legacy that has become the signature hi-tech international conference from India.

The responsibility on the shoulders of the 2019 Organizing Committee has been unprecedented, to say the least, not only because we ought to carry forward the hard-earned legacy of the conference, but also propel it further with added novelty, originality and panache. I am glad to share that this year's program is resplendent with unmissable visionary Keynotes, thought-provoking Panel discussions, highly meritorious Papers and Interactive presentations, a deeply insightful Tutorial program, an exciting Design Contest and a Hackathon, and a multitude of theme-based Forums. I am highly confident that the 2019 Program will inspire a new wave of thought leadership and technical excellence in the Indian VLSI & ES ecosystem.

The 2019 conference is particularly special for the Delhi National Capital Region as the conference is returning to the capital after a decade. Personally, it is nostalgic for me to have been part of the prestigious conference in Delhi in 2003 and 2009 as a participant, and in 2019 as one of the Organizers. The choice of venue is also unique this year: the Manekshaw Centre is not only symbolic of Indian pride and patriotism, but also gives a good break from the usual hotel setting while still offering all the comfort and conveniences of the usual venues. Being close to the airport, it is convenient to our outstation participants as well, while being easily accessible by the participants native to NCR and the Northern region in general. We are grateful to the VLSI Society of India and its Steering Committee for their guidance and direction throughout, and for providing us the opportunity to host the conference in Delhi this year.

The theme for VLSID & ES 2019 “Autonomous Intelligence for a Safe, Secure and Smart World” resonates with the global technological trends wherein the Internet of Things, Autonomous Driving, Vehicle Electrification, Machine Learning and Artificial Intelligence have caught the imagination of technology innovators and investors worldwide. I believe that the thoughts and ideas exchanged during the event shall reinforce the synergy among all stake holders (industry, academia, government and media) and provide tremendous impetus to the Indian VLSID & ES ecosystem in creating a new wave of innovation in India yielding leading-edge products and solutions for global technological needs.

To conclude, I would like to congratulate and sincerely thank the entire 2019 Organizing Committee, all the esteemed Chairs, Advisory Board members, Sponsors, Exhibitors, Partners and the Government of India departments without whose support and contribution this would not have been possible.

We hope that you find the event and its proceedings useful. We look forward to welcoming and interacting with you during the event, and hearing from you about your experiences and feedback. We look forward to your enthusiastic participation and support in making this yet another benchmark event in the history of the conference. Thank you.

With Warm Regards and Best Wishes,

Bedanta Choudhury
Organizing Chair