

VLSID 2018 Best Paper Awards

Arun Kumar Choudhury Best Paper Award

Combined Inference and Satisfiability Based Methods for Complete Signal Restoration in Post-Silicon Validation

Xiaobang Liu and Ranga Vemuri

University of Cincinnati

Nripendra Nath Biswas Best Student paper award

Mitigating Aperture Error in Pipelined ADCs Without a Front-end Sample-and-Hold Amplifier

Diego James, Abishek T. Kunnath, A. Purushothaman and Bibhu Datta Sahoo

Amrita Vishwa Vidyapeetham University, University of Illinois at Urbana-Champaign

Honorable Mention Award

A 1.2 pJ/cycle KHz Timer Circuit for Heavily Duty-Cycled Systems

Manikandan RR, Vipul Singhal, Rajat Chauhan, Vinod Menezes and Mahesh Mehandale

Indian Institute of Science, Bangalore, Texas Instruments India Pvt. Ltd.