

① Row and column major Array addressing :-

• Row major :-

$$\Rightarrow a[n_1][n_2] \dots [n_k]$$

\Rightarrow To access $a[i_1][i_2] \dots [i_k]$, we have to get content of address :-

$$\star \boxed{a + ((i_1 - low_1) \times n_2 \times n_3 \times \dots \times n_k + (i_2 - low_2) \times n_3 \times \dots \times n_k + \dots + (i_k - low_k)) \times W}$$

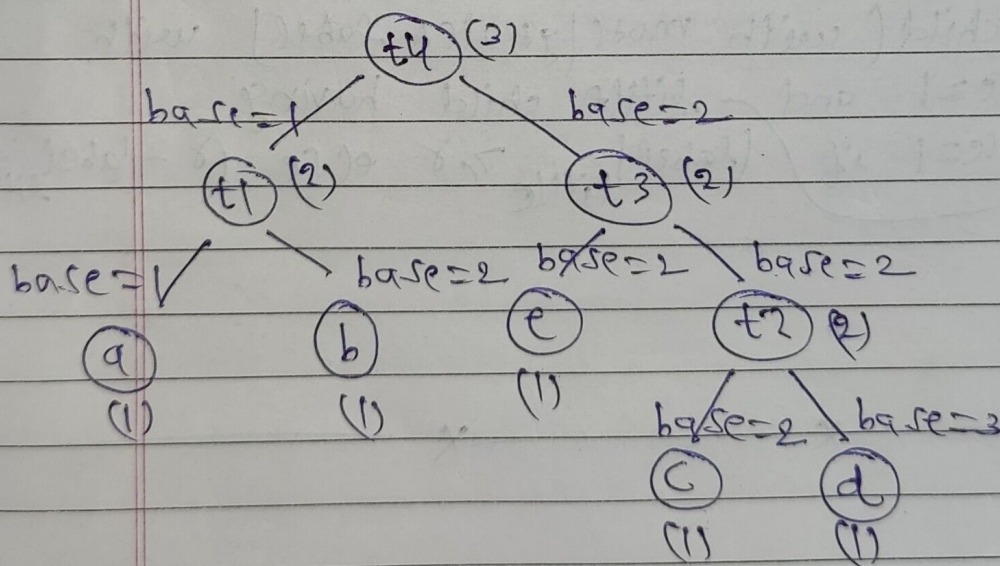
$\Rightarrow W$: width (bytes) in array of each element

• Column Major :-

$$\star \boxed{a + ((i_1 - low_1) + (i_2 - low_2) \times n_1 + \dots + (i_k - low_k) \times n_1 \times n_2 \times \dots \times n_{k-1})) \times W}$$

② Ex: Show Number (sufficient Registers) :-

base = 1



LD R3, d

LD R2, c

OP₁ R3, R2, R3

LD R2, e

OP₂ R3, R2, R3

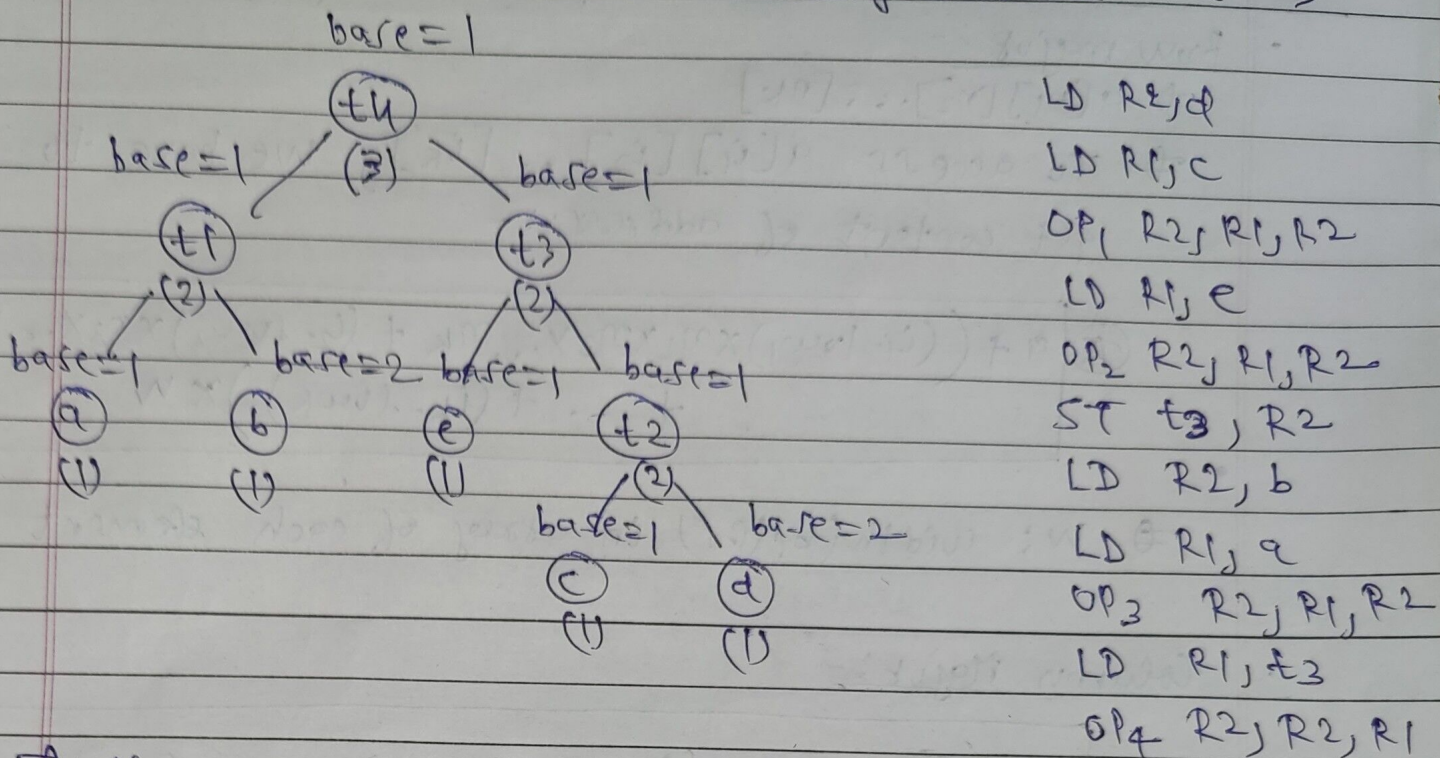
LD R2, b

LD R1, a

OP₃ R2, R1, R2

OP R3, R2, R3

3. Ershov Number (in sufficient registers) :- ($r=2$)



⇒ If we are node N ,

(i) $(Label)_N \leq r \rightarrow$ apply the original algo. for base calculation of left & right node

(ii) $(Label)_N > r \rightarrow$ If any of them (child) have $\geq r$ label, then select a

[In both the cases, result will appear in R_2]

big child (with most/greatest label) with $base=1$ and little child having $base=1$ if $(Label)_{little} \geq r$ else $(r - label_{little})$

x x x

④. SDD for XOR :- (Intermediate Code gen)

$\Rightarrow B_1 \wedge B_2 \equiv (!B_1 \text{ and } B_2) \parallel (B_1 \text{ and } !B_2)$

$\Rightarrow B \rightarrow B_1 \wedge B_2 \{$ $B_1.\text{true} = \text{new label}();$
 $B_1.\text{false} = \text{new label}();$

$B_2.\text{false} = B_1.\text{true}$

$B_2.\text{true} = B_1.\text{true}$

$b_3 = \text{new Boolean}();$

$b_3.\text{code} = B_1.\text{code}$

$b_3.\text{false} = B_1.\text{false}$

$b_3.\text{true} = \text{new label}();$

$b_4 = \text{new Boolean}();$

$b_4.\text{code} = B_2.\text{code}$

$b_4.\text{false} = B_1.\text{true}$

$b_4.\text{true} = B_1.\text{false}$

$B.\text{code} = B_1.\text{code} \parallel \text{gen}(B_1.\text{false};) \parallel$

$B_2.\text{code} \parallel \text{gen}(B_1.\text{true};) \parallel$

$b_3.\text{code} \parallel \text{gen}(b_3.\text{true};) \parallel$

$b_4.\text{code}$

}

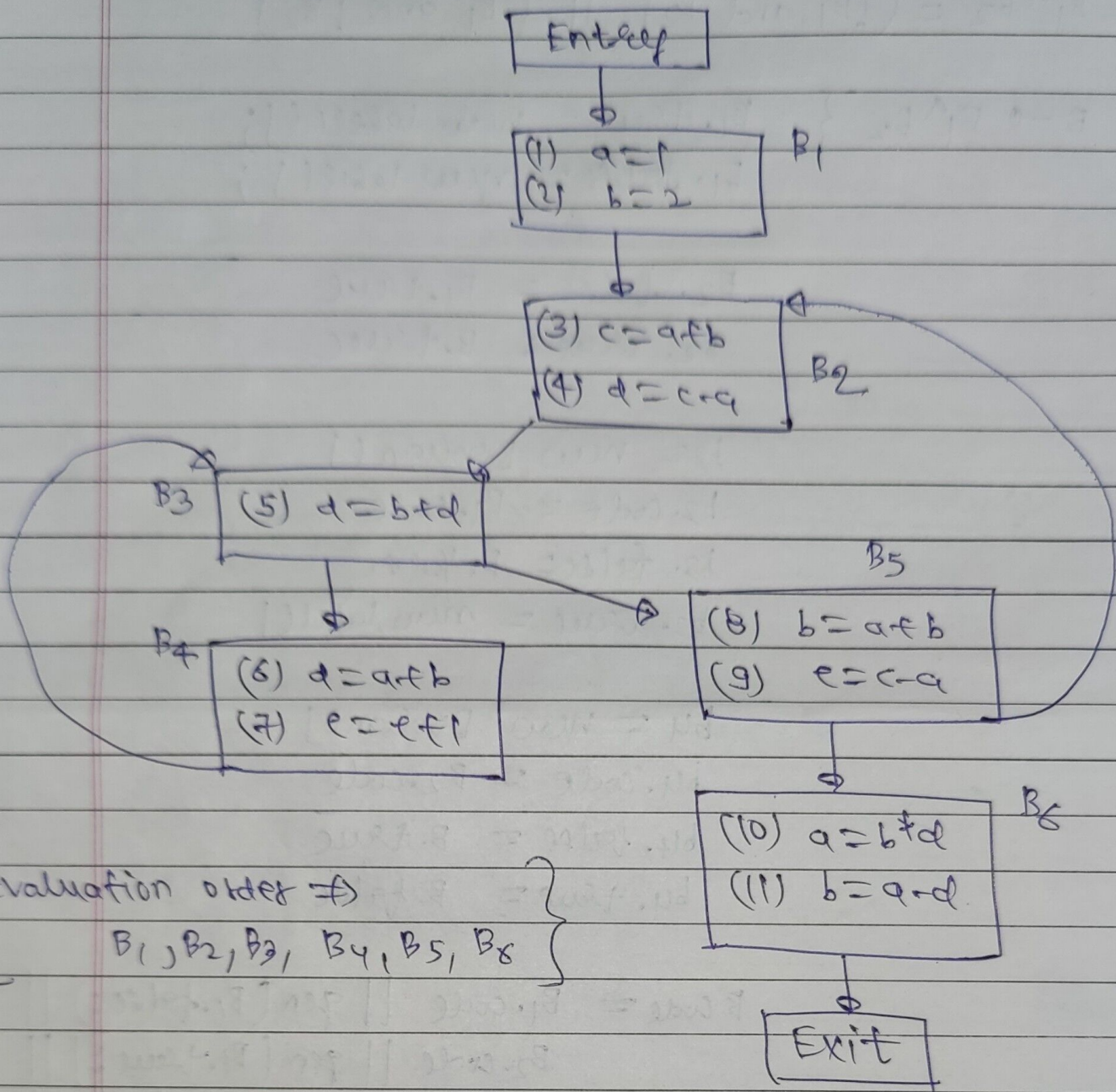
x

x

x

x

⑤ Reaching definition :- (Forward Analysis)



Evaluation order \Rightarrow

$\{ B_1, B_2, B_3, B_4, B_5, B_6 \}$

Block	Kill	Gen	In	Out	In	Out
B ₁	4, 5, 6, 11	1, 2
B ₂	5, 6	3, 4
B ₃	4, 6	5
B ₄	4, 5, 9	6, 7
B ₅	2, 7, 11	8, 9
B ₆	1, 2, 8	10, 11

⑥ Available Expression :- (Forward Analysis)

	E-kill	E-gen	
B ₁	ABCEF	ϕ	A: a + b
B ₂	CEF	AB	B: c - a
B ₃	CEF	ϕ	C: b + d
B ₄	CDEF	A	D: e + 1
B ₅	ACDE	B	F: a - d
B ₆	ABCE	F	

X X X X

⑦ live variable analysis : Backward analysis

• Evaluation order \Rightarrow

B₆, B₅, B₄, B₃, B₂, B₁

	use	def
B ₁	ϕ	a, b
B ₂	a, b	c, d
B ₃	b, d	d
B ₄	a, b, e	d, e
B ₅	a, b, c	b, e
B ₆	b, d	a, b

Ans

X X X X X X