

Further Infos: <http://hci.rwth-aachen.de/fabscan>

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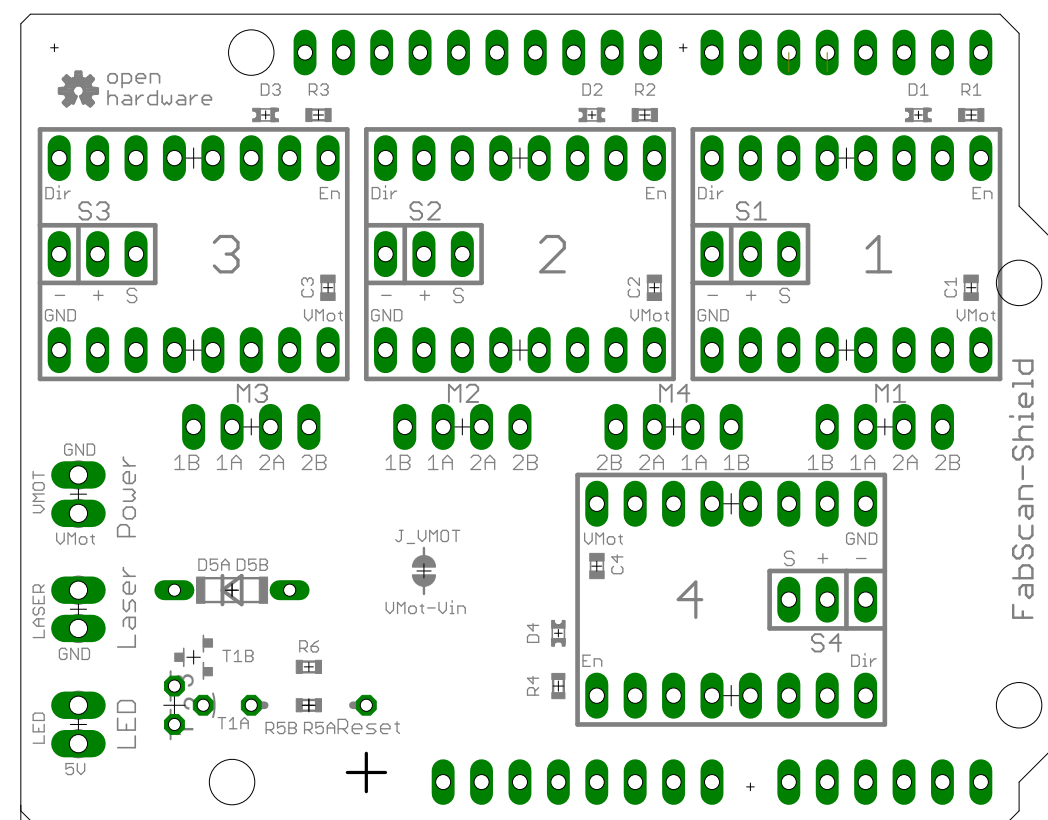
Designed by Andreas Walterott (Walterott electronic)
Based on the work of Francis Engelmann and Sebastian Setz



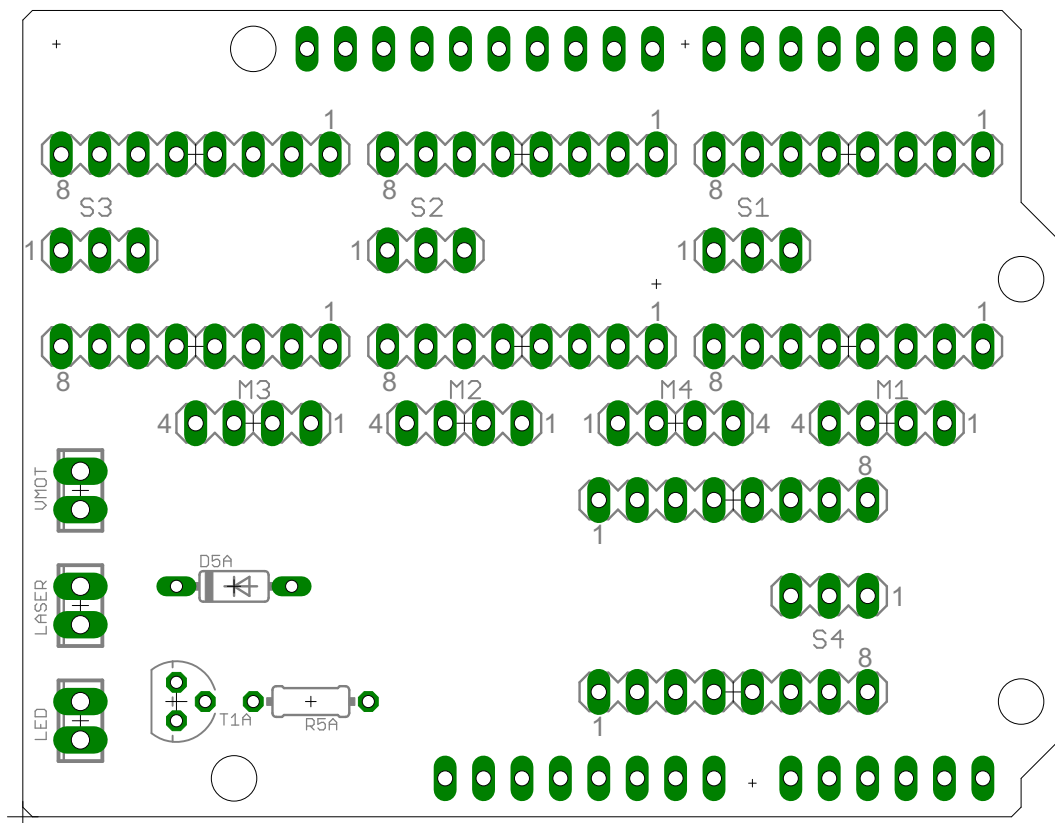
FabScan-Shield_v11

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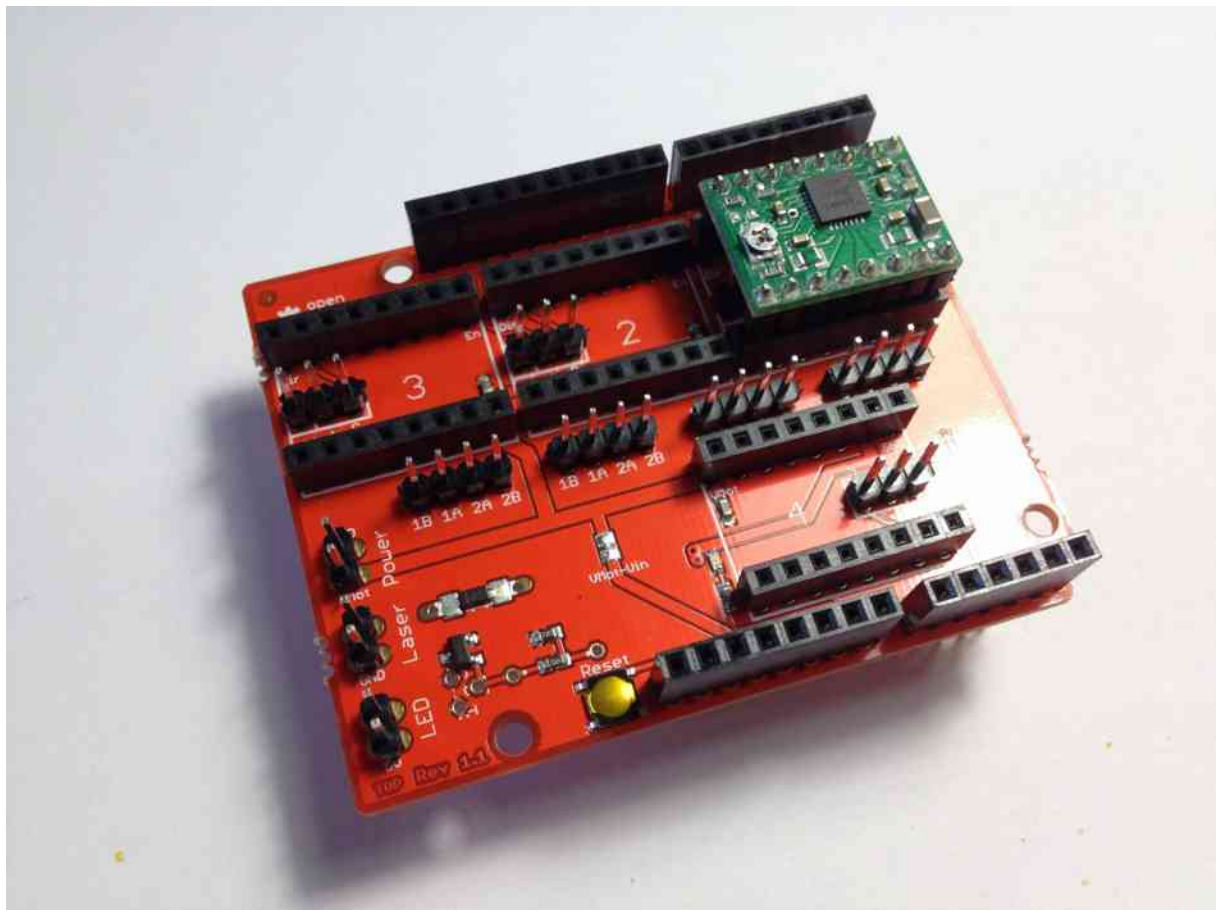
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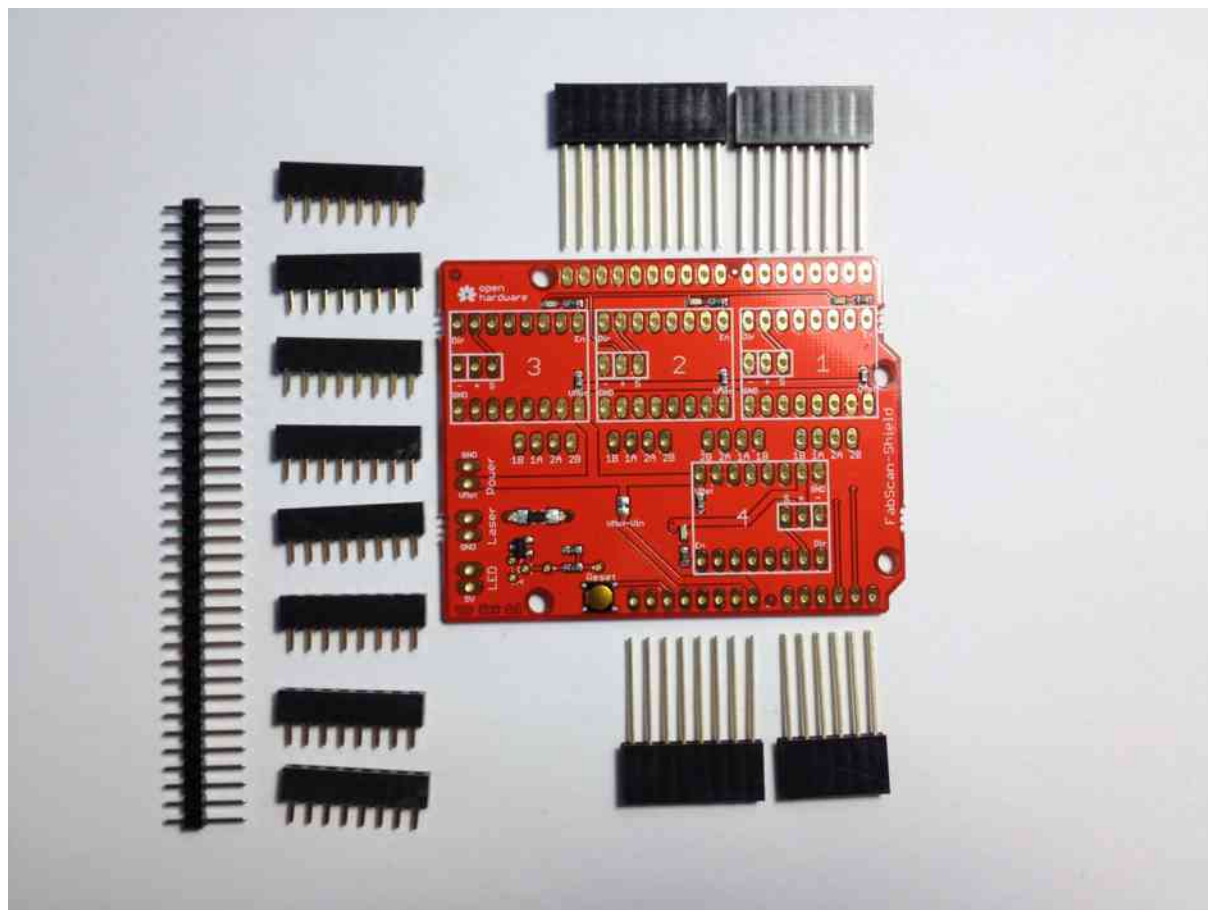


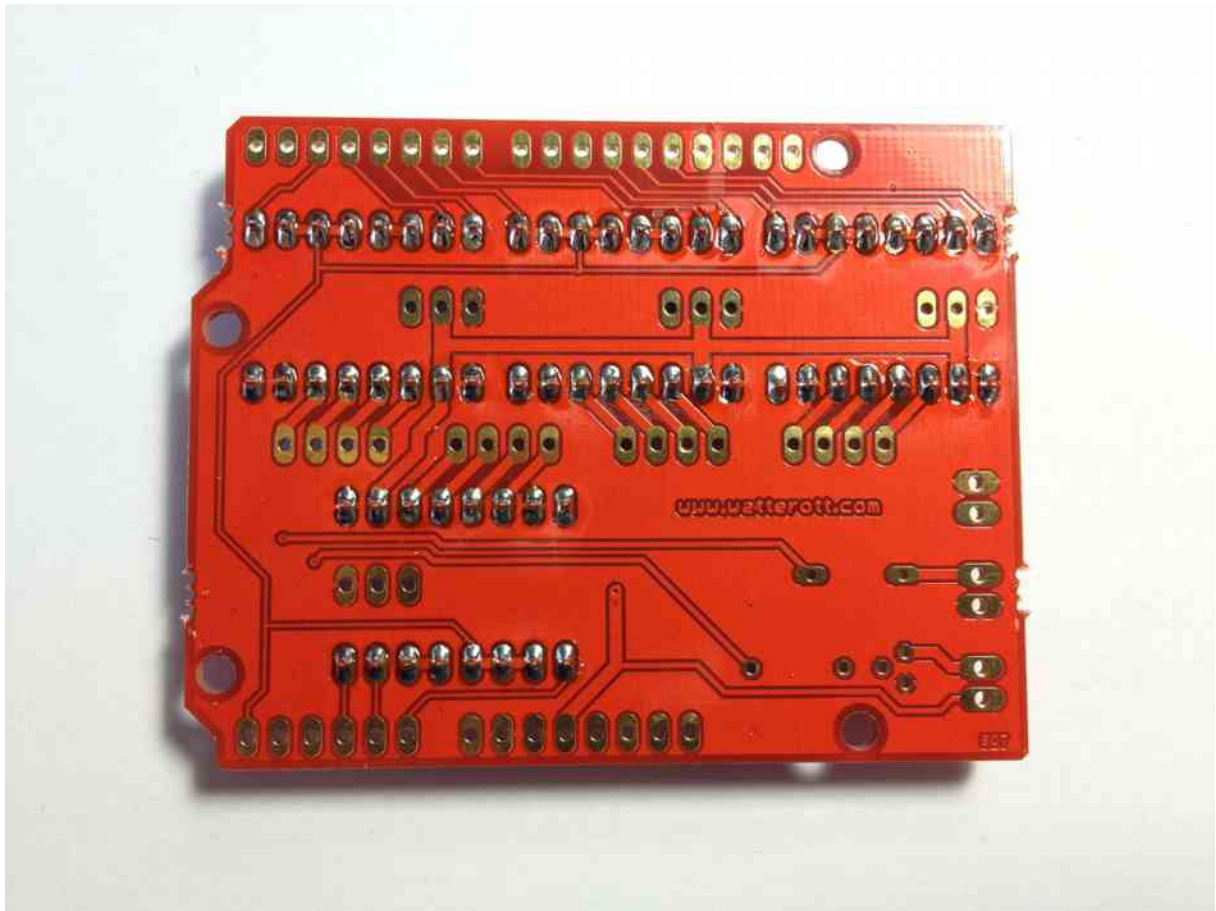
THT Assembly

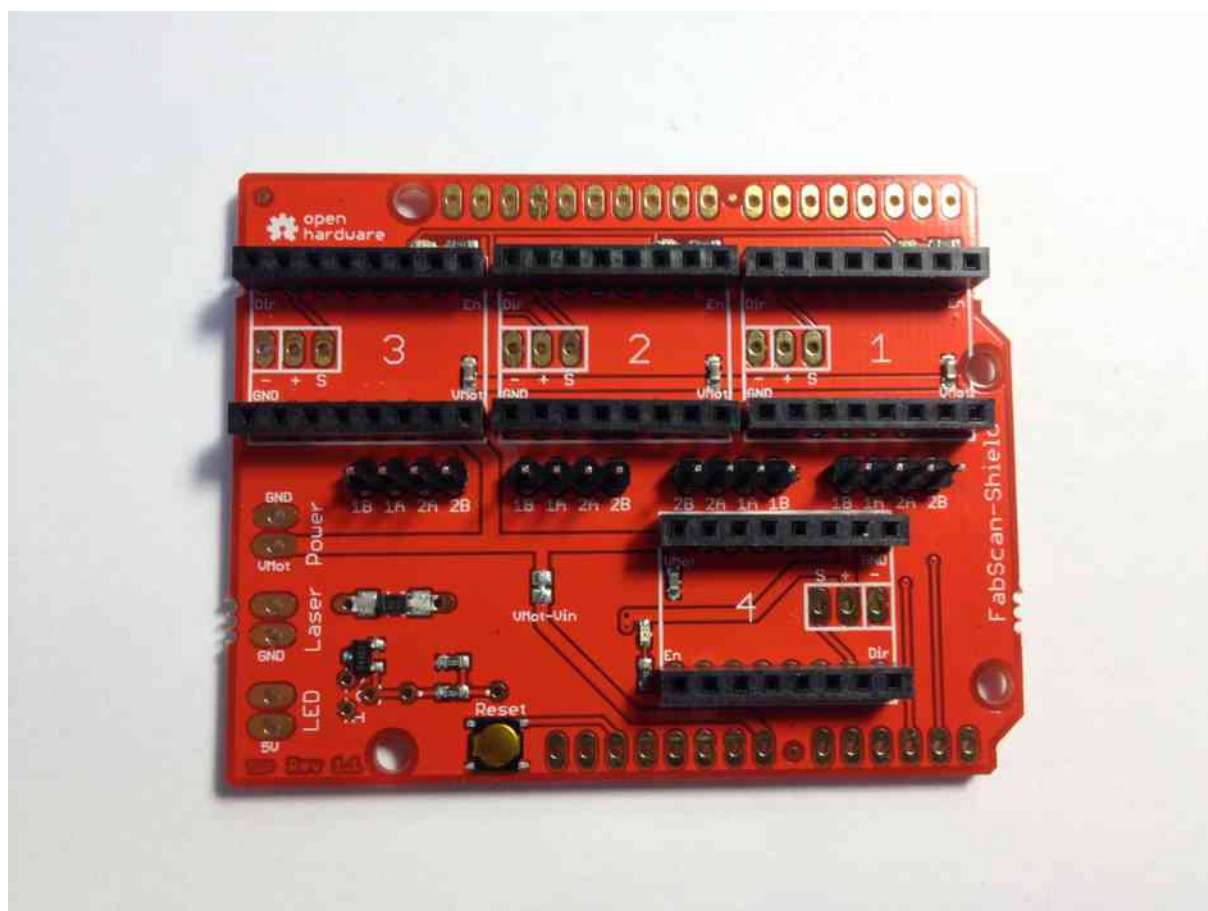
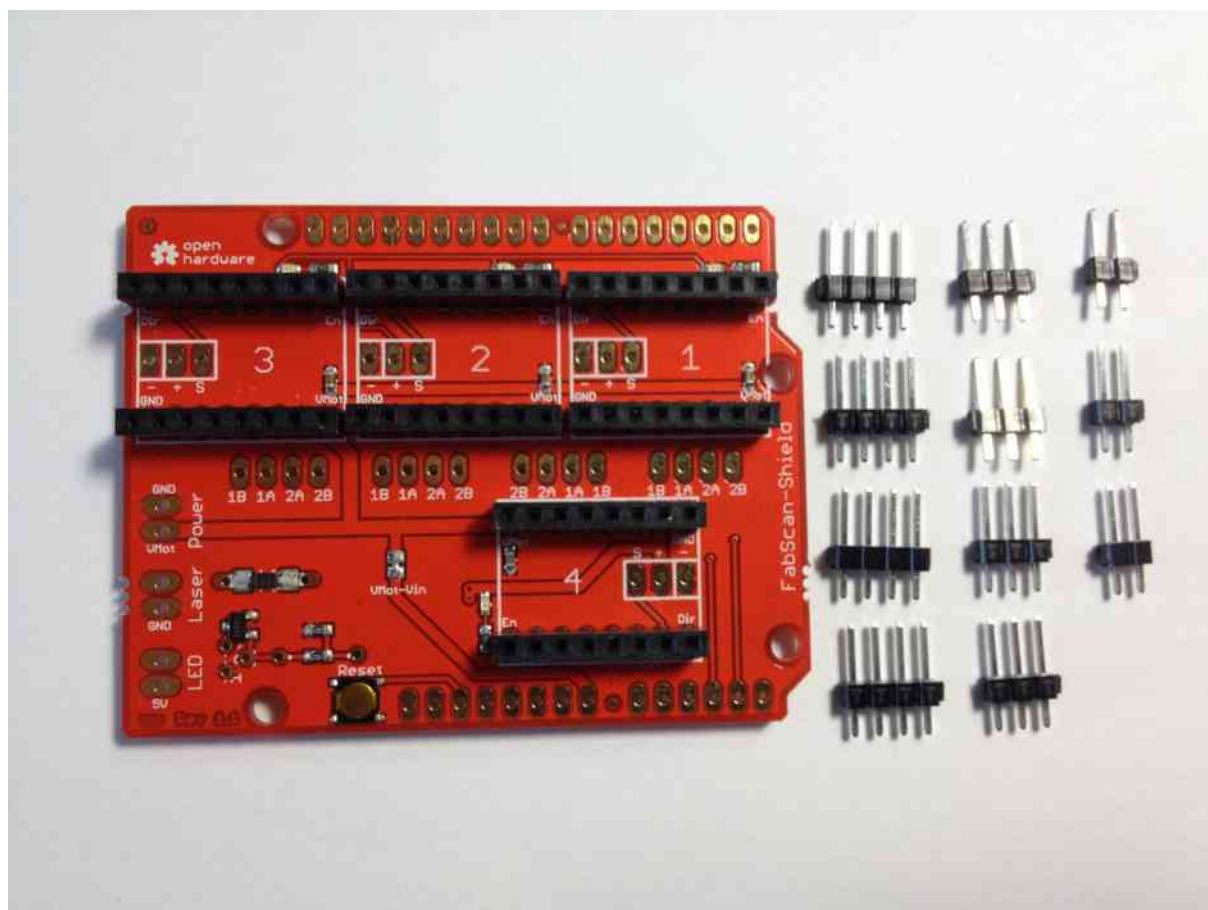


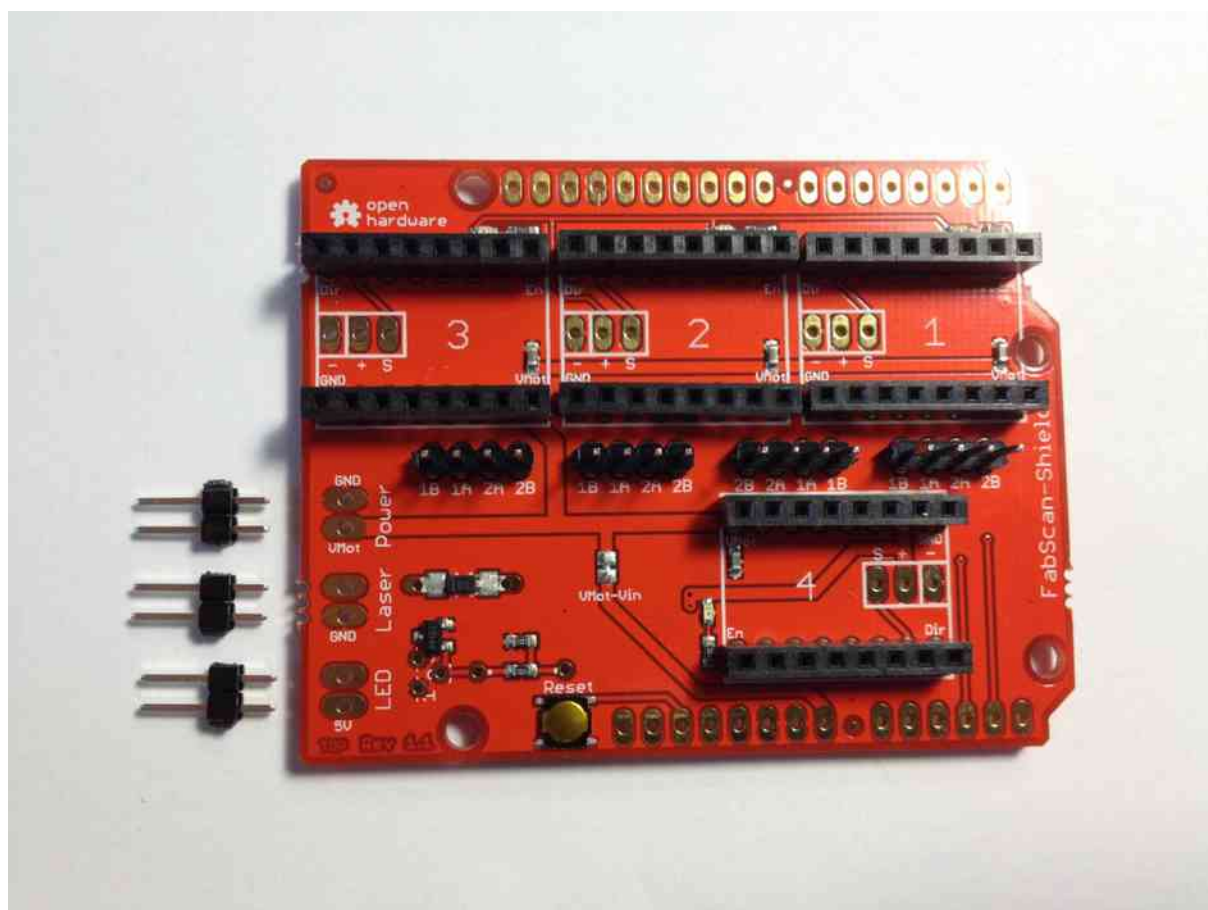
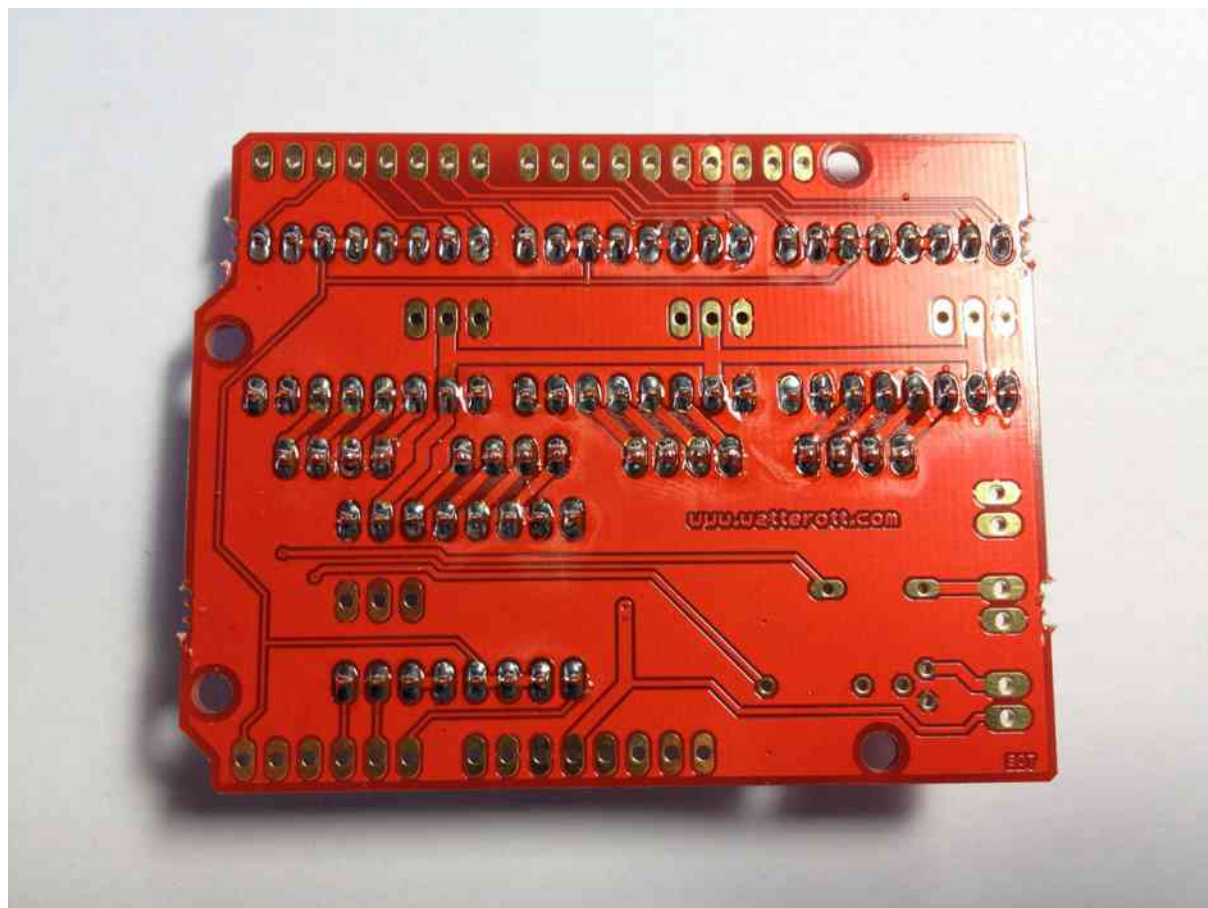
Assembly Guide

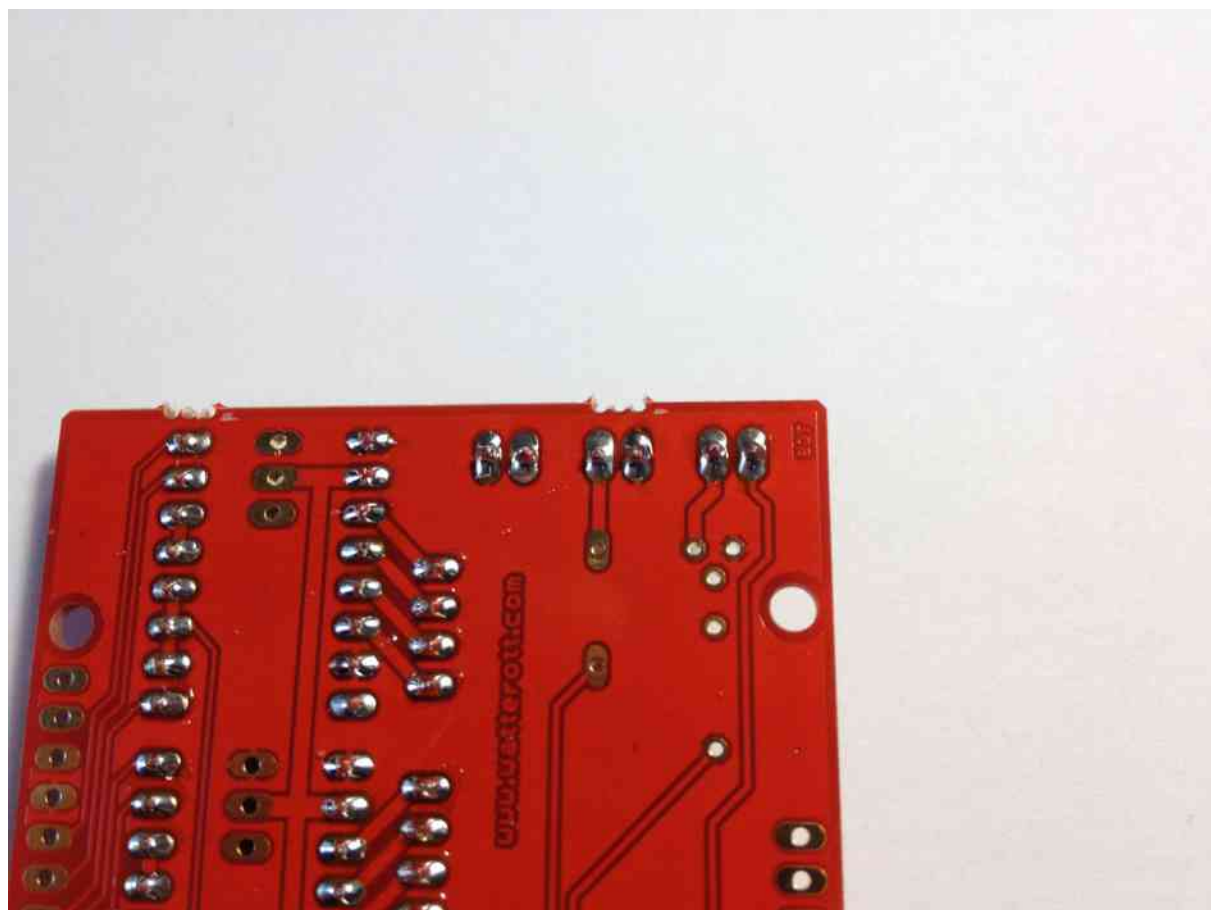


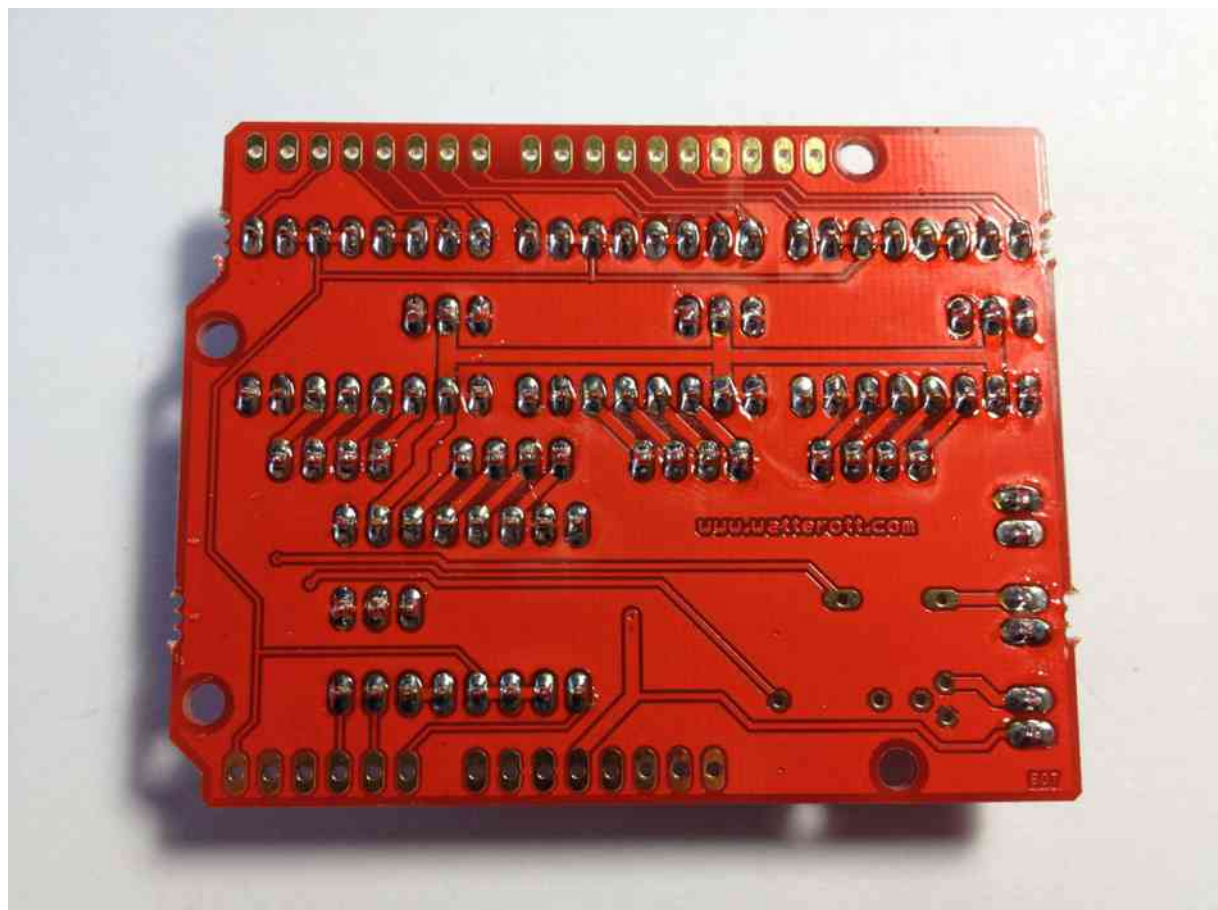
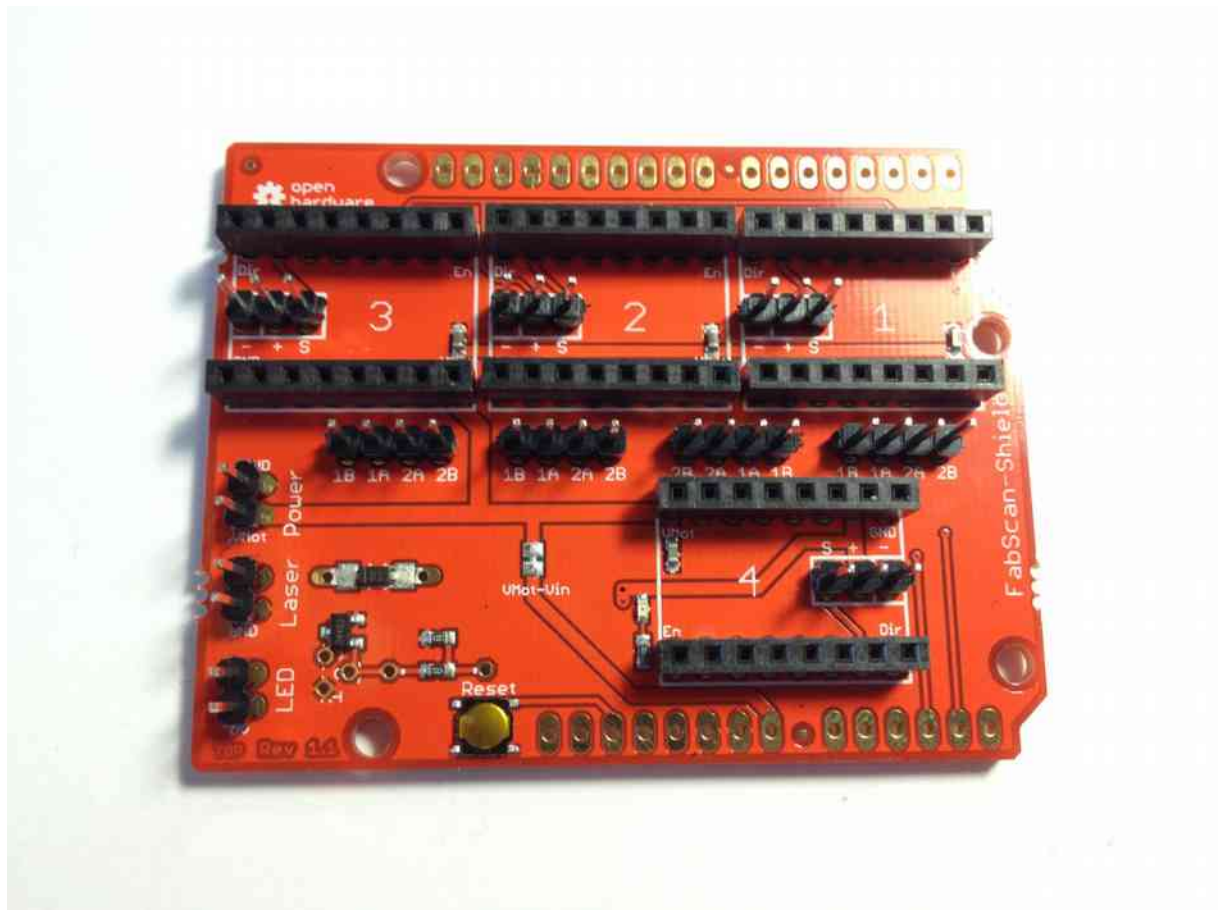


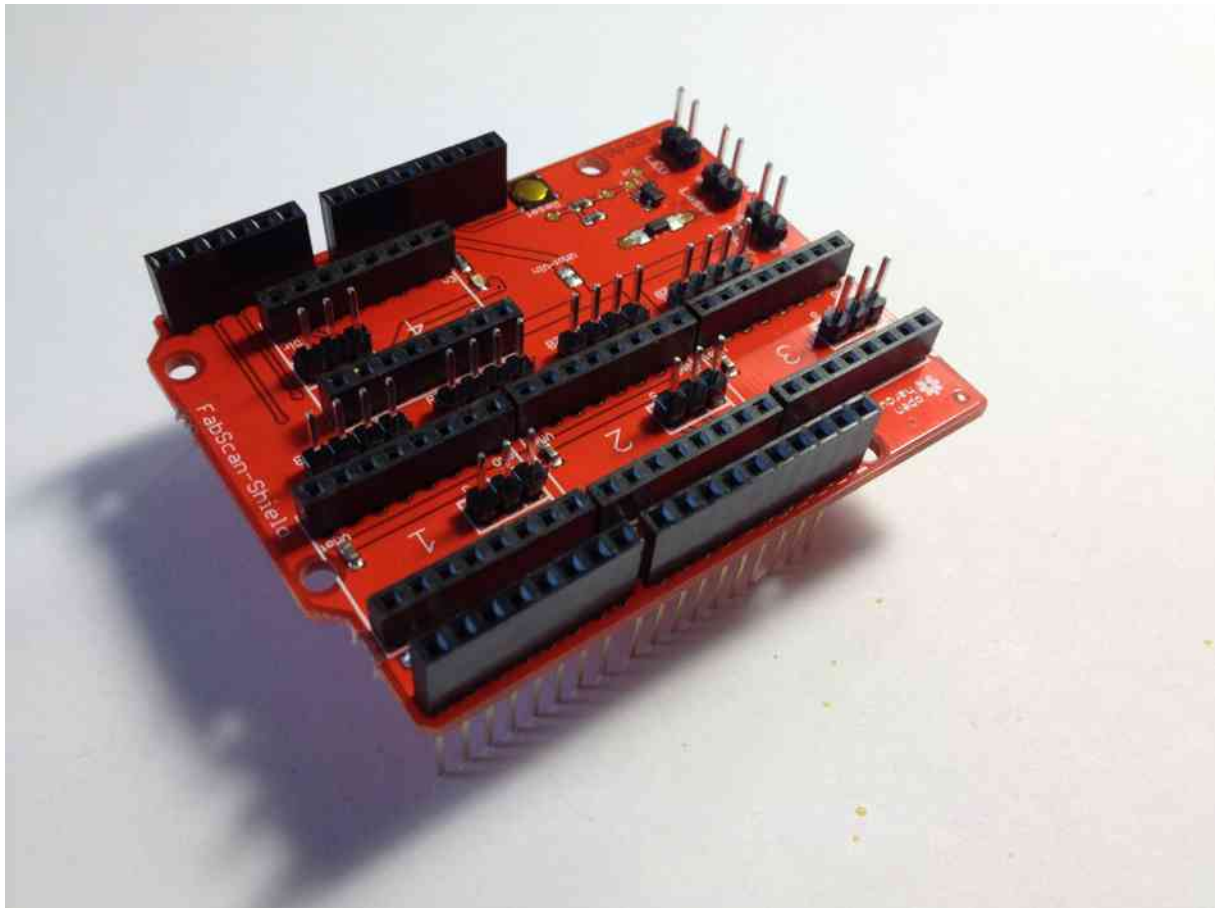
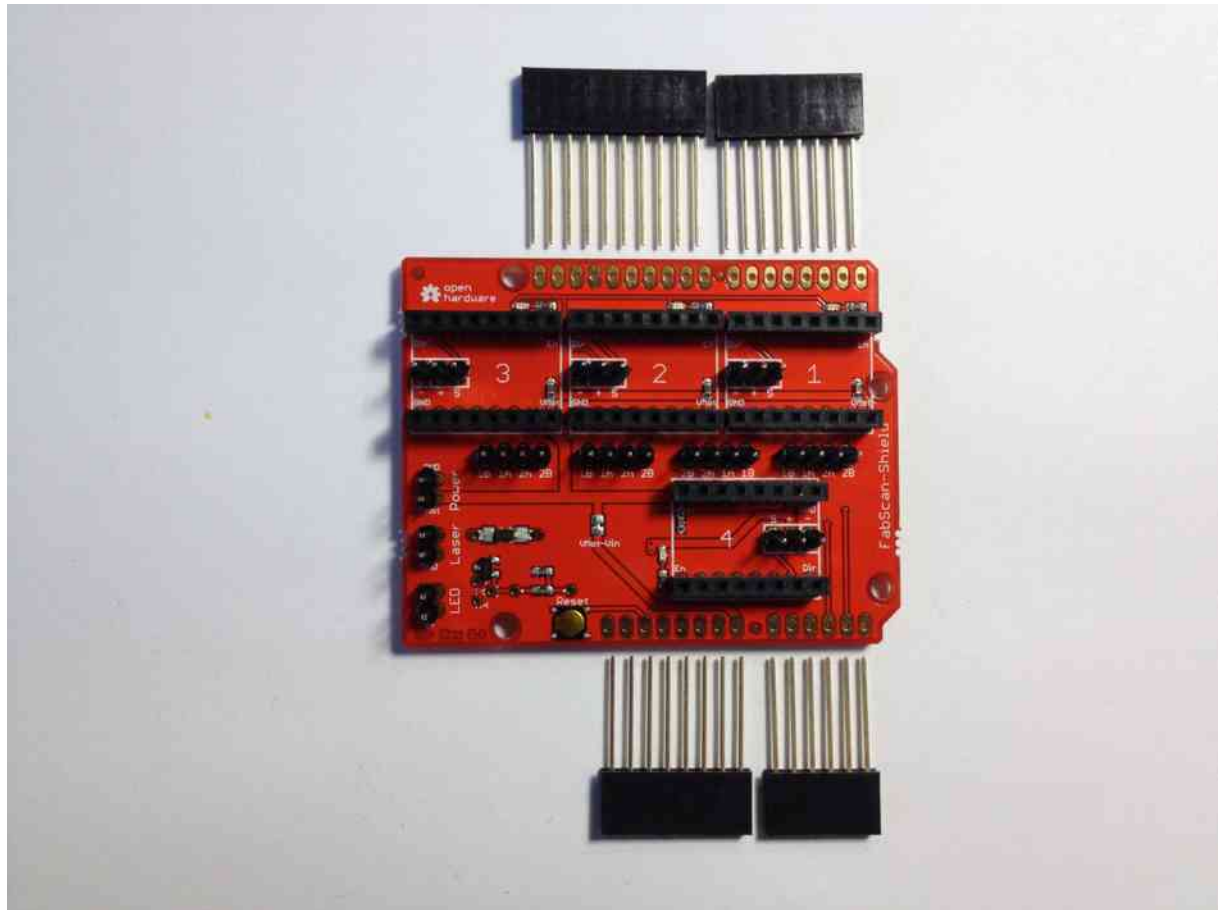


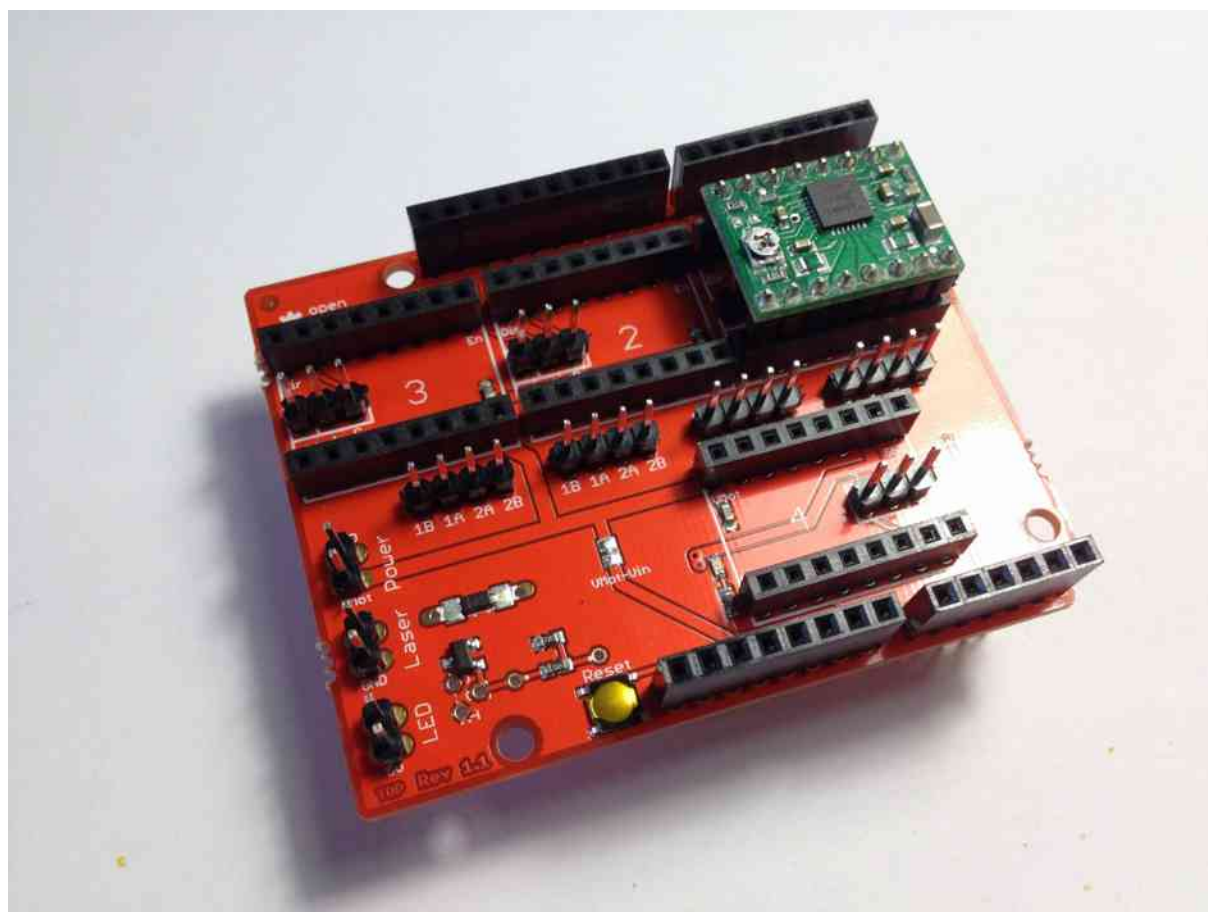












SilentStepStick

Pin configuration for stealthChop mode with 16 microsteps and interpolation to 256 microsteps.

