



Certificate no: UC-63b03df2-9655-4d12-acda-ce29ba2b6351

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Reference Number: 0004

CERTIFICATE OF COMPLETION

Introduction to VHDL for FPGA and ASIC design

Instructors **Scott Dickson**

Ananya Jayaprakash

Date **March 23, 2025**

Length **9.5 total hours**