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-9- 3.4 ==> exponential growth of processes occurs - only a finite number of process ID's on systems - only a finite amount of memory on systems however, since the size of created processes is small and since the OS has lots of swap space available ==> above code will most likely exhaust process table (run out of IDs) instead of run out of ...

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But neither P2 or P3 or P4 can complete. System is not safe. 6.2 Available: A = 1; B = 2 User 1 stills needs (8 2) User 2 stills needs (5 2) User 3 stills needs (2 2) User 4 stills needs (2 3) The algorithm would not have allowed these allocations, since none of the processes are guaranteed to be able to complete.

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Step 5: 7006 → IR; Step 6: AC → Device 6 1.2 1. a. The PC contains 300, the address of the first instruction. This value is loaded in to the MAR. b. The value in location 300 (which is the instruction with the value 1940 in hexadecimal) is loaded into the MBR, and the PC is incremented. These two steps can be done in parallel. c.

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-6- CHAPTER 3 PROCESS DESCRIPTION AND CONTROL 3.1 RUN to READY can be caused by a time-quantum expiration READY to NONRESIDENT occurs if memory is overcommitted, and a process is temporarily swapped out of memory READY to RUN occurs only if a process is allocated the CPU by the dispatcher

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-6- 1.3  $EAT = .9 (10) + .1 [ .8 (10 + 100) + .2 (10 + 100 + 10000) ] = 220ns$  OR  $EAT = 10 + .1 (100) + .02 (10000) = 220ns$  1.4 a. Since the targeted memory module (MM) becomes available for another transaction 600 ns after the initiation of each store operation, and there are 8 MMs, it is possible to initiate a store operation every 100 ns.

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