Date:		Mapping to DIM100	pinout with Adapter card		HSEC Pinout:
20-Jan-2017		F3000	4:: 120 100		2.13
20 Juli 2017		F2800	4x-120-100		2,13
Exp Kit lbl	DIM pin			DIM pin	Exp Kit lbl
	1			51	
	2			52	
	3	12V	12V	53 54	
	5	PGA5NEG PGA1NEG	PGA3NEG	55	
	6	FUATINEO	FUASINEO	56	
ВО		B0	A0, B15, C15, DACA		A0
	8			58	
. B1		B1, A10, C10, PGA7_IN *	A1, DACB		A1
B2	10 11	D2 C6 DCA2 IN *	A2 D6 DCA1 IN *	60 61	A2
ВИ	12	B2, C6, PGA3_IN *	A2, B6, PGA1_IN *	62	AZ
B3	13	B3, VDAC	. АЗ		A3
	14			64	
B4		B4, C8, C3, PGA4_IN *	A4, B8, PGA2_IN *		A4
	16		All VREFHIS **	66	
B5	17	CO	A5		A5
58	18				59
B6	19	C1	A6, PGA5_IN *		A6
60	20 21	C2	A9		61
B7 62	22	C2	A9		A7 63
. 02	22			72	03
. 00	23	GPIO-00	GPIO-01	73	01
02	24	GPIO-02	GPIO-03		03
04	25	GPIO-04	GPIO-05	75	05
06		GPIO-06	GPIO-07		07
	27		5V	77	
08		GPIO-12	GPIO-13		09
10 48	29 30	GPIO-14 GPIO-37,TDO # @@	GPIO-15 GPIO-35,TDI # @		11 49
. 84	31	GPIO-39	GPIO-23, [DCDC]		85
86	32	GPIO-58	5V	82	
12		GPIO-18 ##			13
15	34			84	14
24	35	GPIO-24 #	GPIO-25 #		25
26	36	GPIO-26 #	GPIO-27 #		27
	37	CDIO 45	5V	87	47
16 18	38 39	GPIO-16 GPIO-09	GPIO-17 GPIO-11		17 19
20	40	GPIO-40	GPIO-57		21
. 20	41	GPIO-22 [DCDC]	GPIO-31		23
87	42	•	5V	92	
28	43	GPIO-28	GPIO-29		29
30	44	GPIO-30	GPIO-32		31
32		GPIO-10 @	GPIO-08 @@		33
34		GPIO-34	5V	96	
	47 48	тск	GPIO35,TDI # GPIO37,TDO #	97 98	
	48	TMS	GPI037,1D0 #	98	
	50			100	
			ne ADC channels listed will no longer go to the HSEC connector.		
		to connect this HSEC pin to the MCU's VREFHI inputs			
		two slots via switch			
		2) by default but can be configured to come to this HSEC	pin by soldering/desoldering resistors		
	• • • • • • • • • • • • • • • • • • • •	the other @ via a switch th the other @@ via a switch			
ww signal iliay D	e nippeu W	th the other www via a switch			