8 8 1 9 ADC-81 ADC 82 110  82 11 ADC-82, COMP+ ADC-A2 112  83 13 ADC-83 144  84 15 ADC-84 ADC-A3 166 ADC-A3 174  85 17 ADC-B5 ADC-B5 ADC-B6 ADC-D0, COMP+ ADC-N1 AD	e:		Mapping to	DIM100 pinout with Adapter card		HSEC Pinout:
1	ov-2015			F28377D (180pin)		2.12
1	xn Kit lhl	DIM nin			DIM pin	Exp Kit lbl
2	Ap Ric Ioi				51	EXP INCIDI
## A					52	
## A			12V	12V	53	
80 7 ADC-80 8 8 8 8 8 1 9 ADC-81 10 22 11 ADC-82, COMP+ 112 12 13 31 ADC-83 14 ADC-84 15 ADC-84 16 ADC-85 17 ADC-85 18 16 PIO-58 18 6 19 ADC-00, COMP+ ADCINI 60 20 GPIO-14 87 21 ADC-01 62 22 GPIO-46*** GP 00 23 GPIO-00 02 24 GPIO-02 04 25 GPIO-06 07 70 08 28 GPIO-08 10 29 GPIO-10 48 30 GPIO-11 88 32 GPIO-88 12 33 GPIO-10 13 34 GPIO-12 14 35 GPIO-10 15 34 GPIO-12 15 34 GPIO-12 16 35 GPIO-16 17 GP 18 39 GPIO-17 18 39 GPIO-18 19 39 GPIO-19 10 39 GPIO-10 30 GPIO-10 31 GPIO-10 32 GPIO-00 33 GPIO-00 34 35 GPIO-10 35 GPIO-10 36 36 GPIO-12 37 GPIO-10 38 31 GPIO-10 39 GPIO-10 40 GPIO-20 20 41 GPIO-20 21 41 GPIO-20 22 41 GPIO-20 23 GPIO-10 34 GPIO-30 35 GPIO-30 36 GPIO-16 37 GPIO-30 37 GPIO-30 38 GPIO-16 39 GPIO-16 39 GPIO-16 39 GPIO-30 30 44 GPIO-30 31 45 GPIO-30 32 45 GPIO-32 34 46 GPIO-30 35 GPIO-31 36 TMS 37 GPIO-31 37 GPIO-30 38 TMS 39 GPIO-16 39 GPIO-30 30 GPIO-30 31 45 GPIO-32 31 45 GPIO-32 32 45 GPIO-32 33 46 GPIO-32 34 46 GPIO-30 35 GPIO-31 36 TMS 37 GPIO-31 37 GPIO-30 38 TMS 39 GPIO-31 30 TMS 30 GPIO-31 31 GPIO-30 32 45 GPIO-32 33 GPIO-30 34 46 GPIO-30 35 GPIO-31 36 TMS 37 GPIO-31 37 GPIO-31 38 TMS 39 GPIO-31 39 GPIO-31 30 TMS 30 GPIO-31 30 TMS 30 GPIO-31 31 GPIO-30 32 TMS 33 GPIO-30 34 46 GPIO-30 35 TMS 35 GPIO-31 36 TMS 37 GPIO-31 37 GPIO-31 38 TMS 39 GPIO-31 39 GPIO-31 30 TMS 30 GPIO-31 30 TMS 30 GPIO-31 30 TMS 30 GPIO-31 48 TMS 50 GPIO-31 49 TMS 50 GPIO-31 TMS		4			54	
80 7 ADC-B0 AD 8 8 ADC-B1 AD 8 9 ADC-B1 AD 10 ADC-B2 COMP+ ADC-A 11 ADC-B2 COMP+ ADC-A 12 ADC-B3 ADC-B3 ADC-B3 ADC-B4 ADC-A 13 ADC-B3 ADC-B4 ADC-B5 A		5			55	
B1   9   ADC-B1   ADC     B2   11   ADC-B2, COMP+   ADC     12   13   ADC-B3     14   B4   15   ADC-B4   ADC     B5   17   ADC-B5   ADC     B6   19   ADC-D0, COMP+   ADC     B7   21   ADC-D1     60   20   GPIO-44     B7   21   ADC-D1     62   22   GPIO-46 ***   GPIO-20     64   GPIO-20   GPIO-10     65   GPIO-10   GPIO-10     66   26   GPIO-10   GPIO-10     67   27   ADC-B1   GPIO-10     77   70   28   GPIO-10   GPIO-10     84   31   GPIO-14   GPIO-10   GPIO-10     85   GPIO-10   GPIO-20   GPIO-10   GPIO-20   GPIO-20   GPIO-20   GPIO-30		6			56	
BI   9   ADC-81   ADC     B2   11   ADC-82, COMP+						
B1 9 ADC-B1 ADC-B2 11 ADC-B2, COMP+ ADC-A 12 B3 13 ADC-B3 14 B4 15 ADC-B4 ADC-B5 16 ADC-B5 18 18 GPIO-SB 19 ADC-D0, COMP+ ADCINI 60 20 GPIO-40 87 21 ADC-D1 62 22 GPIO-04 87 21 ADC-D1 62 22 GPIO-06 63 GPIO-06 77 ADC-B5 64 GPIO-39 65 GPIO-16 67 ADC-B5 68 19 ADC-D0, COMP+ ADCINI 69 ADC-D0, COMP+ ADCINI 60 ADC-D0, COMP+ ADCINI 61 ADC-D1 62 ADC-D0, COMP+ ADCINI 63 GPIO-00 64 ADC-D0, COMP+ ADCINI 65 ADC-D0, COMP+ ADCINI 66 ADC-D0, COMP+ ADCINI 67 ADCINI 68 ADC-D0, COMP+ ADCINI 69 ADCINI 69 ADC-D0, COMP+ ADCINI 69 ADCINI 60 ADC-D0, COMP+ ADCINI 60 A	В		ADC-B0	ADC-A0, DAC		A0
No.					58	
B2	В		ADC-B1	ADC-A1, DAC		A1
12					60	
B3	В		ADC-B2, COMP+	ADC-A2, COMP+		A2
14					62	
B4	В		ADC-B3	ADC-A3		A3
16			ADC 04		64	
85 17 ADC-85 58 18 GPIO-38 86 19 ADC-D0, COMP+ ADCINI 60 20 GPIO-44 87 21 ADC-D1 62 22 GPIO-46 87 21 ADC-D1 62 22 GPIO-6***	В		ADC-B4	ADC-A4, COMP+		A4
\$\frac{1}{86}\$  \text{19}  \text{ADC-DQ, COMP+}  \text{ADCIN1} \\ 60  \text{20}  \text{ADC-D1} \\ 62  \text{21}  \text{ADC-D1} \\ 62   \text{22}  \text{GPIO-00} \\ 62    \text{GPIO-00} \\ 62    \text{GPIO-00} \\ 62      \text{GPIO-04} \\ 65  \q	_		ADC DE	All VREFHIS *	66	
B6 19 ADC-DO, COMP+ 60 20 GPIO-44 B7 21 ADC-D1 62 22 GPIO-46***				ADC-A5		A5
60 20 GPIO-44 87 21 ADC-D1 62 22 GPIO-46***				GPIO-59		59
87 21 ADC-D1 62 22 GPIO-46***  GP  00 23 GPIO-00  02 24 GPIO-02  04 25 GPIO-04  06 26 GPIO-06  27 08 28 GPIO-08  10 29 GPIO-10  48 30 GPIO-12  84 31 GPIO-14  86 32 GPIO-48  12 33 GPIO-49  15 34 GPIO-26  37 GPIO-26  37 16 38 GPIO-16  18 39 GPIO-18  20 40 GPIO-20  21 41 GPIO-20  22 41 GPIO-23  33 44 GPIO-30  34 GPIO-30  35 GPIO-24  46 GPIO-30  37 47 48 TCK  49 TMS  50 GPIO-71 ***  GP  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMUO and EMU1				ADCIN14, COMP+ GPIO-45		A6 61
62 22 GPIO-46 ***  00 23 GPIO-00  01 24 GPIO-02  04 25 GPIO-04  06 26 GPIO-08  10 29 GPIO-10  48 30 GPIO-12  84 31 GPIO-14  85 32 GPIO-48  12 33 GPIO-40  15 34 GPIO-49  15 34 GPIO-24  26 36 GPIO-26  37  16 38 GPIO-16  18 39 GPIO-18  20 40 GPIO-20  21 41 GPIO-20  22 41 GPIO-39  28 43 GPIO-32  39 44 GPIO-32  30 44 GPIO-32  31 45 GPIO-34  47 48 TCK  49 TMS  50 GPIO-71 **  G  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs				ADCIN15	70	A7
00			+	GPIO-47 ***		63
02		- 44		GF10-47	, ,	
02	(	23	GPIO-00	GPIO-01	73	01
04				GPIO-03	74	03
06 26 GPIO-06 27 08 28 GPIO-08 10 29 GPIO-10 48 30 GPIO-12 84 31 GPIO-48 12 33 GPIO-48 12 33 GPIO-24 24 35 GPIO-24 26 36 GPIO-26 37 16 38 GPIO-16 18 39 GPIO-18 20 40 GPIO-20 22 41 GPIO-22 87 42 GPIO-39 28 43 GPIO-39 39 44 GPIO-30 30 44 GPIO-30 31 46 GPIO-32 31 46 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71** G * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1			+	GPIO-05		05
27 08 28 GPIO-08 10 29 GPIO-10 48 30 GPIO-12 84 31 GPIO-14 86 32 GPIO-40 15 34 GPIO-24 26 36 GPIO-26 37 16 38 GPIO-16 18 39 GPIO-18 20 40 GPIO-20 22 41 GPIO-22 87 42 GPIO-39 28 43 GPIO-39 28 43 GPIO-30 32 45 GPIO-32 34 46 GPIO-30 34 47 48 TCK 49 TMS 50 GPIO-71 ** GG  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1				GPIO-07		07
08				5V	77	
48 30 GPIO-12 84 31 GPIO-14 86 32 GPIO-48 12 33 GPIO-40 15 34 GPIO-34***  48 35 GPIO-24 26 36 GPIO-26 37 16 38 GPIO-16 18 39 GPIO-18 20 40 GPIO-20 22 41 GPIO-20 22 41 GPIO-22 87 42 GPIO-39 28 43 GPIO-28 30 44 GPIO-30 32 45 GPIO-32 34 46 GPIO-30 32 45 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71 **  6 Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1	C		GPIO-08	GPIO-09		09
84 31 GPIO-14 86 32 GPIO-48 12 33 GPIO-40 15 34 GPIO-24 24 35 GPIO-26 37 16 38 GPIO-16 18 39 GPIO-18 20 40 GPIO-20 22 41 GPIO-22 87 42 GPIO-39 28 43 GPIO-28 30 44 GPIO-30 32 45 GPIO-32 34 46 GPIO-32 34 46 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71 **  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1	1	10 29	GPIO-10	GPIO-11	79	11
86 32 GPIO-48 12 33 GPIO-40 15 34 GPIO-43*** 26 36 GPIO-26 37 16 38 GPIO-16 18 39 GPIO-18 20 40 GPIO-20 22 41 GPIO-20 22 41 GPIO-39 28 43 GPIO-38 30 44 GPIO-30 32 45 GPIO-32 34 46 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71**  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMUO and EMU1	4	48 30	GPIO-12	GPIO-13	80	49
12	8	31	GPIO-14	GPIO-15	81	85
15	8	32	GPIO-48	5V	82	
24 35 GPIO-24 26 36 GPIO-26 37 16 38 GPIO-16 18 39 GPIO-18 20 40 GPIO-20 22 41 GPIO-22 87 42 GPIO-39 28 43 GPIO-28 30 44 GPIO-30 32 45 GPIO-32 34 46 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71**  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1	1	12 33	GPIO-40	GPIO-41	83	13
26				GPIO-42 ***		14
37 16 38 GPIO-16 18 39 GPIO-18 20 40 GPIO-20 22 41 GPIO-22 87 42 GPIO-39 28 43 GPIO-28 30 44 GPIO-30 32 45 GPIO-32 34 46 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71 **  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1			-	GPIO-25		25
16 38 GPIO-16 18 39 GPIO-18 20 40 GPIO-20 22 41 GPIO-22 87 42 GPIO-39 28 43 GPIO-28 30 44 GPIO-30 32 45 GPIO-32 34 46 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71 **  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1	2		GPIO-26	GPIO-27		27
18				5V	87	
20				GPIO-17		17
22 41 GPIO-22 87 42 GPIO-39 28 43 GPIO-28 30 44 GPIO-30 32 45 GPIO-32 34 46 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71 ** G  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1				GPIO-19		19
87				GPIO-21		21
28				GPIO-23		23
30				5V	92	20
32				GPIO-29 GPIO-31		29 31
34 46 GPIO-34 47 48 TCK 49 TMS 50 GPIO-71 **  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1						33
47 48 TCK 49 TMS 50 GPI0-71 **  Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPI0-70 and 71 to EMU0 and EMU1				GPIO-33	95 96	33
48 TCK 49 TMS 50 GPIO-71 **  * Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1	- 3		55 54	TDI	97	
49 TMS 50 GPIO-71 **  Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  * Switches may be altered to connect this GPIO-70 and 71 to EMU0 and EMU1			тск	TDO	98	
* Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1				TRSTn	99	
* Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs  ** Oohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1				GPIO-70 **	100	
** 0ohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1				0.10 70		
** 0ohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1						
** 0ohm resistors can be removed in order to tie GPIO-70 and 71 to EMU0 and EMU1		* Switche	s may be altered to connect this HSEC pi	in to the MCU's VREFHI inputs		
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*** Can be dedicated to on-card USB or brought through the connector via jumpers on the cCARD		*** Can b	e dedicated to on-card USB or brought th	hrough the connector via jumpers on the cCARD		