
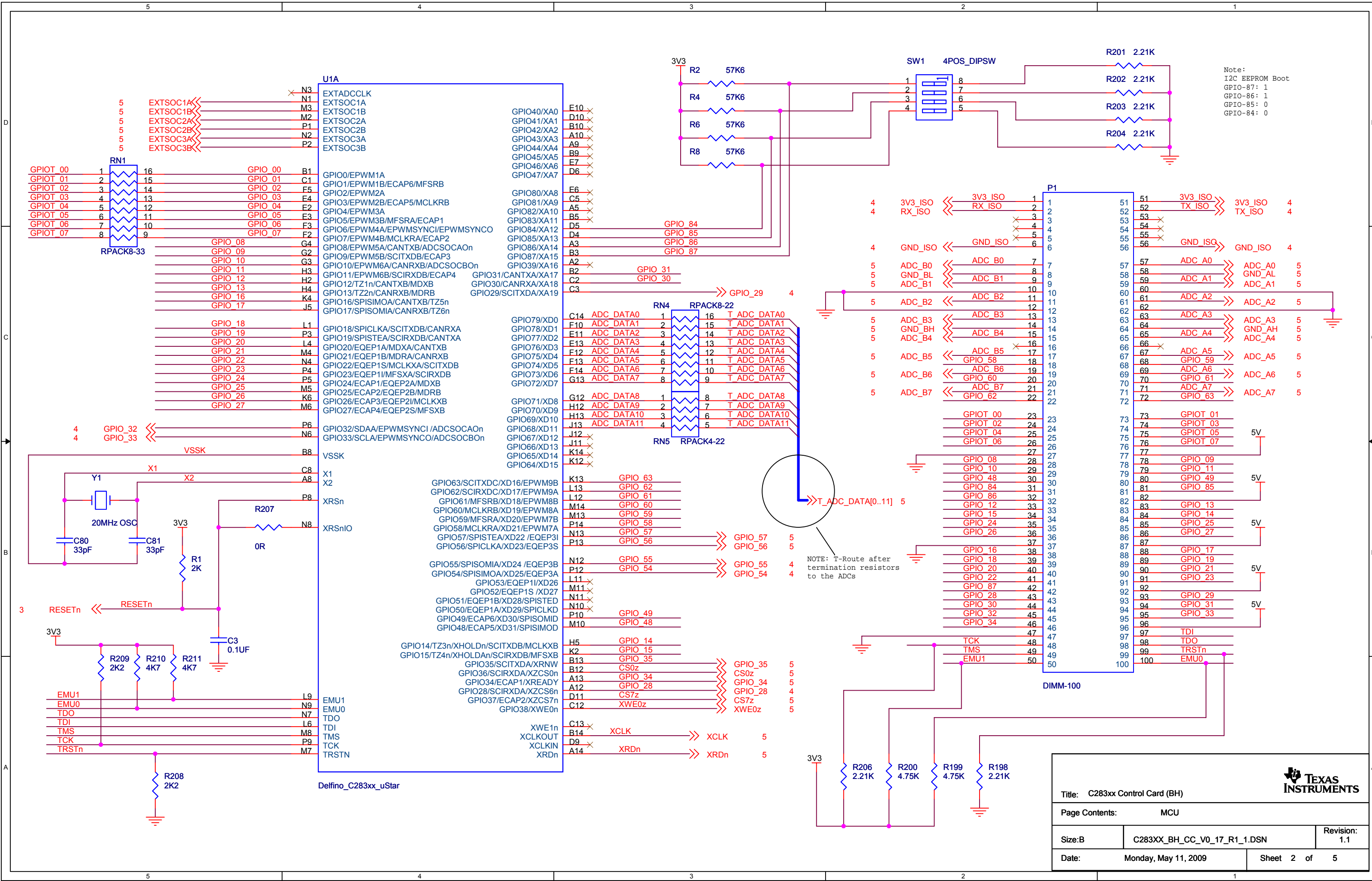
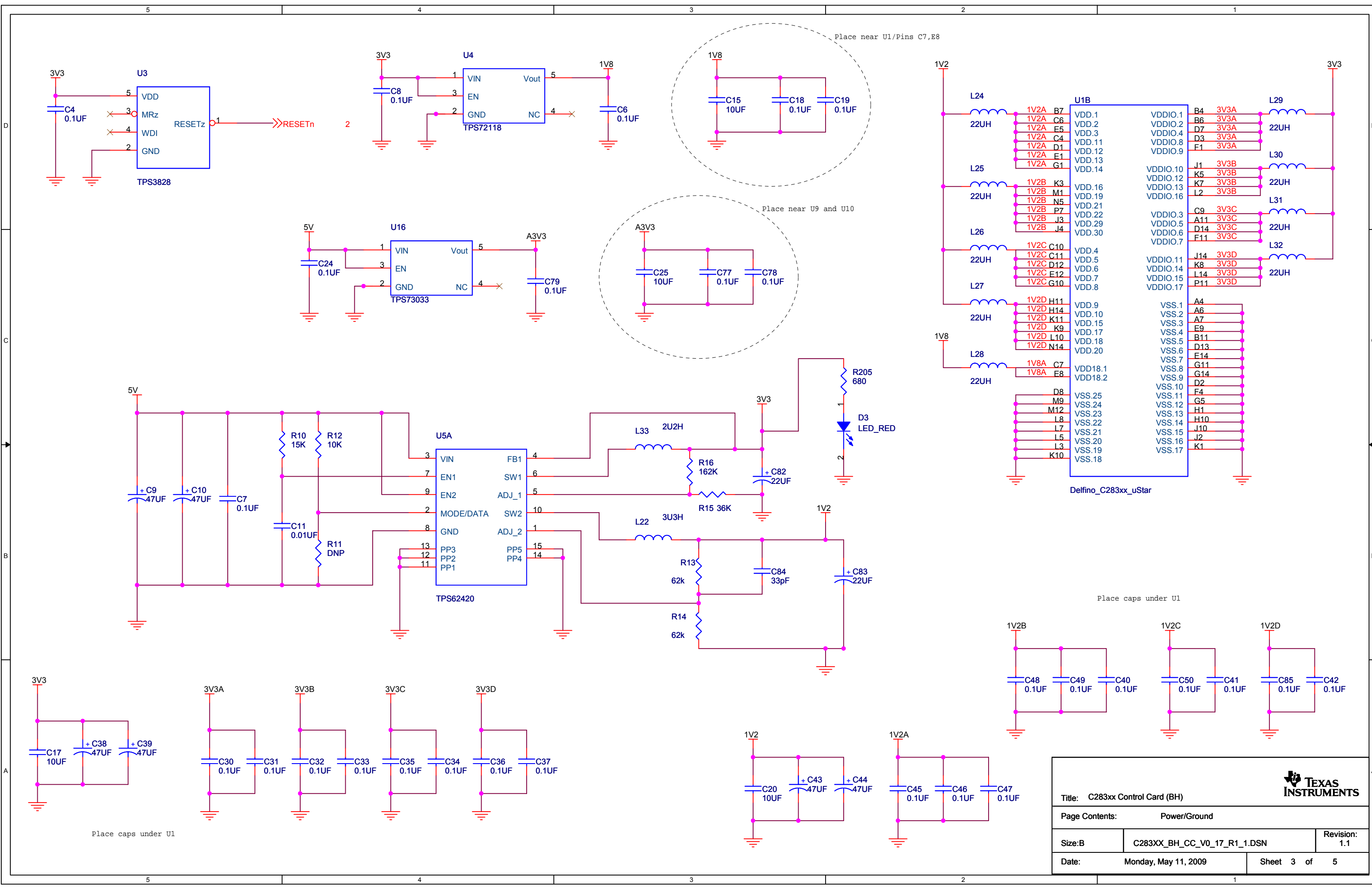


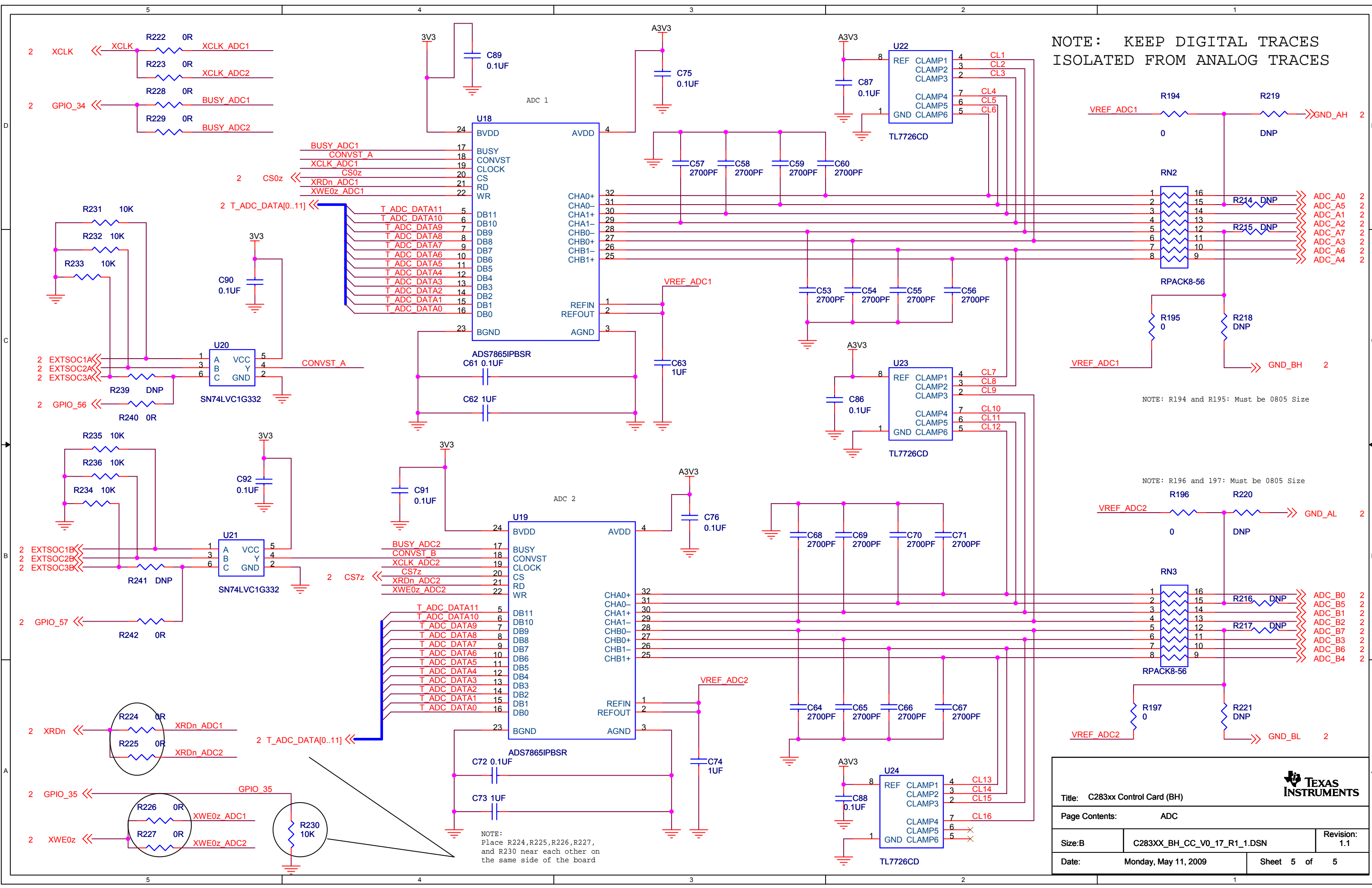
5	4	3	2	1
D	NOTES:			
	V0.4 - 1st Release	V0.15 p2 - Added nets GPIO_56 and GPIO_57 p5 - Removed U20, U21, C92 - Added U25, R237,R238	DIRECTORY: 1 - Notes 2 - MCU 3 - Power/Ground 4 - IO 5 - ADC	
	V0.5 - modified ADC pinout (- changed how the XTAL is connected to the VSSK pin.			
C	V0.6 - Added 2.2k pullup (R206) to TCK on P1	V0.16 p5 - Removed U25 - Added U20, U21, C92, R239, R240, R241, R242		
	V0.7 p5 - Changed ADCs U18 and UU19 to ADS7865	V0.17 p5 - Changed Size of R231,R232 R233,R234,R235,R236,R239 R240,R241,R242		
	V0.8 p2 - MCU - Added traces to GPIO 55/54 - Added RN4 and RN5 to ADC_DATA line - Added note for Post RN4/5 route p3 - Power/Gound - Removed C29,C37 - Named Power Nets after the Inductors (L24-32) - Added C85 p4 - IO - Changed GPIO34/35 to GPIO54/55 p5 - ADC - Updated XCLK_ADC1/2 trace names - Added Nets GPIO_34/35 to this sheet - Added connection for busy signal on ADC - Deleted U11,12,13,14 - Added U22,23,24	Board Release 1.1 -same schematics as V0.17 -udated title blocks -changed project name to C283XX_BH_CC_V0_17_R1_1.DSN		
B	V0.9 p5 - ADC - Added caps C86,87,88 to U22,23,24			
	V0.10 p3 - Power - added PowerPad package pins to part U5. p4 - IO - Changed connect of C26 and C28 to ISO_GND			
	V0.11 p5 - Added Decoupling Caps C89,90,91,92			
A	V0.12 p2 - Changed Y1 to a different Part # & Footprint p3 - Changed C82/83 to a different part # - Changed C9,10,38,39,43,44 to different part # - Changed C15,17,20,25 to different part # p5 - Changed R230 to 10k			
	V0.13 p5 - Added R231,233,234,235,236,237 10k pulldowns to ADCSOC signals - Changed C63 and C74 cap values to 1uF			
	V0.14 p5 - Added nets VREF_ADC1 and VREF_ADC2 - Changed connections to R194, R194,R218,R219,R196,R197, R220,R221			
5	4	3	2	1

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NOTE: KEEP DIGITAL TRACES
ISOLATED FROM ANALOG TRACES

NOTE: R194 and R195: Must be 0805 Size

NOTE: R196 and 197: Must be 0805 Size

NOTE:
Place R224, R225, R226, R227,
and R230 near each other on
the same side of the board

		
Title: C283xx Control Card (BH)		
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