Mapping to DIM100 pinout with Adapter card

F280025

		HSEC Pinout:
Date:		
30-Jul-2019		2.15

Exp Kit lbl	DIM pin	Signal	Signal	DIM pin	Exp Kit lbl
	1			51	
	2			52	
	3	12V	12V	53	
	4	-	-	54	
	5	-	-	55	
	6			56	
B0	7	A11/C0/CMP1/TESTANA0	A0/C15/ADCINCAL/CMP3P	57	A0
	8			58	
B1	9	A12/C1/TESTANA1/CMP2/CMP4P	A1/CMP1P	59	A1
	10			60	
B2	11	C4/A14/CMP3P	A2/C9/CMP1P/CMP3	61	A2
	12			62	
B3	13	C6/CMP3P	A3/C5/VDAC/CMP3	63	A3
	14			64	
B4	15	A15/C7/CMP1	A4/C14/CMP2P/CMP4	65	A4
	16	-	All VREFHIs *	66	
B5	17	C8/A9/CMP4P/CMP2P	A5/C2/CMP3	67	A5
58	18	-	-	68	59
B6	19	A10/C10/CMP2	A6/CMP1P	69	A6
60	20	GPIO-35/TDI	-	70	61
B7	21	A8/C11/CMP2P/CMP4P #	A7/C3/CMP4	71	A7
62	22	-	-	72	63
00	23	GPIO-00	GPIO-01	73	01
02	24	GPIO-02	GPIO-03	74	03
04	25	GPIO-04	GPIO-05	75	05
06	26	GPIO-06	GPIO-07	76	07
	27		5V	77	
08	28	GPIO-08	GPIO-09	78	09
10	29	GPIO-10	GPIO-11 @	79	11
48	30	GPIO-12 @	GPIO-13 @	80	49
84	31	GPIO-14	GPIO-15	81	85
86	32	GPIO-42	5V	82	
12	33	GPIO-39	-	83	13
15	34	-	-	84	14
24	35	GPIO-24 ** #	GPIO-25 # @	85	25
26	36	GPIO-26 # @	GPIO-27 # @	86	27
	37		5V	87	
16	38	GPIO-16	GPIO-17	88	17
18	39	GPIO-18/X2 ##	GPIO-19/X1 ##	89	19
20	40	GPIO-40	GPIO-41	90	21
22	41	GPIO-22	GPIO-23	91	23
87	42	GPIO-37/TDO	5V	92	
28	43	GPIO-28 @@	GPIO-29 @@	93	29
30	44	GPIO-30	GPIO-31	94	31
32	45	GPIO-32**	GPIO-33	95	33
34	46	GPIO-34	5V	96	
	47		GPIO-35/TDI	97	
	48	TCK	GPIO-37/TDO	98	
	49	TMS	-	99	
	50			100	

* Resistors may be altered to connect this HSEC pin to the MCU's VREFHI inputs
** Boot mode pin
# Signal can be route to one of two HSEC pins via switch
GPIO can only be used with INTOSC
@ Signal is shared with FSI header and can be disconnected by desoldering resistors.
@@ Can be connected to PC terminal via switch