F28035 ISO controlCARD board pin-out description

spare	1	51	spare
spare	2	52	spare
spare	3	53	*spare*
spare	4	54	*spare*
spare	5	55	*spare*
spare	6	56	*spare*
ADCIN-B0	7	57	ADCIN-A0
GND	8	58	GND
ADCIN-B1	9	59	ADCIN-A1
GND	10	60	GND
ADCIN-B2/COMP1B/AIO10	11	61	ADCIN-A2/COMP1A/AIO2
GND	12	62	GND
ADCIN-B3	13	63	ADCIN-A3
GND	14	64	GND
ADCIN-B4/COMP2B/AIO12	15	65	ADCIN-A4 /COMP2A/AIO4
Spare	16	66	VREFHI-ext
ADCIN-B5	17	67	ADCIN-A5
Spare	18	68	*spare*
ADCIN-B6/CMP3B/AIO14	19	69	ADCIN-A6/COMP3A/AIO06
Spare	20	70	*Spare*
ADCIN-B7	21	71	ADCIN-A7
spare	22	72	*Spare*
GPIO-00 / EPWM-1A	23	73	GPIO-01 / EPWM-1B / EMU0 / COMP1OUT
GPIO-02 / EPWM-2A	24	74	GPIO-03 / EPWM-2B / SPISOMIA / COMP2OUT
GPIO-04 / EPWM-3A	25	75	GPIO-05 / EPWM-3B / SPISIMOA / ECAP1
GPIO-06 / EPWM-4A / EPWMSYNCI	26	76	GPIO-07 / EPWM-4B / SCIRX-A
GND	27	77	+5V in
GPIO-08 / EPWM-5A / -- / ADCSOCA	28	78	GPIO-09 / EPWM-5B / LINTX-A
GPIO-10 / EPWM-6A / -- / ADCSOCB	29	79	GPIO-11 / EPWM-6B / LINRX-A
GPIO-40 / EPWM-7A	30	80	GPIO-41 / EPWM-7B
GPIO-42 / -- / -- / COMP1OUT	31	81	GPIO-43 / -- / -- / COMP2OUT
GPIO-44	32	82	+5V in
GPIO-12 / TZ1n / SCITX-A / SPISIMO-B	33	83	GPIO-13 / TZ2n / -- / SPISOMI-B
GPIO-15 / TZ1n / LINRX-A / SPISTE-B	34	84	GPIO-14 / TZ3n /LINTX-A / SPICLK-B
GPIO-24 / ECAP1 / -- / SPISIMO-B	35	85	GPIO-25 / -- / -- / SPISOMI-B
GPIO-26 / -- / -- / SPICLK-B	36	86	GPIO-27 / -- / -- / SPISTE-B
GND	37	87	+5V in
GPIO-16 / SPISIMO-A / -- / TZ2n	38	88	GPIO-17 / SPISOMI-A / -- / TZ3n
GPIO-18 / SPICLK-A / LINTX-A / XCLKOUT	39	89	GPIO-19 / SPISTE-A / LINRX-A / ECAP1
GPIO-20 / EQEP1A / -- / COMP1OUT	40	90	GPIO-21 / EQEP1B / -- / COMP2OUT
GPIO-22 / EQEP1S / -- / LINTX-A	41	91	GPIO-23 / EQEP1I / -- / LINRX-A
GPIO-39	42	92	+5V in
GPIO-28 / SCIRX-A / I2CSDA-A / TZ2n	43	93	GPIO-29 / SCITX-A / I2CSCL-A / TZ3n
GPIO-30 / CANRX-A / -- / --	44	94	GPIO-31 / CANTX-A / -- / --
GPIO-32 / I2CSDA-A / EPWMSYNCI / ADCSOCA	45	95	GPIO-33 / I2CSCL-A / EPWMSYNCO / ADCSOCB
GPIO-34 / COMP2OUT / EMU1 / COMP3OUT	46	96	+5V in
GND	47	97	
Spare	48	98	*Spare*
Spare	49	99	*Spare*
Spare	50	100	*Spare*