:			cCARD Pinout:		F28388D			Р		
ay2020										_
	HSEC pin 1	MCU pin	MCU Usage for Std	JTAG-EMU1	HSEC cCARD standard  JTAG-EMU0	MCU Usage for Std	MCU pin	HSEC pin		F
	3	TMS		JTAG-TMS	JTAG-TRSTn		TRSTn	4		I
	7	TCK VSS		JTAG-TCK GND	JTAG-TDO JTAG-TDI		TDO	6 8		±
	9 11	ADC-A0 ADC-A1		ADC1 (and/or DACA) ADC1 (and/or DACB)	GND ADC2		VSS ADC-B0	10 12		+
	13			GND	ADC2		ADC-B1	14		E
	15 17	ADC-A2 ADC-A3		ADC1 (and/or CMPIN+) ADC1	GND ADC2		ADC-B2	16 18		$\vdash$
	19	VSS ADC-A4		GND ADC1 (and/or CMPIN+)	ADC2		ADC-B3	20		F
<u> </u>	21 23	ADC-A5		ADC1	GND ADC2		VSS ADC-B4	22 24	Analog	E
<u></u>	25 27	ADCIN14 ADCIN15		ADC (and/or CMPIN+) ADC	ADC2 ADC		ADC-B5 ADC-D0	26 28	∺	$\vdash$
Analog	29			GND	ADC		ADC-D1	30	Ĕ	F
_ ⋖	31 33	ADC-C2 ADC-C3		ADC ADC	Rsv ADC		ADC-D2	32 34	⋖	
	35 37	VSS ADC-C4		GND ADC	ADC GND		ADC-D3 VSS	36 38		F
	39	ADC-C5		ADC	ADC		ADC-D4	40		E
	41 43			Rsv VREFLO	ADC Rsv		ADC-D5	42 44		
	45	All VREFHIS *		VREFHI	GND		VSS	46		İ
	47 49	VSS GPIO-00		GND PWM1A	PWM3A		GPIO-04	50		t
	51 53	GPIO-01 GPIO-02		PWM1B PWM2A	PWM3B PWMA		GPIO-05 GPIO-06	52 54		H
	55	GPIO-03		PWM2B	PWMA		GPIO-08 GPIO-07	54 56		F
	57	GPIO-08		PWMA	PWMA or TZ1		GPIO-12	58		1
	59	GPIO-09		PWMB	PWMB or TZ2		GPIO-13	60		F
-	61 63	GPIO-10 GPIO-11		PWMA PWMB	PWMA or TZ3 PWMB or TZ4		GPIO-14 GPIO-15	62 64		+
	65			GND	Rsv			66		F
	67 69	GPIO-16 GPIO-17		SPISIMO SPISOMI	QEPA or McBSP-MDX QEPB or McBSP-MDR		GPIO-20 GPIO-21	68 70		1
	71 73	GPIO-18 GPIO-19		SPICLK SPISTE	QEPS or McBSP-MCLKX QEPI or McBSP-MFSX		GPIO-22 GPIO-23	72 74		F
	75	GPIO-24		eCAP or SPISIMO	SCIRX/UARTRX		GPIO-28##	76		
	77 79	GPIO-25 GPIO-26		eCAP or SPISOMI eCAP or SPICLK	SCITX/UARTTX CANRX		GPIO-29## GPIO-30	78 80		ŀ
	81	GPIO-27		eCAP or SPISTE	CANTX		GPIO-31**	82		t
	83 85	VSS GPIO-32		GND I2CSDA	SV0 GPIO		GPIO-34**	84 86		ŀ
	87	GPIO-33		12CSCL	GPIO		GPIO39	88		ļ
	89 91	GPIO40 GPIO41		GPIO GPIO	GPIO GPIO		GPIO125 GPIO45	90 92		ŀ
	93 95	GPIO96 GPIO98		GPIO GPIO	GPIO GPIO		GPIO97 GPIO99	94 96		F
	97	VSS		GND	5V0			98		t
	99 101	GPIO-48 GPIO-49		SD-D SD-C	QEPA QEPB		GPIO-54 GPIO-55	100 102		ŀ
	103	GPIO-50		SD-D	QEPS		GPIO-56	104		t
	105 107	GPIO-51 GPIO-52		SD-C SD-D	GPIO or McBSP-MCLKR		GPIO-57 GPIO-58	106 108		ŀ
_	109	GPIO-53		SD-C	GPIO or McBSP-MFSR		GPIO-59	110	Digital	ŀ
<u> </u>	111 113	VSS		GND Rsv	Rsv			114	ej:	ŀ
Digital	115 117			Rsv Rsv	Rsv		VDD	116	Ä	ŀ
	119	VDDIO		VDDIO	Device Reset (Active low)		XRSn	120		ŀ
										L
	121 123	GPIO35 GPIO37		GPIO GPIO	GPIO GPIO		GPIO36 GPIO38	122 124		+
	125	GPIO60		GPIO	GPIO		GPIO61	126		İ
	127 129	GPI062 GPI064		GPIO GPIO	GPIO GPIO		GPIO63 GPIO65	128 130		1
	131	GPIO66		GPIO	GPIO		GPIO67	132		ļ
	133 135	GPI068		GPIO GND	GPIO Rsv		GPIO69	134 136		ŀ
-	137 139	GPI070 GPI072#		GPIO GPIO	GPIO GPIO		GPI071 GPI073	138 140		ŀ
	141	GPIO74		GPIO	GPIO		GPIO126	142		
-	143 145	GPIO76 GPIO78		GPIO GPIO	GPIO GPIO		GPI077 GPI079	144 146		1
	147	GPIO80		GPIO	GPIO		GPIO81	148		
	149 151	GPI082 GPI084#		GPIO GPIO	GPIO GPIO		GPIO83 GPIO85	150 152		1
-	153 155	GPIO86 GPIO88		GPIO GPIO	GPIO GPIO		GPI087 GPI089	154 156		F
	157			GND	5V0			158		-
	159 161	GPIO90 GPIO92		GPIO GPIO	GPIO GPIO		GPI091 GPI093	160 162		-
	163	GPIO94		GPIO	GPIO		GPIO133	164		
-	165 167	GPIO127 GPIO147		GPIO GPIO	GPIO GPIO		GPIO128 GPIO95	166 168		1
	169	GPI0104		GPIO	GPIO		GPI0107	170 172		
	171 173			Rsv Rsv	Rsv Rsv			174		ŀ
	175 177			Rsv Rsv	Rsv ERRORSTS		ERRORSTS	176 178		1
	177			GND	SV0		ENNONSIS	180		#
										+
	*******	may be altered to account of the	EC nin to th - \$40111 115	inquite						#
	** Connec		EC pin to the MCU's VREF	ni inputs						1
	#Boot mod	le pin t through isolation to embedded o	lehug probe							Ŧ
	connec	2051 ISSIGNATION TO EMBERGED C	prooc							+
										+