Mapping to DIM100 pinout with Adapter card
F28388D

Date: 20-May-2020

Exp Kit lbl	DIM pin			DIM pin	Exp Kit lbl
	1			51	
	2			52	
	3	12V	12V	53	
	4			54	
	5			55	
	6			56	
B0	7	ADC-B0	ADC-A0	57	A0
	8			58	
B1	9	ADC-B1	ADC-A1	59	A1
	10			60	
B2	11	ADC-B2	ADC-A2	61	A2
	12			62	
B3	13	ADC-B3	ADC-A3	63	A3
	14			64	
B4	15	ADC-B4	ADC-A4	65	A4
	16		All VREFHIs *	66	
B5	17	ADC-B5	ADC-A5	67	A5
58	18	GPIO-58	GPIO-59	68	59
B6	19	ADC-D0	ADCIN14	69	A6
60	20	GPIO125	GPIO45	70	61
B7	21	ADC-D1	ADCIN15	71	A7
62	22	GPIO97	GPIO99	72	63
00	23	GPIO-00	GPIO-01	73	01
02	24	GPIO-02	GPIO-03	74	03
04	25	GPIO-04	GPIO-05	75	05
06	26	GPIO-06	GPIO-07	76	07
	27		5V	77	
08	28	GPIO-08	GPIO-09	78	09
10	29	GPIO-10	GPIO-11	79	11
48	30	GPIO-12	GPIO-13	80	49
84	31	GPIO-14	GPIO-15	81	85
86	32	GPIO-48	5V	82	
12	33	GPIO40	GPIO41	83	13
15	34	GPIO98	GPIO96	84	14
24	35	GPIO-24	GPIO-25	85	25
26	36	GPIO-26	GPIO-27	86	27
	37		5V	87	
16	38	GPIO-16	GPIO-17	88	17
18	39	GPIO-18	GPIO-19	89	19
20	40	GPIO-20	GPIO-21	90	21
22	41	GPIO-22	GPIO-23	91	23
87	42	GPIO39	5V	92	
28	43	GPIO-28##	GPIO-29##	93	29
30	44	GPIO-30	GPIO-31**	94	31
32	45	GPIO-32	GPIO-33	95	33
34	46	GPIO-34**	5V	96	
	47			97	
	48	TCK	TDO	98	
	49	TMS	TRSTn	99	
	50			100	

*Switches may be altered to connect this HSEC pin to the MCU's VREFHI inputs
** Connected to LED
#Boot mode pin
Connect through isolation to embedded debug probe