

Date:		cCARD Pinout:		F280025		HSEC Pinout:		
12Mar2020						2.15		
Analog	HSEC pin	MCU pin	MCU Usage for Std	HSEC cCARD standard	HSEC cCARD standard	MCU Usage for Std	MCU pin	HSEC pin
	1			JTAG-EMU1	JTAG-EMU0			2
	3	TMS	TMS	JTAG-TMS	JTAG-TRSTn			4
	5	TCK	TCK	JTAG-TCK	JTAG-TDO	TDO	GPIO-37/TDO	6
	7	VSS		GND	JTAG-TDI	TDI	GPIO-35/TDI	8
	9	A0/C15/ADCINCAL/CMP3P	Analog	ADC1 (and/or DACA)	GND		VSS	10
	11	A1/CMP1P	Analog	ADC1 (and/or DACb)	ADC2	Analog	A11/C0/CMP1/TESTANA0	12
	13			GND	ADC2	Analog	A12/C1/TESTANA1/CMP2/CMP4P	14
	15	A2/C9/CMP1P/CMP3	Analog	ADC1 (and/or CMPIN+)	GND			16
	17	A3/C5/VDAC/CMP3	Analog	ADC1	ADC2	Analog	C4/A14/CMP3P	18
	19	VSS		GND	ADC2	Analog	C6/CMP3P	20
	21	A4/C14/CMP2P/CMP4	Analog	ADC1 (and/or CMPIN+)	GND		VSS	22
	23	A5/C2/CMP3	Analog	ADC1	ADC2	Analog	A15/C7/CMP1	24
	25	A6/CMP1P	Analog	ADC (and/or CMPIN+)	ADC2	Analog	C8/A9/CMP4P/CMP2P	26
	27	A7/C3/CMP4	Analog	ADC	ADC	Analog	A10/C10/CMP2	28
	29	-		GND	ADC	Analog	A8/C11/CMP2P/CMP4P #	30
	31	-		ADC	Rsv			32
	33	-		ADC		Analog	A8/C11/CMP2P/CMP4P #	34
	35	VSS		GND	ADC			36
	37	-		ADC	GND		VSS	38
	39	-		ADC				40
	41	-		Rsv	ADC			42
	43	-		VREFLO	Rsv			44
	45	All VREFHIs *	VREFHI	VREFHI	GND		VSS	46
	47	VSS		GND	SV0			48
	49	GPIO-00	PWM1_A	PWM1A	PWM3A	PWM3_A	GPIO-04	50
	51	GPIO-01	PWM1_B	PWM1B	PWM3B	PWM3_B	GPIO-05	52
	53	GPIO-02	PWM2_A	PWM2A	PWMA	PWM4_A	GPIO-06	54
55	GPIO-03	PWM2_B	PWM2B	PWMB	PWM4_B	GPIO-07	56	
Digital	57	GPIO-08	PWM5_A	PWMA	PWMA or TZ1	PWM7_A	GPIO-12 @	58
	59	GPIO-09	PWM5_B	PWMB	PWMB or TZ2	PWM7_B	GPIO-13 @	60
	61	GPIO-10	PWM6_A	PWMA	PWMA or TZ3	PWM8_A	GPIO-14	62
	63	GPIO-11 @	PWM6_B	PWMB	PWMB or TZ4	PWM8_B	GPIO-15	64
	65	-		GND	Rsv			66
	67	GPIO-16	SPIA_SIMO	SPISIMO	QEPA or McBSP-MDX	EQEP1_A	GPIO-40	68
	69	GPIO-17	SPIA_SOMI	SPISOMI	QEPB or McBSP-MDR	EQEP1_B	GPIO-41	70
	71	GPIO-18/X2 ##	SPIA_CLK	SPICLK	QEPA or McBSP-MCLKX	EQEP1_STROBE	GPIO-22	72
	73	GPIO-19/X1 ##	SPIA_STE	SPISTE	QEPI or McBSP-MFSX	EQEP1_INDEX	GPIO-23	74
	75	GPIO-24 ** #	SPIB_SIMO	eCAP or SPISIMO	SCIRX/UARTRX	SCIA_RX	GPIO-28 @ @	76
	77	GPIO-25 # @	SPIB_SOMI	eCAP or SPISOMI	SCITX/UARTTX	SCIA_TX	GPIO-29 @ @	78
	79	GPIO-26 # @	SPIB_CLK	eCAP or SPICLK	CANRX	CANA_RX	GPIO-30	80
	81	GPIO-27 # @	SPIB_STE	eCAP or SPISTE	CANTX	CANA_TX	GPIO-31	82
	83	VSS		GND	SV0			84
	85	GPIO-32**	I2CA_SDA	I2CSDA	GPIO	GPIO	GPIO-34	86
	87	GPIO-33	I2CA_SCL	I2CSCL	GPIO	GPIO	GPIO-37/TDO	88
	89	GPIO-39	GPIO	GPIO	GPIO	GPIO	GPIO-35/TDI	90
	91	-		GPIO	GPIO			92
	93	-		GPIO	GPIO			94
	95	-		GPIO	GPIO			96
	97	VSS		GND	SV0			98
	99	GPIO-42	GPIO	SD-D	QEPA	EQEP2_A	GPIO-24**#	100
	101	GPIO-43	GPIO	SD-C	QEPB	EQEP2_B	GPIO-25#	102
	103	GPIO-44	GPIO	SD-D	QEPA	EQEP2_INDEX	GPIO-27#	104
	105	GPIO-45	GPIO	SD-C	QEPI	EQEP2_STROBE	GPIO-26#	106
	107	GPIO-46	GPIO	SD-D	GPIO or McBSP-MCLKR			108
	109	-		SD-C	GPIO or McBSP-MFSR			110
	111	VSS		GND	SV0			112
	113	-		Rsv	Rsv			114
	115	-		Rsv	Rsv			116
	117	-		ERRORSTS	VDD		VDD	118
	119	VDDIO		VDDIO	Device Reset (Active low)	XRSn	XRSn	120
	121	-		GPIO	GPIO			122
	123	-		GPIO	GPIO			124
	125	-		GPIO	GPIO			126
	127	-		GPIO	GPIO			128
	129	-		GPIO	GPIO			130
	131	-		GPIO	GPIO			132
	133	-		GPIO	GPIO			134
	135	-		GND	Rsv			136
	137	-		GPIO	GPIO			138
	139	-		GPIO	GPIO			140
	141	-		GPIO	GPIO			142
	143	-		GPIO	GPIO			144
	145	-		GPIO	GPIO			146
	147	-		GPIO	GPIO			148
	149	-		GPIO	GPIO			150
	151	-		GPIO	GPIO			152
	153	-		GPIO	GPIO			154
	155	-		GPIO	GPIO			156
	157	-		GND	SV0			158
	159	-		GPIO	GPIO			160
	161	-		GPIO	GPIO			162
	163	-		GPIO	GPIO			164
	165	-		GPIO	GPIO			166
	167	-		GPIO	GPIO			168
	169	-		GPIO	GPIO			170
	171	-		Rsv	Rsv			172
	173	-		Rsv	Rsv			174
	175	-		Rsv	Rsv			176
	177	-		Rsv	Rsv			178
	179	-		GND	SV0			180
* Resistors may be altered to connect this HSEC pin to the MCU's VREFHI inputs								
** Boot mode pin								
# Signal can be route to one of two HSEC pins via switch								
## GPIO can only be used with INTOSC								
@ Signal is shared with FSI header and can be disconnected by desoldering resistors.								
@@ Can be connected to PC terminal via switch								
"- Signal is a "no connect"								