Date:		Mapping to	DIM100 pinout with Ada	pter card		HSEC Pinout:
4-Mar-2015		F28377D (180pin)			2.11	
Exp Kit lbl	DIM pin				DIM pin	Exp Kit lbl
EXP INC IO	1				51	EXP INC IO
-	2				52	
-	3	12V		12V	53	
	4				54	
	5				55	
	6				56	
_						
B0		ADC-B0		ADC-A0, DAC		A0
	8				58	
B1		ADC-B1		ADC-A1, DAC		A1
	10	ADC D2 COMD.		ADC 42 COMB.	60	42
B2	11 12	ADC-B2, COMP+		ADC-A2, COMP+	61 62	A2
_ B3		ADC-B3		ADC-A3		A3
ВЗ	14			ADC-AS	64	7.3
B4		ADC-B4, COMP+		ADC-A4, COMP+		A4
- 54	16			All VREFHIS *	66	
B5		ADC-B5		ADC-A5		A5
58		GPIO-58		GPIO-59		59
B6	19	ADC-D0, COMP+		ADCIN14, COMP+	69	A6
60	20	GPIO-44		GPIO-45	70	61
В7	21	ADC-D1		ADCIN15	71	A7
62	22	GPIO-46 ***		GPIO-47 ***	72	63
. 00	23	GPIO-00		GPIO-01	73	01
02		GPIO-02		GPIO-03		03
04		GPIO-04		GPIO-05		05
06		GPIO-06		GPIO-07		07
	27			5V	77	
08	28	GPIO-08		GPIO-09	78	09
10	29	GPIO-10		GPIO-11	79	11
48	30	GPIO-12		GPIO-13	80	49
84	31	GPIO-14		GPIO-15	81	85
86		GPIO-48		5V	82	
12		GPIO-40		GPIO-41		13
15		GPIO-43 ***		GPIO-42 ***		14
24		GPIO-24		GPIO-25		25
26		GPIO-26		GPIO-27		27
10	37	CDIO 16		500 17	87	
_ 16 18		GPIO-16 GPIO-18		GPIO-17 GPIO-19		17 19
20		GPIO-18 GPIO-20		GPIO-19		21
22		GPIO-22		GPIO-23		23
87		GPIO-39		5V	92	
28		GPIO-28		GPIO-29		29
30		GPIO-30		GPIO-31		31
32		GPIO-32		GPIO-33		33
34	46	GPIO-34		5V	96	
	47			TDI	97	
	48	TCK		TDO	98	
	49	TMS		TRSTn	99	
	50	GPIO-71 **		GPIO-70 **	100	
	* Switches	may be altered to connect this HSEC pir	n to the MCU's VREFHI inputs			
		esistors can be removed in order to tie G				
			rough the connector via jumpers on the cCARD	)		