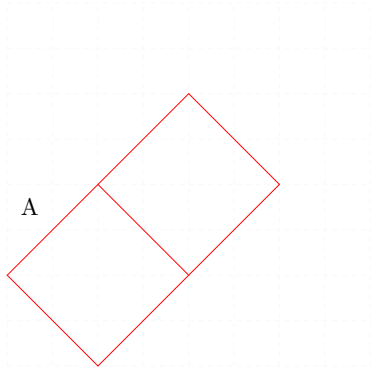
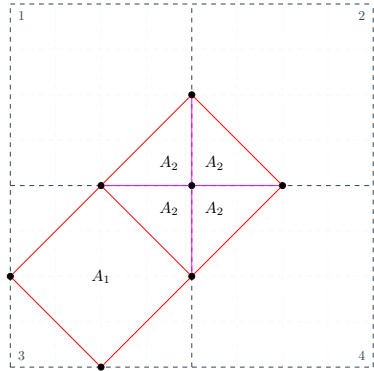


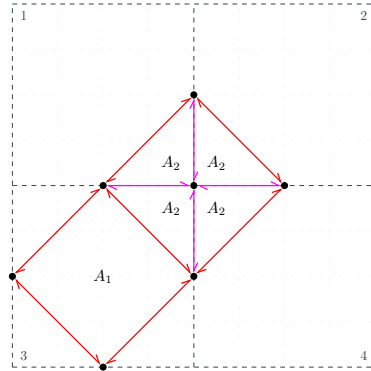
Input Layer A



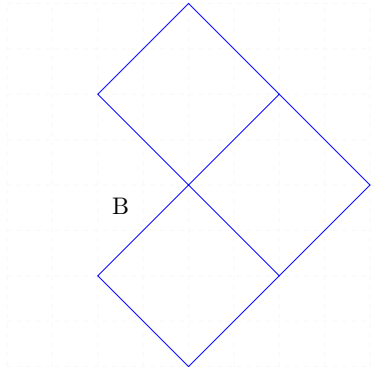
Clipped Edges A



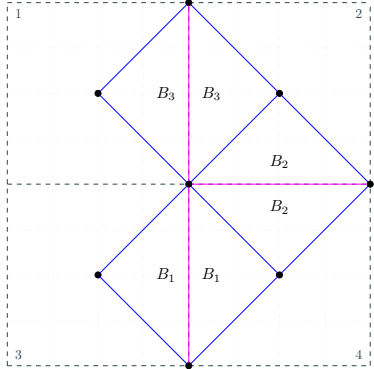
Partitioned DCEL for A



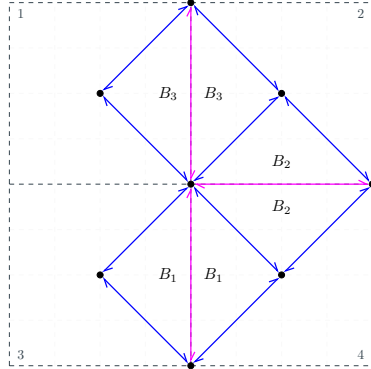
Input Layer B



Clipped Edges B



Partitioned DCEL for B



DCELs for overlay at partition level

