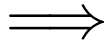
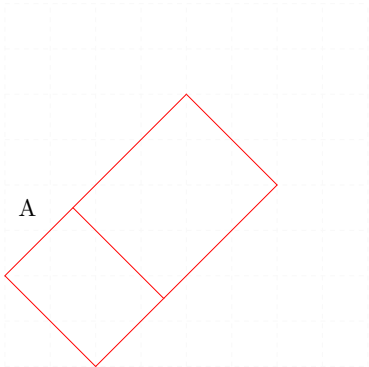
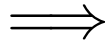
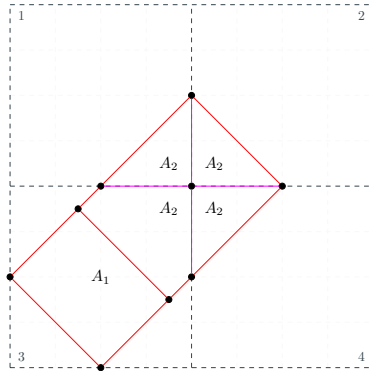


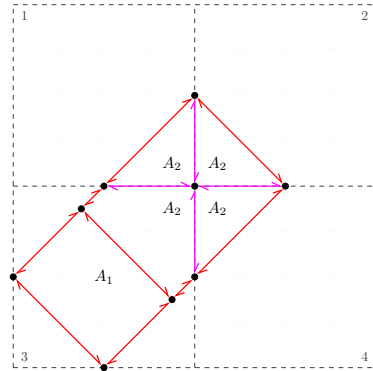
Input Layer A



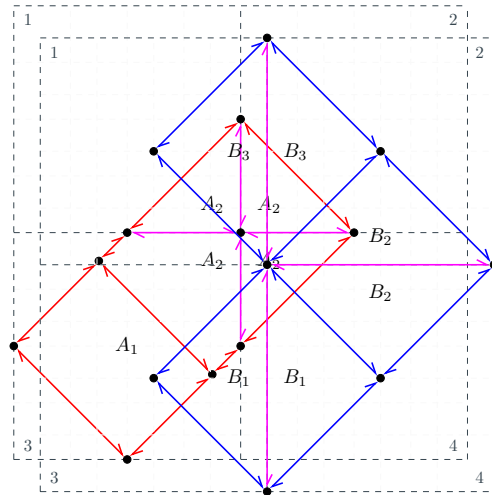
Clipped Edges A



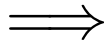
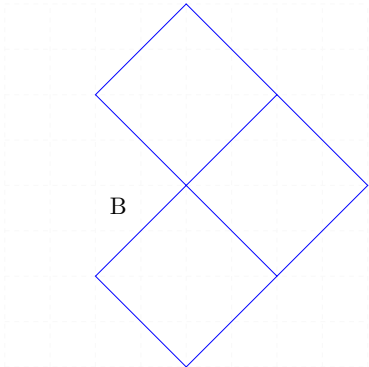
Partitioned DCEL for A



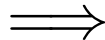
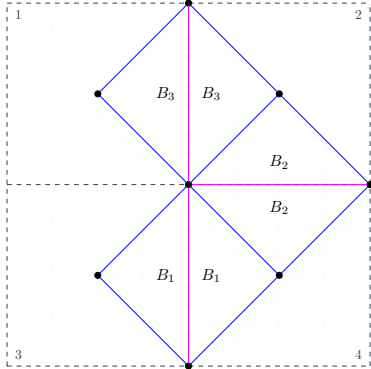
DCELs for overlay at partition level



Input Layer B



Clipped Edges B



Partitioned DCEL for B

