

ChronusQ::InCoreCholesky  
RIERI::computeDiagonalLibcint

ChronusQ::InCoreCholesky  
RIERI::computeDiagonalLibint

ChronusQ::computeDiagonal  
Prebuilt4Index

```
graph LR; A["ChronusQ::InCoreCholesky<br/>RIERI::computeDiagonalLibcint"] --> C["ChronusQ::computeDiagonal<br/>Prebuilt4Index"]; B["ChronusQ::InCoreCholesky<br/>RIERI::computeDiagonalLibint"] --> C;
```

The diagram illustrates a mapping or compilation process. On the left, there are two identical source boxes. Each box contains two lines of text: 'ChronusQ::InCoreCholesky' and 'RIERI::computeDiagonalLibcint' (top) or 'RIERI::computeDiagonalLibint' (bottom). Blue arrows point from the right side of each source box to a single target box on the right. The target box is shaded gray and contains the text 'ChronusQ::computeDiagonal' and 'Prebuilt4Index'.