



### **Bank 1**

Power

8 TX Channels CDR  
8 RX Channels CDR

JTAG

SMBus

CLKIN0, CLKOUT0

### **Bank 2**

Power

D(39:0)

-or-

D(3:0) + LVDS

CLKIN1, CLKOUT1

### **Bank 3**

Power

D(79:40)

-or-

LVDS

CLKIN2, CLKOUT2