

Chapter 1

Schematic and PCB Layout

The schematic of the HSMC-to-VLDB PCB is shown in figures 1.1, 1.2 and 1.3. The PCB layout is shown in figures 1.4 and 1.5.



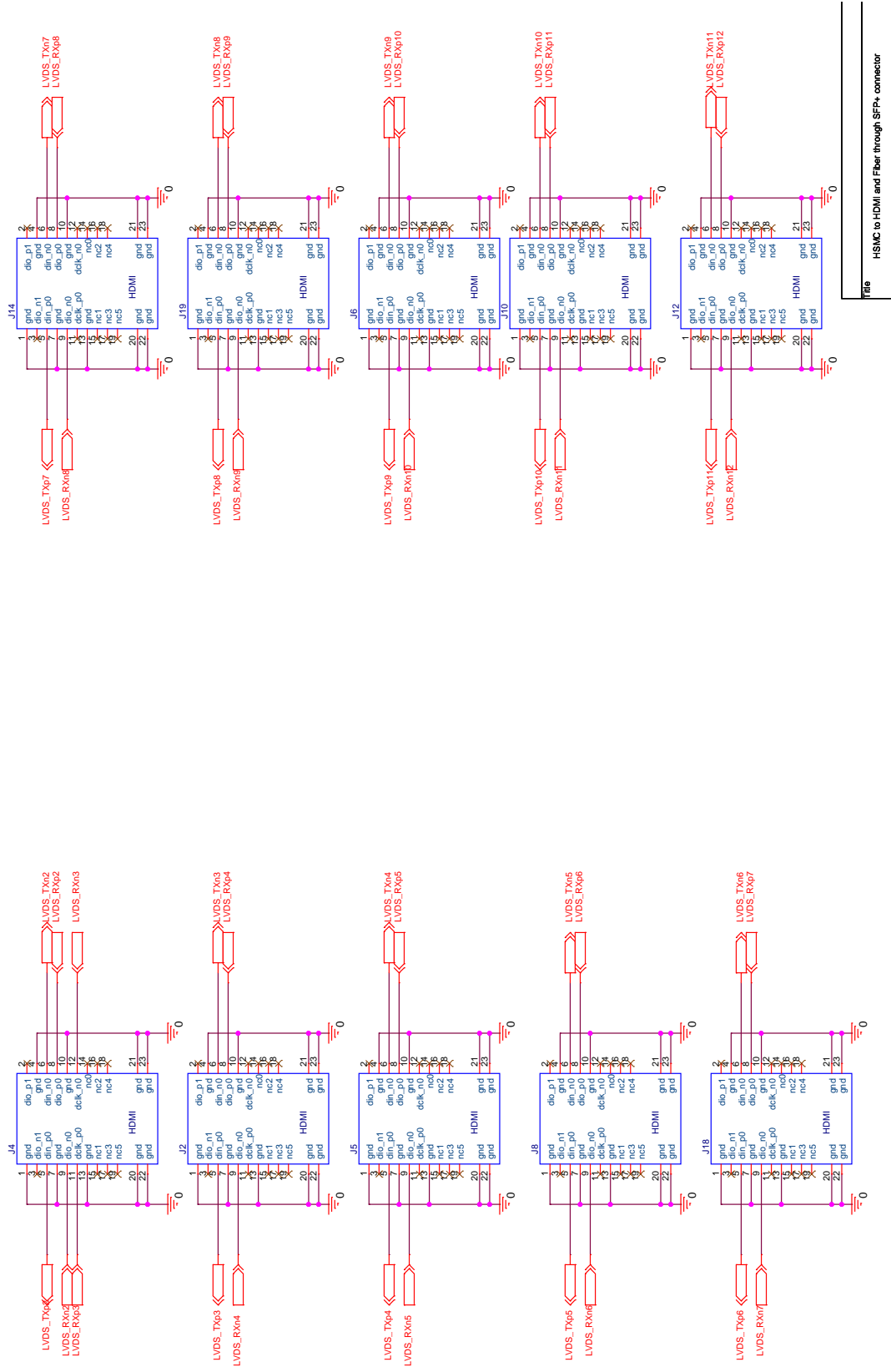
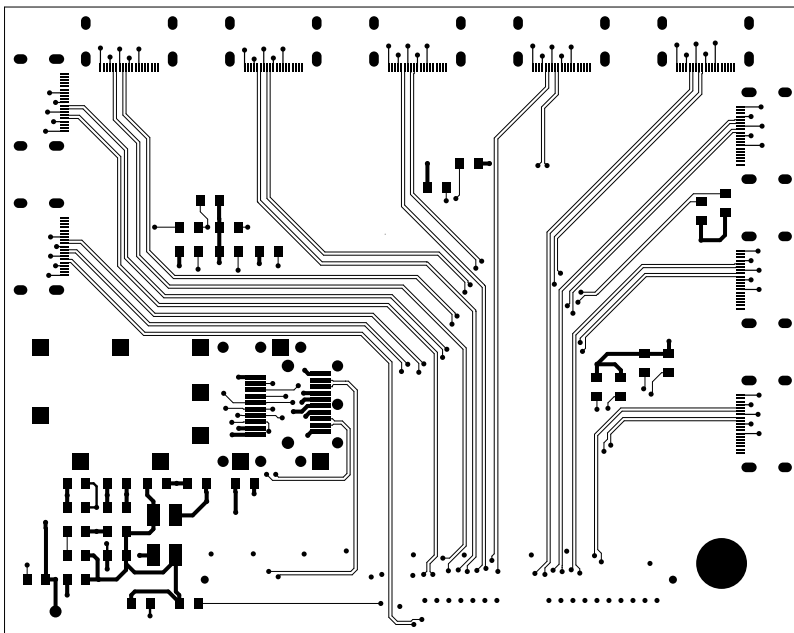
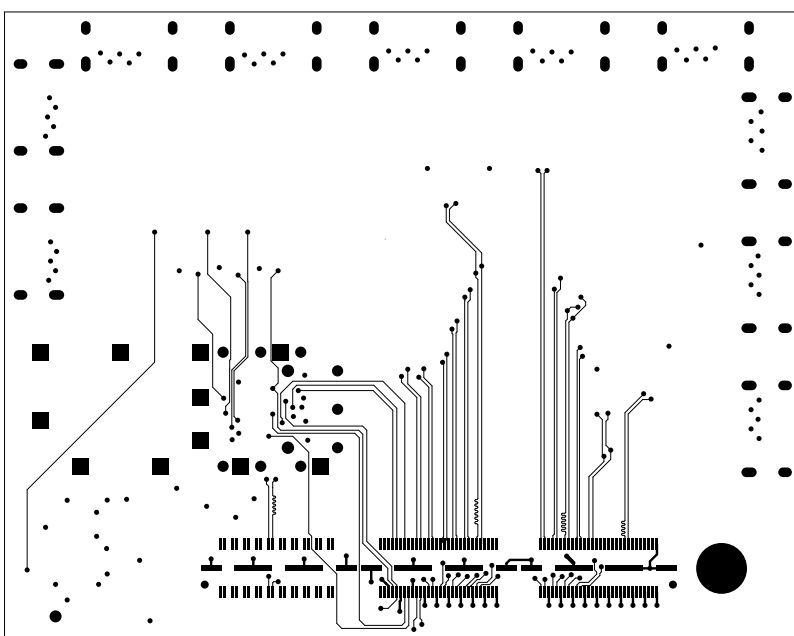


Figure 1.2: PCB Schematic, HDMI connectors.



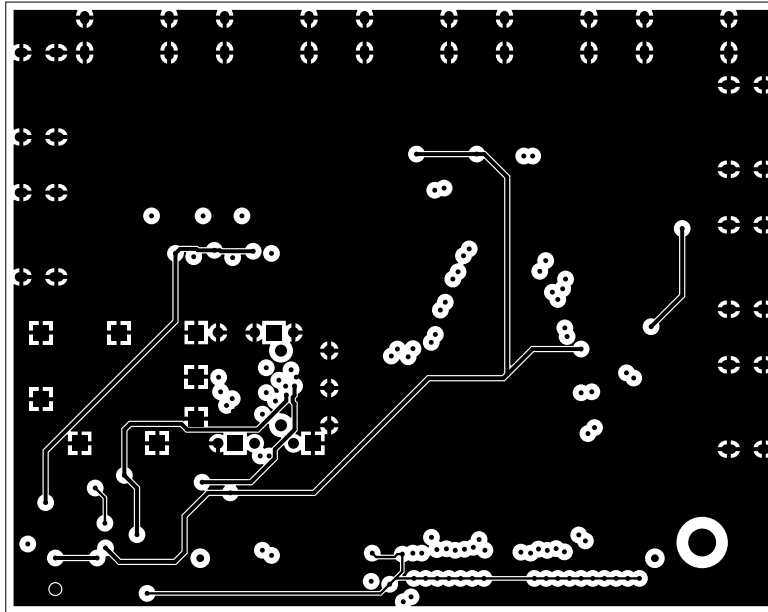


(a) Copper top.

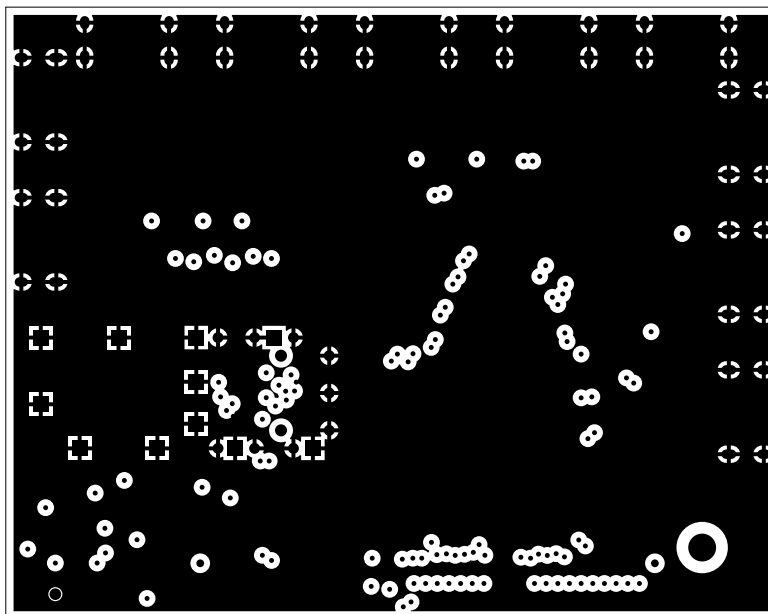


(b) Copper bottom.

Figure 1.4: PCB Layout, top and bottom layers.



(a) Inner layer: voltage.



(b) Inner layer: ground.

Figure 1.5: PCB Layout, inner layers.