Linking Part2

Relocation

Relocating sections and symbol definitions

```
typedef struct {

long offset; /* Offset of the reference to relocate */

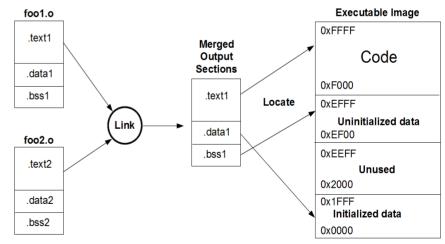
long type:32, /* Relocation type */

symbol:32; /* Symbol table index */

long addend; /* Constant part of relocation expression */

Elf64_Rela;

- code/link/elfstructs.c
```



https://nhivp.github.io/msp430-gcc/ 2018-07-19/linker-scripts

Relocation

Relocating symbols within sections

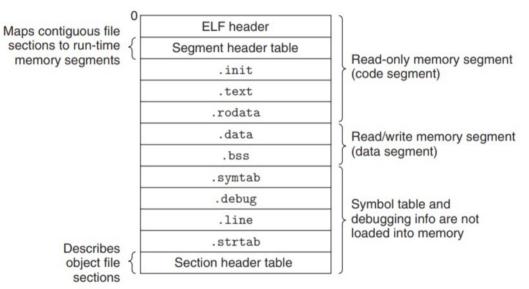
Find the places that need relocation

ELF executable

readelf -l /static_link/prog2

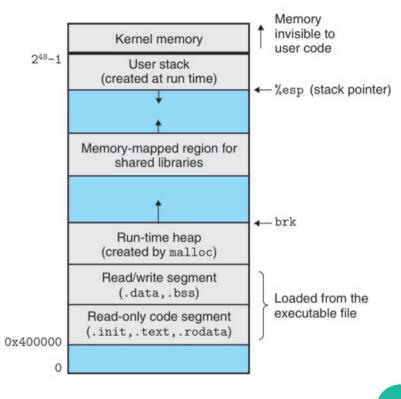
Contiguous memory segment chunks indicated by program

header table



Loading executable

- Coping code and jump to entry po
- Is guided by program header table



Shared Library

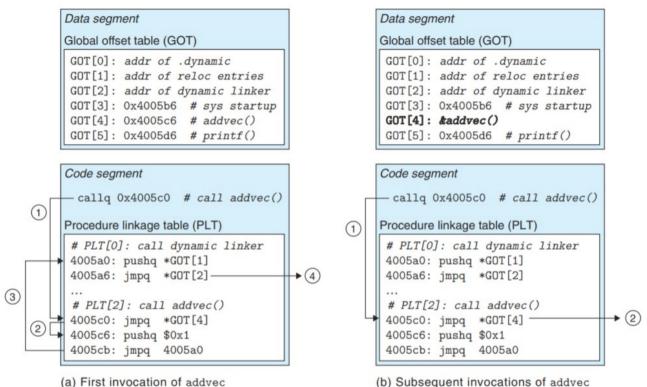
- No duplicate libraries on the same system
- One copy of .text can be shared by different running processes
- Mostly position independent code

Position independent code

- Does not need relocation
- Data reference: global offset table(GOT)
- Function call: procedure linkage table(PLT)

PLT & GOT

Resolve the address at first call



https://www.youtube.com/watch?v=kUk5pw4w0h4

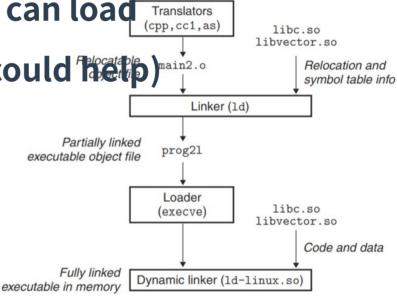
Dynamic linking

Resolve address at run-time

If use in conjunction <dlfcn.h> you can load

shared libraries freely(-rdynamic could help) ain 2.0

Example time :shrimp:



vector.h

main2.c

Interposition

- Something like a decorator in python??
 - Compile-time
 - Link-time
 - Run-time
- Let's look at some code:)
 https://github.com/Alanasdw/csapp_ch7

