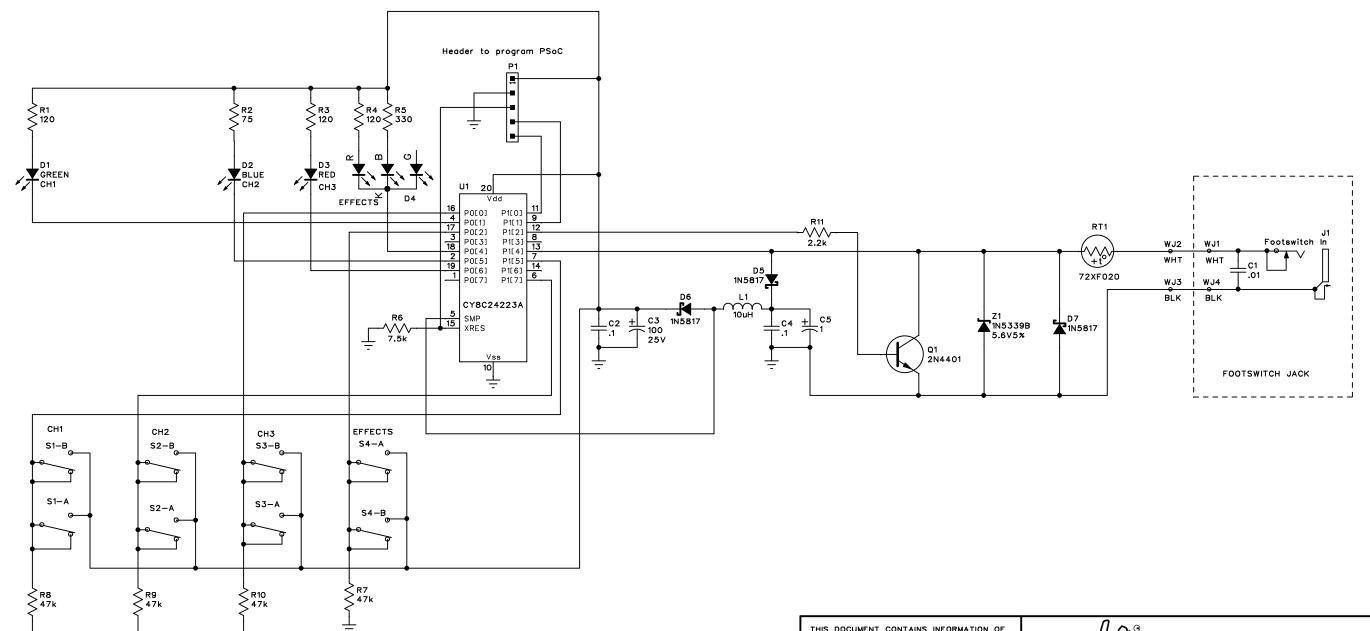
ISSP interface to PSoC is provided as one way to program the PSoC during initial testing (Ref. Cypress Application Note AN2014)

REVISIONS			
REV.	DESCRIPTION	DATE	APPROVED
Α	PR1045	17-AUG-11	нвк
В	EC5047	07-MAY-12	нвк



- 4. THIS SCHEMATIC IS FOR PCB FABRICATION P/N 0091168000 AND PCB ASSEMBLY 0091167000.
- 3. ALL POLARIZED CAPACITORS IN uF, 20%; 50V MINIMUM.
- ALL UNPOLARIZED CAPACITORS IN uF, 10% OR BETTER; 50V MINIMUM. (POWER SUPPLY BYPASS CAPACITORS ARE 20%).
- 1. ALL RESISTORS IN OHMS, 5%; 1/4W.

NOTES: (UNLESS OTHERWISE NOTED)

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CHECKED BY: _____

APPROVED BY: _____

DRAWN: B. DESI ENGR: H. KAPLAN
DATABASE FILE: Z1045S2.SCH

Cheve pro

MUSICAL INSTRUMENTS Corona, CA U.S.A.

TITLE: SERVICE DIAGRAM, COMBINED (schematic)
EVH 5150 III FOOTSWITCH
4 BTN, 2-WIRE

SIZE DRAWING NUMBER REV. B

RELEASE DATE: 17-AUG-2011 SHEET: 1 OF 2