		I Tenice Hotkeve	11K eve		Simulator Di
ţ	Specific	lodanio		tilloN	Command
1	ouncillatio	oymnu	Wavelulli	Nellist	.AC
	ESC - Exit Mode	ESC - Exit Mode			BACKANNO
	F3 – Draw Wire				.DC
S	F5 – Delete	F5 – Delete	F5 – Delete		.END
əp	F6 – Duplicate	F6 — Duplicate			ENDS
0 [/	F7 – Move	F7 – Move			FOUR
4	F8 – Drag	F8 – Drag			FUNC
	F9 – Undo	F9 — Undo	F9 – Undo	F9 – Undo	FERRET
	Shift+F9 – Redo	Shift+F9 – Redo	Shift+F9 — Redo	Shift+F9 — Redo	.GLOBAL
	Ctrl+Z – Zoom Area	Ctrl+Z – Zoom Area	Ctrl+Z – Zoom Area		<u>D.</u>
	Ctrl+B – Zoom Back	Ctrl+B – Zoom Back	Ctrl+B – Zoom Back		INCLUDE
	Space – Zoom Fit		Ctrl+E – Zoom Extents		.LIB
N	Ctrl+G – Toggle Grid		Ctrl+G – Toggle Grid	Ctrl+G - Goto Line #	.LOADBIAS
ıəi	U – Mark Unncon. Pins	Ctrl+W - Attribute Window	'0' - Clear		.MEASURE
٨	A – Mark Text Anchors	Ctrl+A – Attribute Editor	Ctrl+A – Add Trace		.MODEL
	AtI+Click - Power		Ctrl+Y – Vertical Autorange	Ctrl+R – Run Simulation	.NET
	Ctrl+Click - Attr. Edit		Ctrl+Click - Average		.NODESET
	Ctrl+H – Halt Simulation		Ctrl+H – Halt Simulation	Ctrl+H – Halt Simulation	.NOISE
	R – Resistor	R – Rectangle	Command	Command Line Switches	.0P
	C – Capacitor	C – Circle			OPTIONS.
	L – Inductor	L-Line		1100 1100 1100 1100 1100 1100 1100 110	.PARAM
	D - Diode	A – Arc	IIDS	Use ASUII :raw Tiles. (Degrades performance!)	SAVE
9	G – GND		_	mode.	SAVEBIAS
9 C	S - Spice Directive		-big or -max Start as a max	Start as a maximized window.	STEP
l			- Encrypt Fncrynt a model lihrary	Tel lihrary	.0.

	Flag	Short Description
	-ascii	Use ASCII .raw files. (Degrades performance!)
	q-	Run in batch mode.
	-big or -max	Start as a maximized window.
	-encrypt	Encrypt a model library.
	-FastAccess	Convert a binary .raw file to Fast Access Format.
	-netlist	Convert a schematic to a netlist.
	-nowine	Prevent use of WINE(Linux) workarounds.
	-PCBnetlist	Convert a schematic to a PCB netlist.
7	-registry	Store user preferences in the registry.
	-Run	Start simulating the schematic on open.
	-801	Allow MOSFET's to have up to 7 nodes in subcircuit.
	-uninstall	Executes one step of the uninstallation process.
	-wine	Force use of WINE(Linux) workarounds.

Ctrl+E – Mirror Ctrl+R – Rotate

Ctrl+R - Rotate

T – Text

F2 - Component

T – Text

d

F4 - Label Net Ctrl+E - Mirror



Simulator Directives -CommandShort D.ACPerform a.BACKANNOAnnotate.DCPerform a.ENDEnd of Su.ENDEnd of Su.ENDSEnd of Su.ENDSEnd of Su.FOURCompute.FUNCUser Define a.ICSet Initial.ICSet Initial.ICSet Initial.INCLUDEInclude a.INCLUDEInclude a.INCLUDELoad a Pr.MODELEvaluate U.NODESETSupply Hi.NOISEPerform a.NOISEPerform a.OPFind the C.OPTIONSSet Simul.SAVELimit the.SAVELimit the.SAVELimit the	Short Description Short Description Perform a Small Signal AC Analysis Annotate the Subcircuit Pin Names on Port currents Perform a DC Source Sweep Analysis End of Netlist End of Subcircuit Definition Compute a Fourier Component User Defined Functions Download a File Given the URL Declare Global Nodes Set Initial Conditions Include another File Include a Library Load a Previously Solved DC Solution Evaluate User-Defined Electrical Quantities Define a SPICE Model Compute Network Parameters in a .AC Analysis Supply Hints for Initial DC Solution Perform a Noise Analysis Find the DC Operating Point Set Simulator Options Limit the Quantity of Saved Data Save Operating Point to Disk
STEP.	Parameter Sweeps Define a Subcircuit
.TEMP .TRAN .WAVE	Temperature Sweeps Find the DC Small-Signal Transfer Function Do a Nonlinear Transient Analysis Write Selected Nodes to a .WAV file

			I		
		J	1e-15	ш	2.7182818284590452354
—	1e12	р	1e-12	Pi	3.14159265358979323846
5	1e9	u	1e-9	ᅩ	1.3806503e-23
Meg 1e6	1e6	n	1e-6	Ø	1.602176462e-19
\prec	1e3	M	1e-3	TRUE	1
		Mil	25.4e-6	FALSE 0	0

See Demo