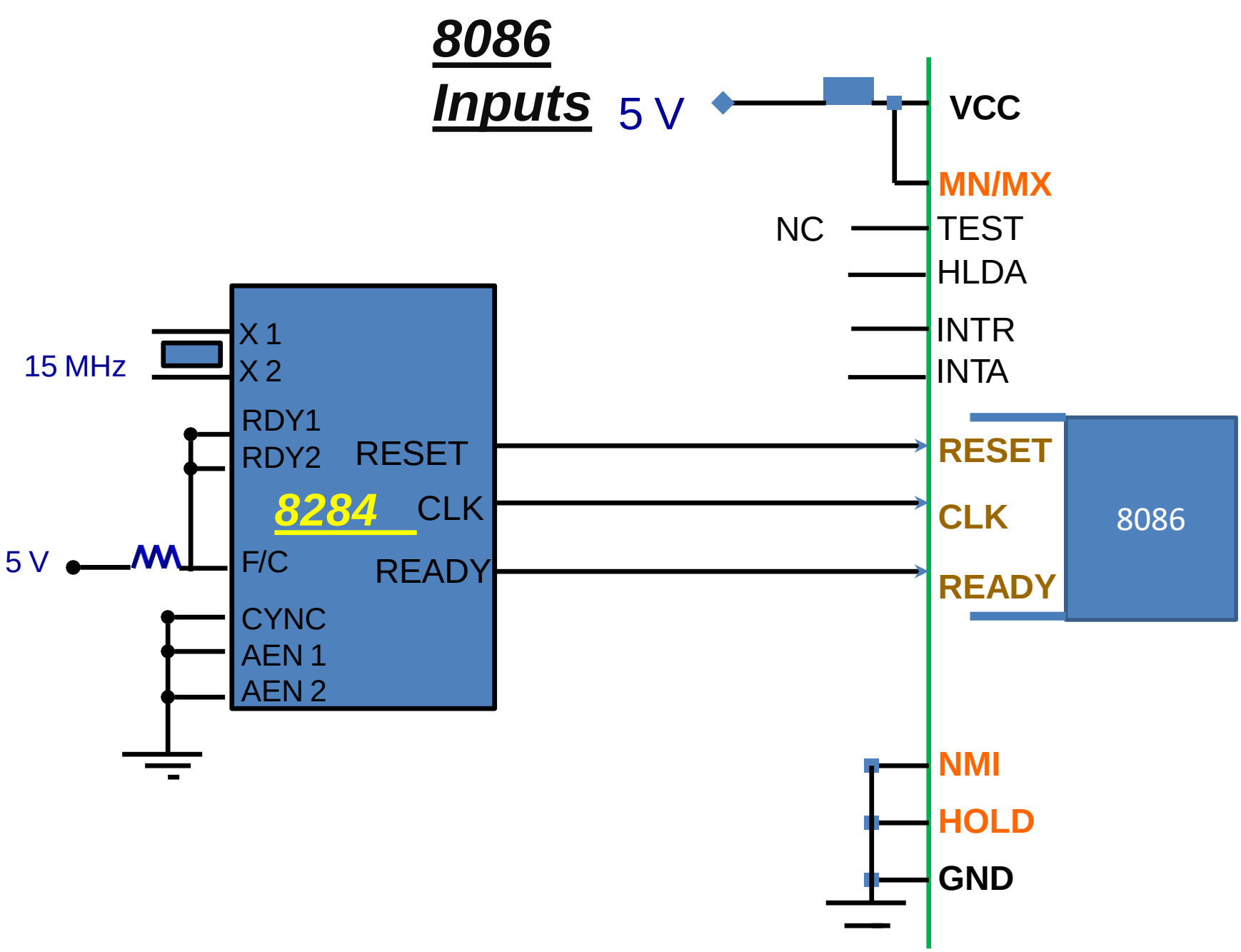
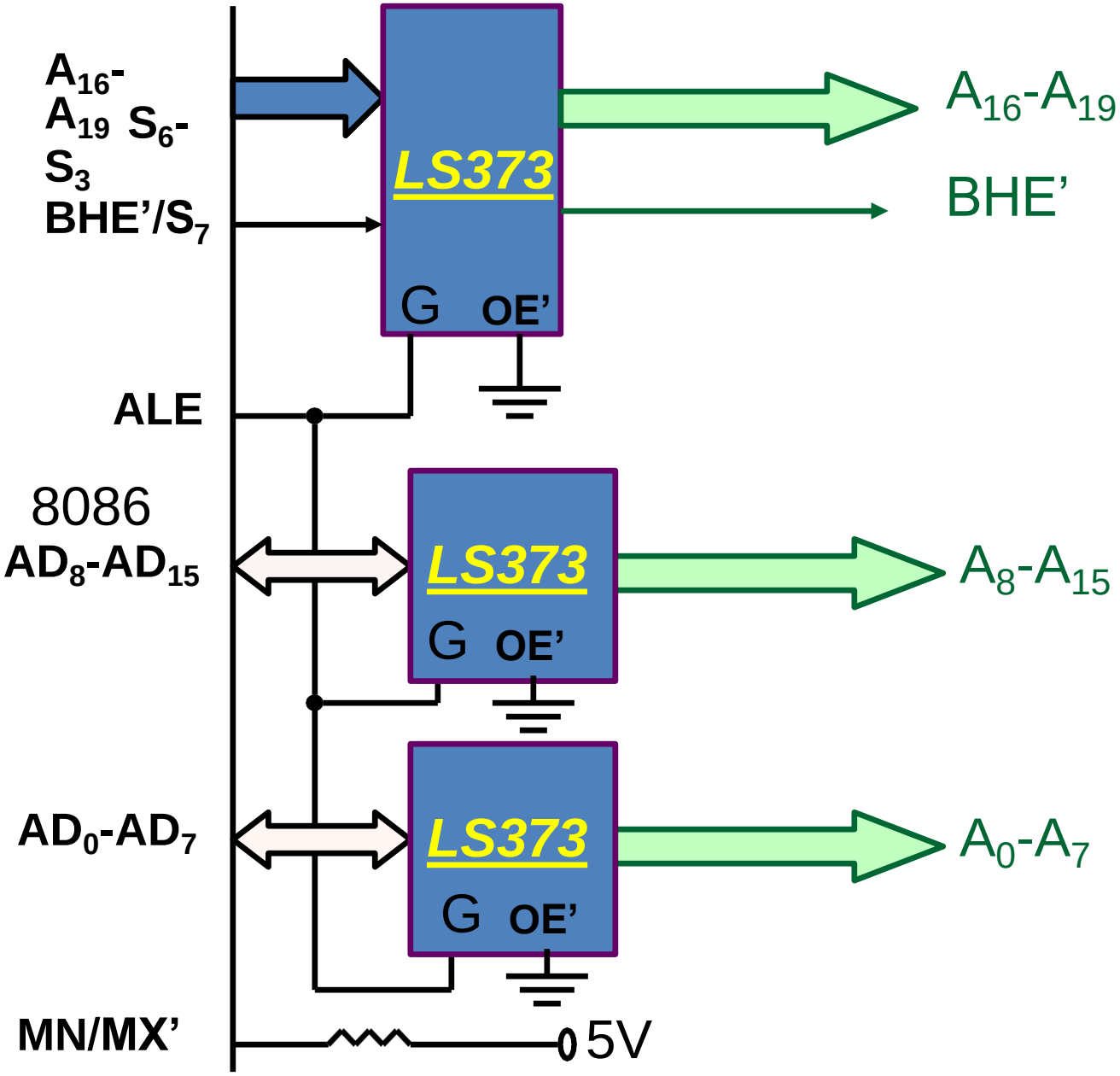
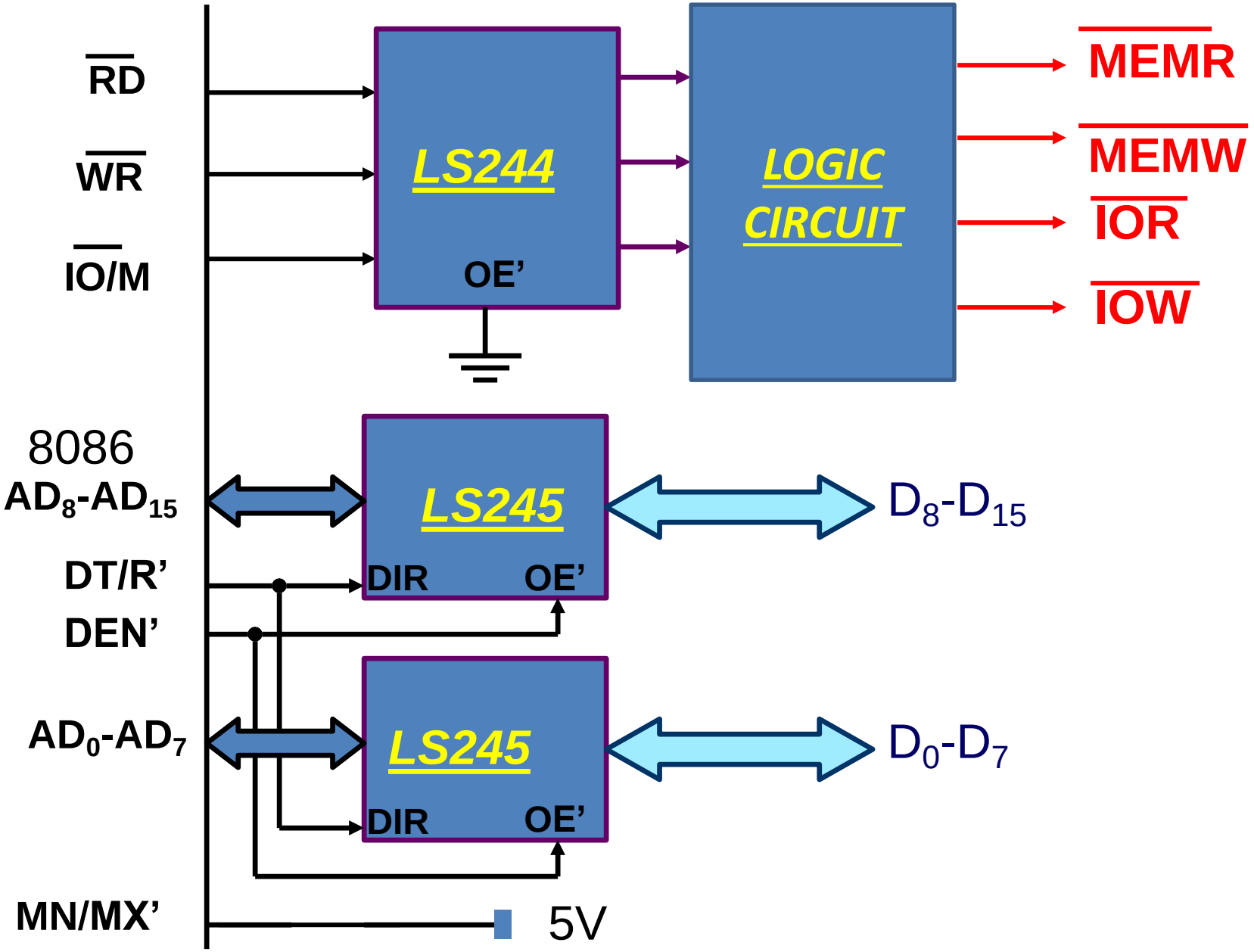
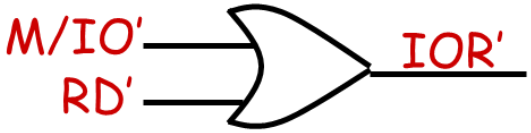

8086
Inputs
5 V
VCC
MN/MX
NC
TEST
HLDA
INTR
INTA
15 MHz
X 1
X 2
RDY1
RDY2
RESET
8284
CLK
5 V
F/C
READY
CYNC
AEN 1
AEN 2
RESET
CLK
READY
8086
NMI
HOLD
GND

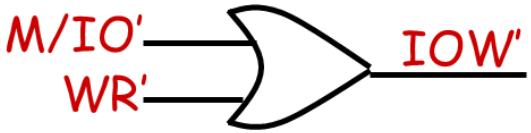
System Bus of 8086 (Address)



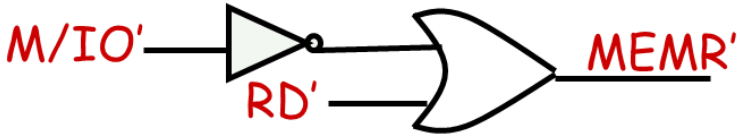
SYSTEM BUS OF 8086 (DATA + CONTROL)

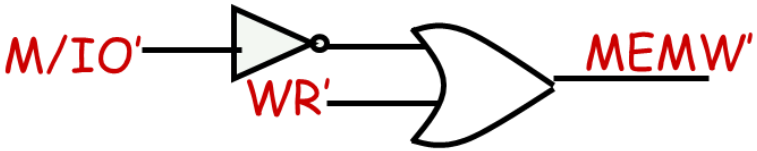






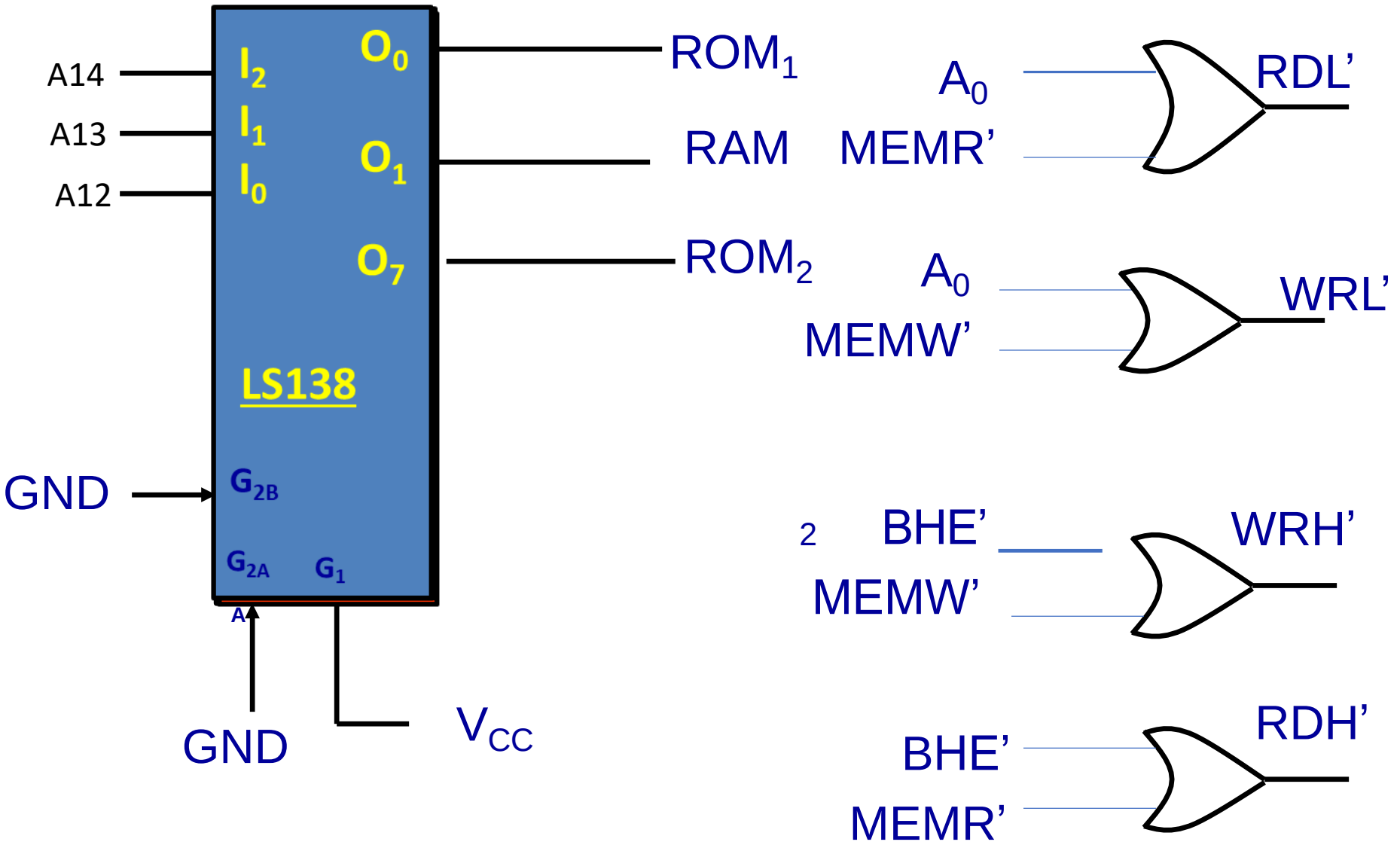
M/IO'	RD'	WR'	Bus cycle
1	0	1	MEMR'
1	1	0	MEMW'
0	0	1	IOR'
0	1	0	IOW'



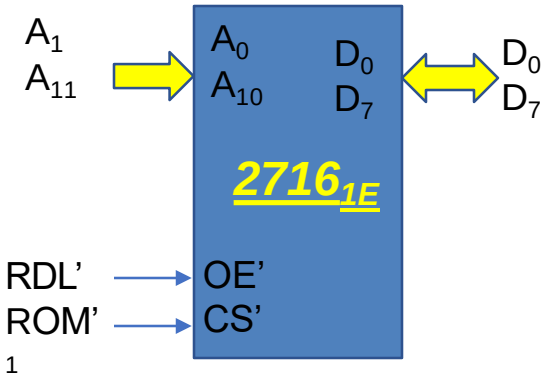


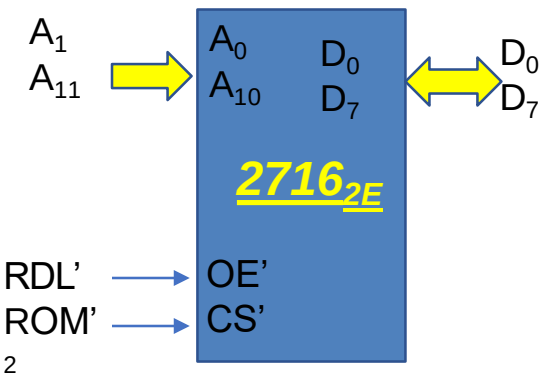
back

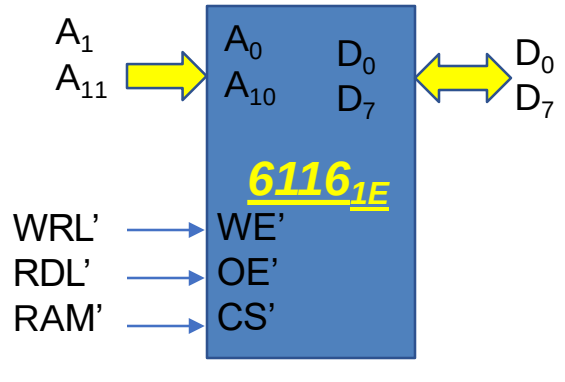
Memory Decoder

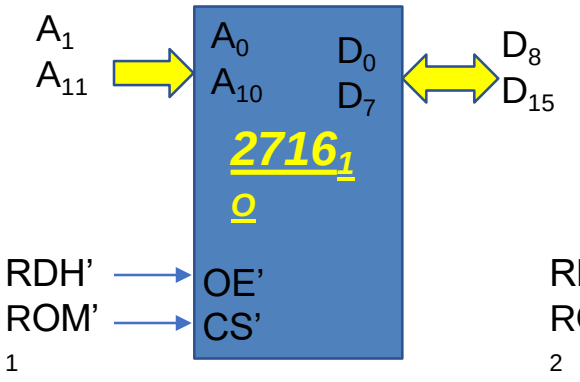


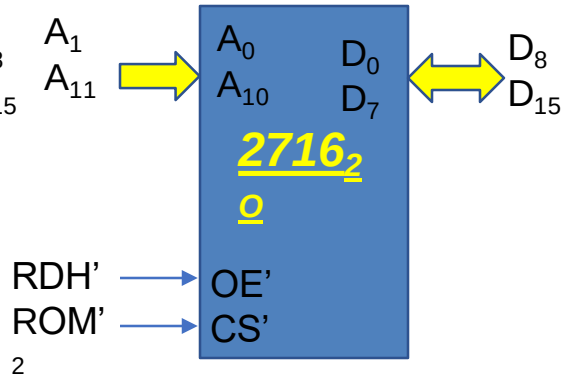
Memory Interfacing

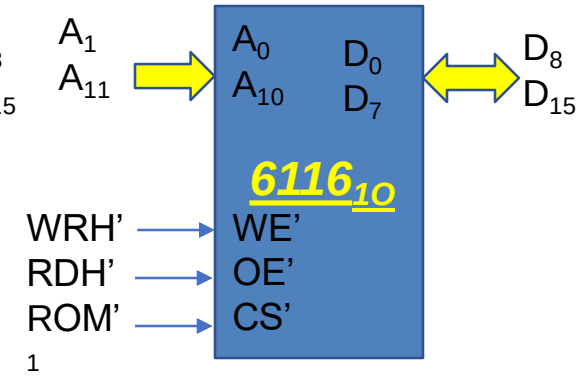




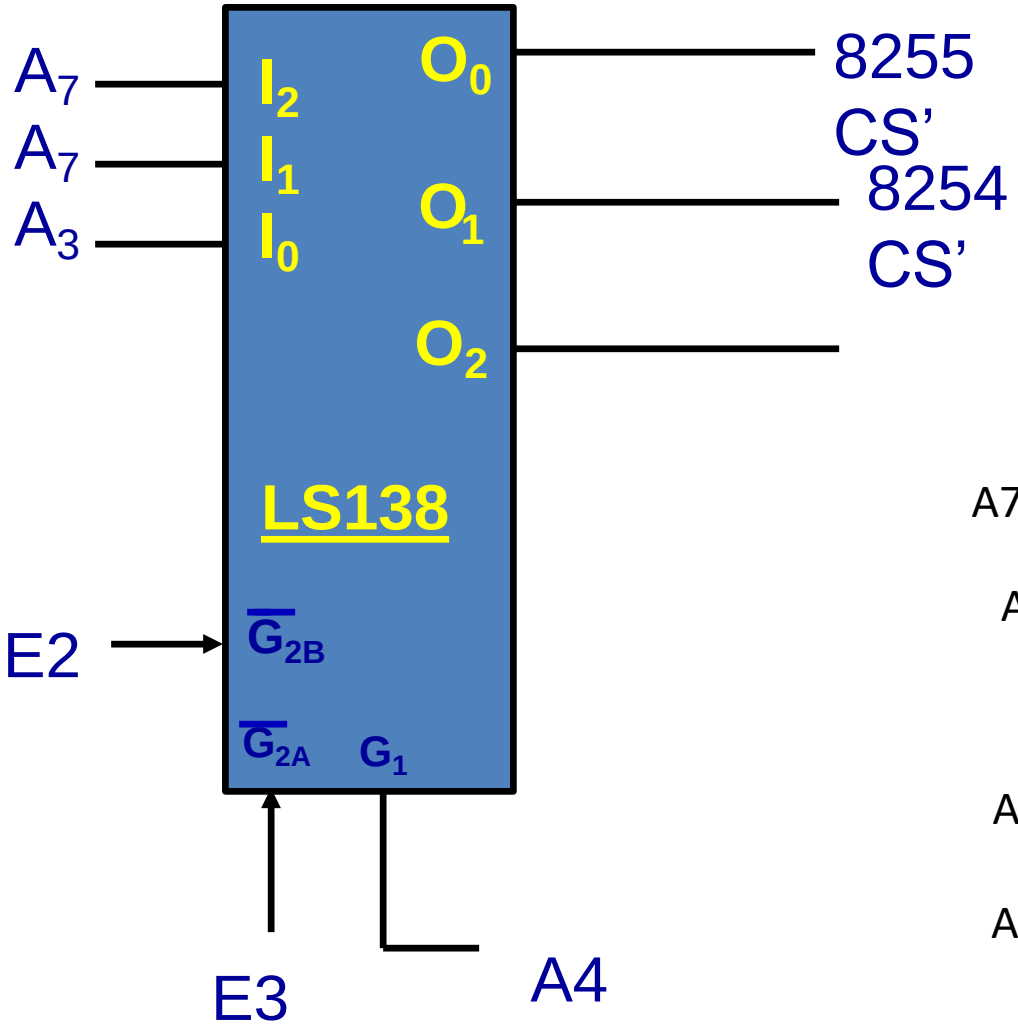


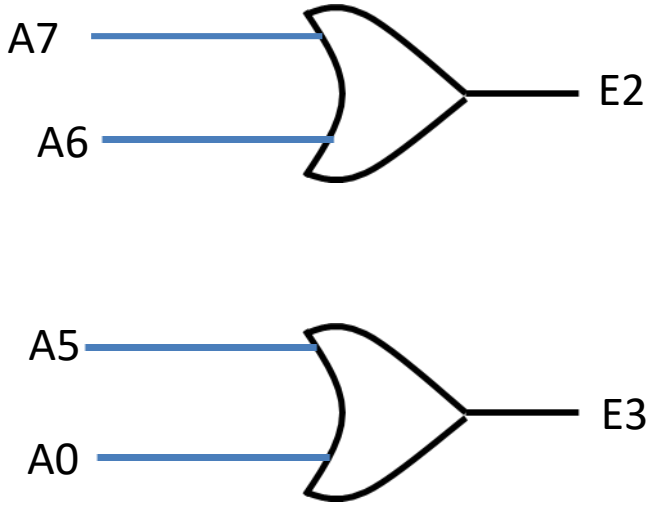




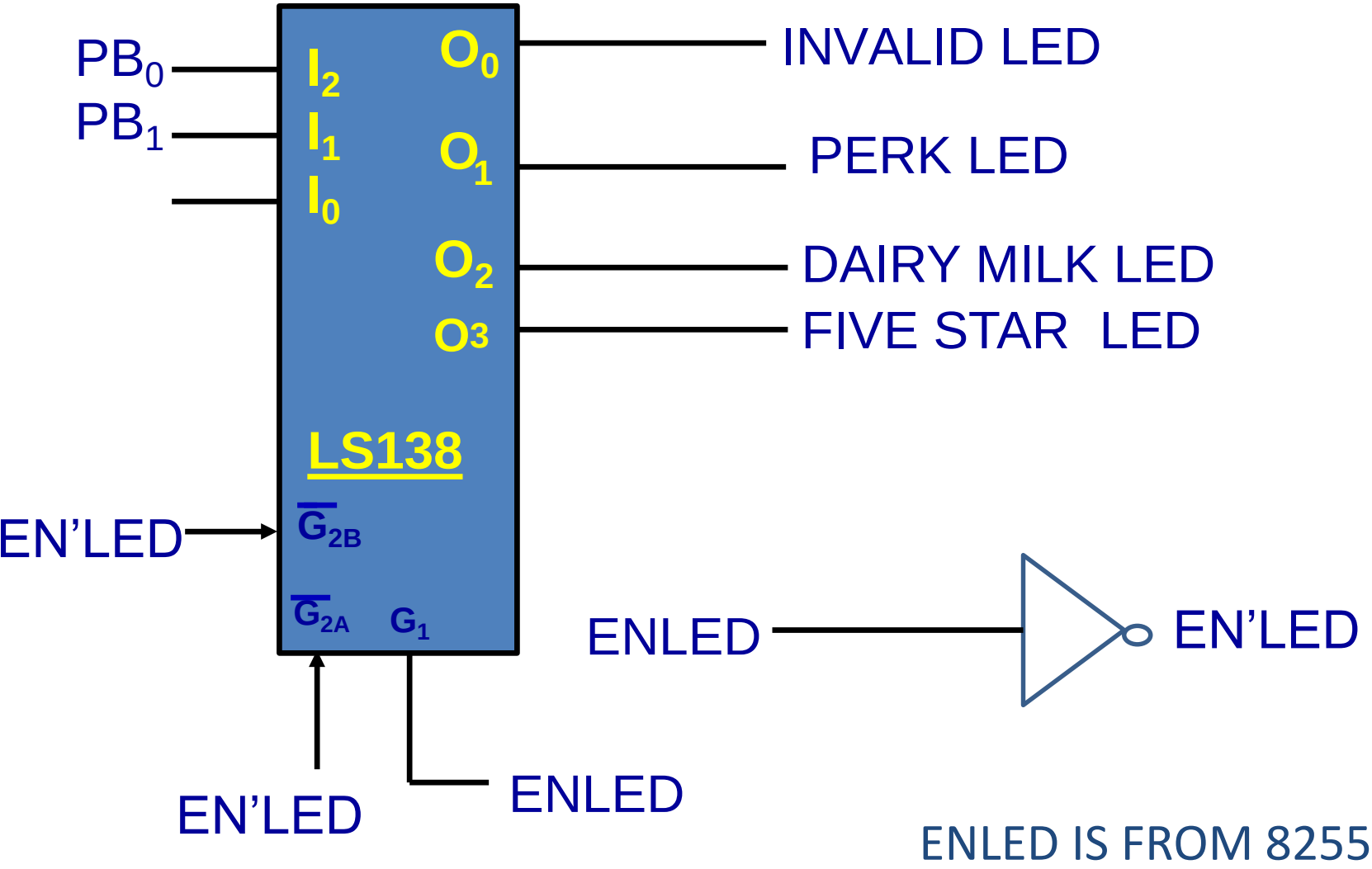


I/O Decoder


A7
A7
A3
I2
I1
I0
O0
O1
O2
8255
CS'
8254
CS'
LS138
E2
G2B
G2A
G1
E3
A4

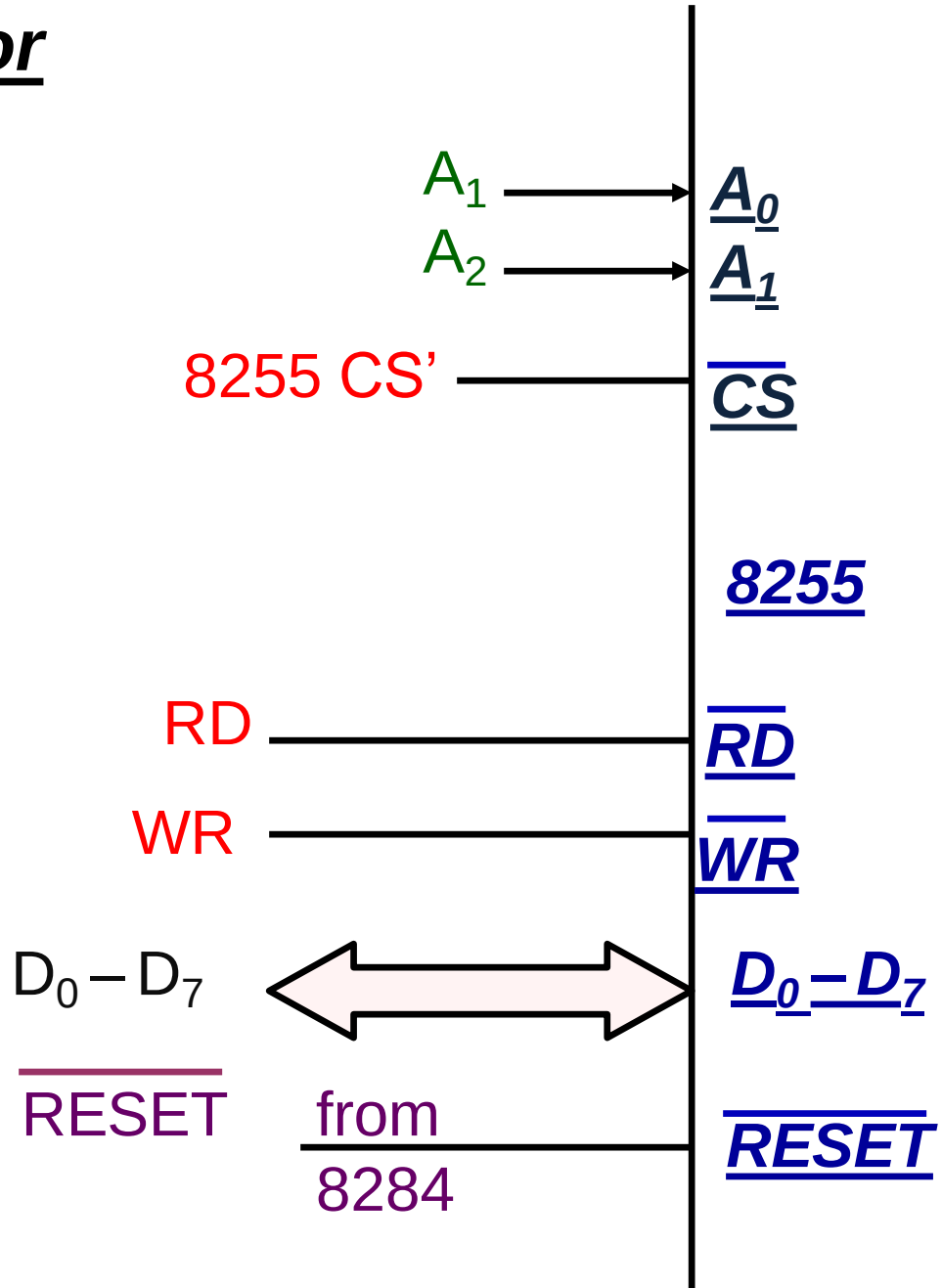

A7
A6
E2
A5
A0
E3

LED DECODER

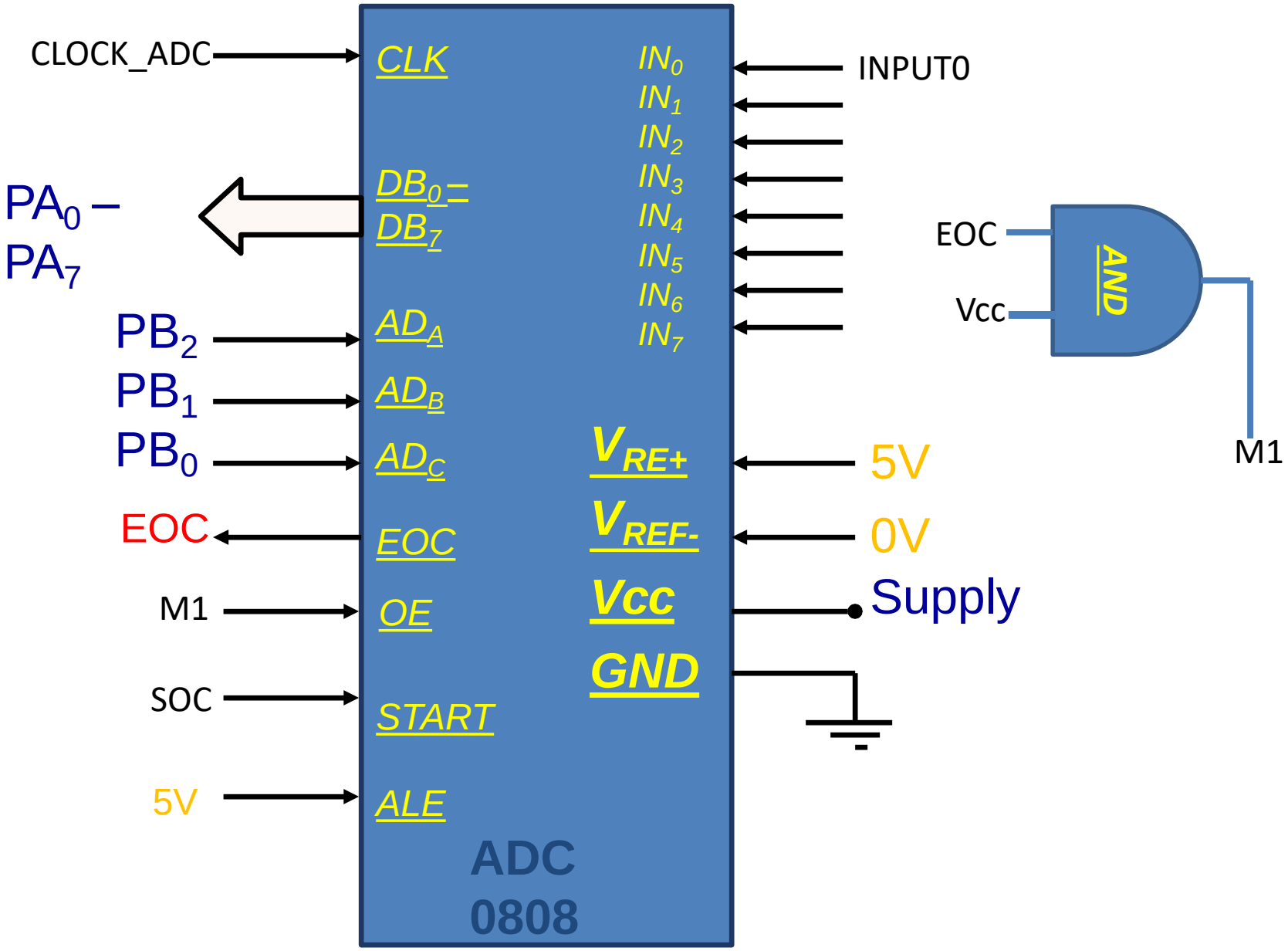


ENLED IS FROM 8255

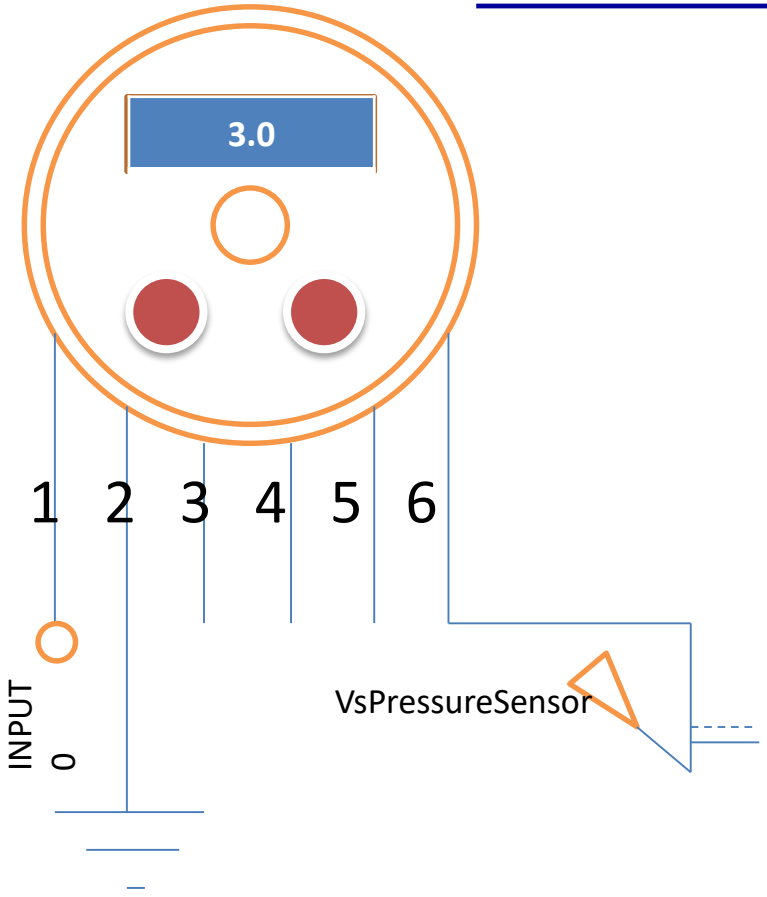
8255 Interface to the processor



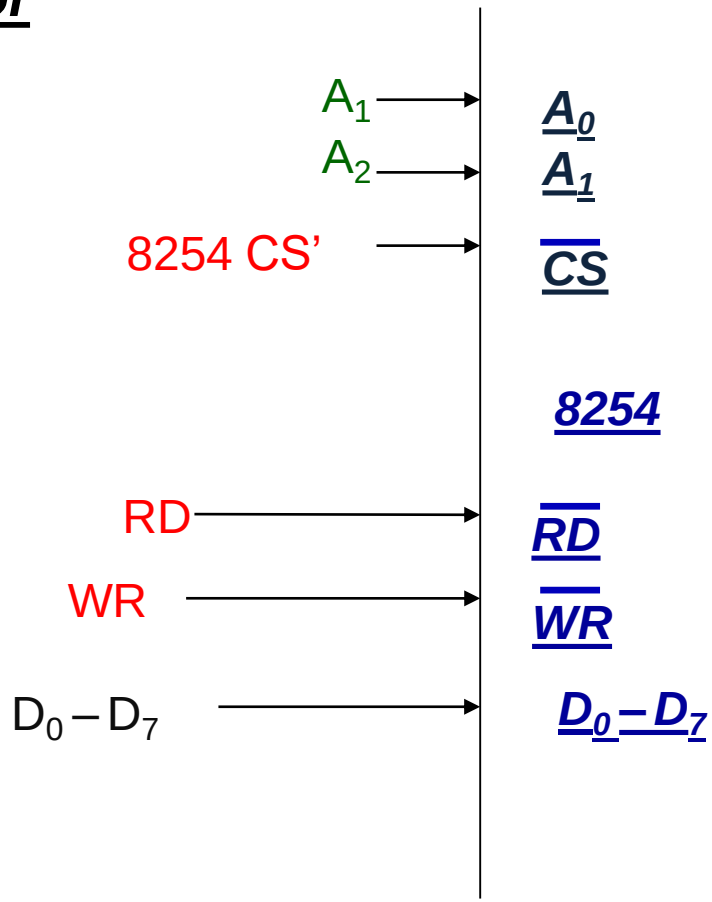
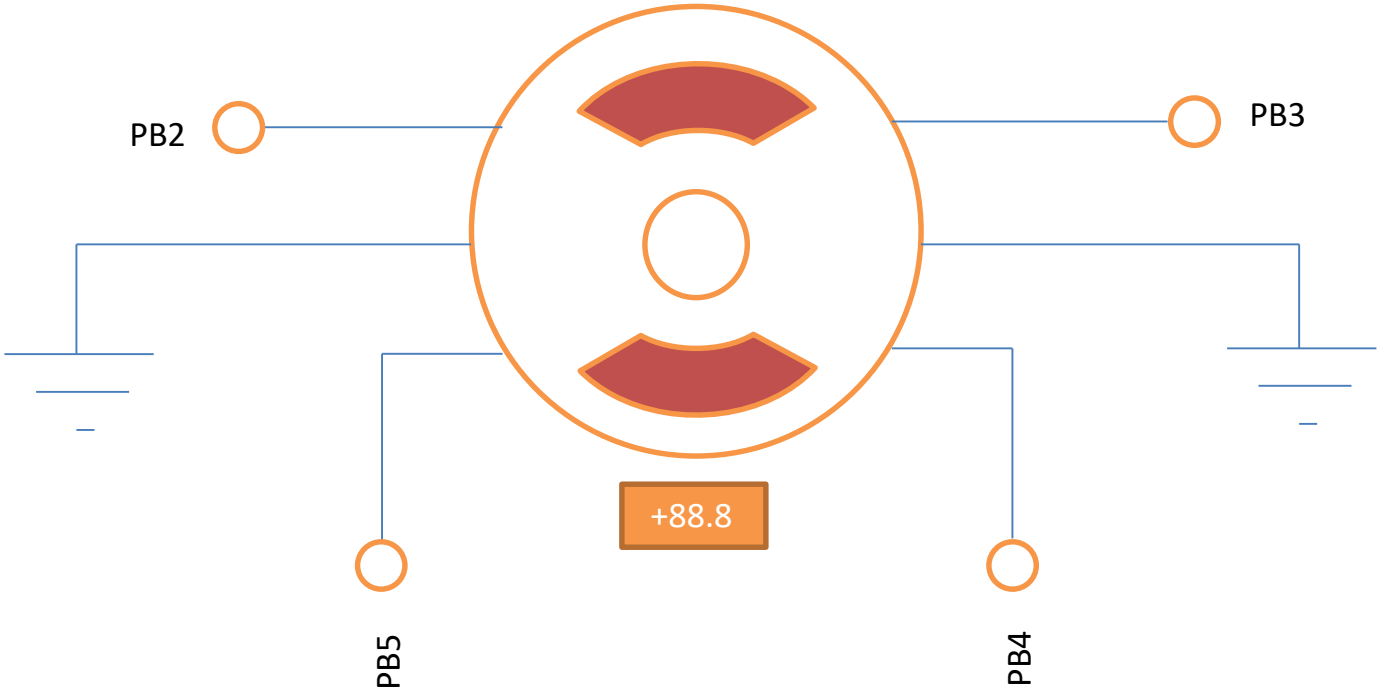
ADC 0808


CLOCK_ADC
CLK
INPUT0
IN0
IN1
IN2
IN3
IN4
IN5
IN6
IN7
PA0 –
PA7
DB0 –
DB7
EOC
Vcc
AND
M1
PB2
PB1
PB0
ADA
ADB
ADC
VRE+
5V
EOC
EOC
VREF-
0V
M1
OE
Vcc
Supply
SOC
START
GND
5V
ALE
ADC
0808

MPX 4250 – Pressure Sensor



8254 Interface to the processor



UNIPOLAR STEPPER MOTOR