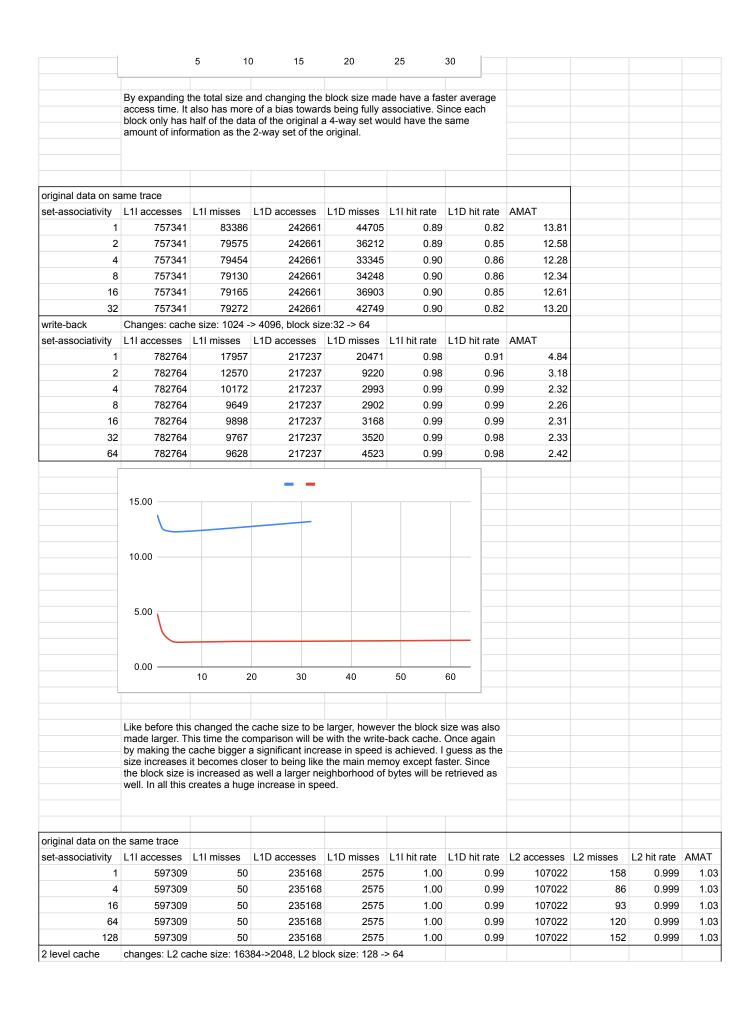
Cache simulator w	vrite-up									
write-through										
set-associativity	L1I accesses	L1I misses	L1D accesses	L1D misses	L1I hit rate	L1D hit rate	AMAT			
1	2137414	196600	695066	344475	0.91	0.50	20.10			
2	2137414	137369	695066	310377	0.94	0.55	16.81			
4	2137414	136670	695066	303300	0.94	0.56	16.53			
8	2137414	137530	695066	302639	0.94	0.56	16.54			
16	2137414	139179	695066	301566	0.93	0.57	16.56			
32	2137414	138097	695066	300909	0.93	0.57	16.50			
32	2137414	130091	093000	300909	0.34	0.57	10.50			
write-back										
set-associativity	L1I accesses	L1I misses	L1D accesses	L1D misses	L1I hit rate	L1D hit rate	AMAT			
1	2137414	196600	695066	97727	0.91	0.86	11.39			
2	2137414	137369	695066	60779	0.94	0.91	8.00			
4	2137414	136670	695066	55941	0.94	0.92	7.80			
8	2137414	137530	695066	59822	0.94	0.91	7.97			
16	2137414	139179	695066	62382	0.93	0.91	8.12			
32	2137414	138097	695066	69906	0.94	0.90	8.34			
2-level write-back set-associativity	L1I accesses	L1I misses	L1D accesses	L1D misses	L1I hit rate	L1D hit rate	L2 accesses	L2 misses	L2 hit rate	AMAT
set-associativity	2137414	137369	695066	60779	0.94	0.91	447746	43485	0.90	2.38
4	2137414	137369	695066	60779	0.94	0.91	447746	28714	0.94	2.15
16	2137414	137369	695066	60779	0.94	0.91	447746	29621	0.93	
64	2137414	137369	695066	60779	0.94	0.91	447746	36340	0.92	2.27
128	2137414	137369	695066	60779	0.94	0.91	447746	41385	0.91	2.35
original data on sa	ame trace									
set-associativity	L1I accesses	L1I misses	L1D accesses	L1D misses	L1I hit rate	L1D hit rate	AMAT			
1	782764	60940	217237	97669	0.92	0.55	16.86			
2	782764	57744	217237	87399	0.93	0.60	15.51			
4	782764	57156	217237	85559	0.93	0.61	15.27			
8	782764	58371	217237	86291	0.93	0.60	15.47			
16	782764	59985	217237	86008	0.92	0.60	15.60			
32	782764		217237	85343	0.92	0.61	15.41			
write-through			> 2048, block siz		0.92	0.01	15.41			
			· ·		I 41 hit rata	L 1D bit rata	A B 4 A T			
set-associativity										
1	782764			89420	0.93					
2	782764	48262		75165	0.94					
4	782764			72305	0.95	0.67				
8	782764	36546		72376	0.95	0.67	11.89			
16	782764	36286	217237	72626	0.95	0.67	11.89			
	20.00									
	15.00									
										
	10.00									
	5.00 —									



	L II accesses	L1I misses	L1D accesses	L1D misses	L1I hit rate	L1D hit	rate	L2 accesses	L2 misses	L2 hit rate	AMAT
1	75734	79575	242661	36212	0.89		0.85	195581	78886	0.60	6.83
4	75734	79575	242661	36212	0.89		0.85	195581	84100	0.57	7.14
16	75734	79575	242661	36212	0.89		0.85	195581	94450	0.52	7.75
64	75734	79575	242661	36212	0.89		0.85	195581	84433	0.57	7.16
	8.00										
	6.00										
	4.00										
	4.00										
	2.00										
	0.00 ———	25	50	75	100	125					
			caches result in	quicker acces		ever I thr					
	something that	it is not being t	aken into accoun aking into accou	quicker acces t here is that la	s times. Howe	ever I thr ay take	nk				
	something that	at is not being ta	aken into accoun aking into accou	quicker acces t here is that la	s times. Howe	ever I thr ay take	nk				