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Arjun Passi

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Permanent Address
6032A, Igloe Dr
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OBJECTIVE Full-time position as a Computer Engineer

EDUCATION

B.S., Computer Engineering (Honors Program); Minor: Computer Science; May, 2015
Virginia Polytechnic Institute and State University, Blacksburg, VA

- **GPA In Major**: 3.97/4.0 **GPA Overall**: 3.93/4.0
- Dean's List with Distinction: *7 semesters*

SKILLS

- Programming Languages : C, C++, Java, MATLAB, Assembly
- Designing Tools : LabView, PSpice, Logic Works, Modelsim
- Working Platforms : Windows, Linux

CLASSES

- Micro Controller Interfacing
- Software Design and Data Structures
- Embedded System Design
- Digital Image Processing
- Large Scale Software Development
- Network Application Design
- Signals and Systems
- Digital Design

EXPERIENCE

Information Technology Leadership Program Intern, *GE Aviation*, June 2014 – Aug 2014

• Lead the design of marketing mobile reports that provide relevant data to business leaders and satisfy business reporting requirements.

• Developed mobile reports in Cognos BI, Roambi, and TM1 to maximize utility.

Mobile Hardware Engineering Co-op, *Harris Corporation***,** May 2013 – Aug 2013

• Performed TIA-603 testing on portable and mobile radios.

• Developed an android application that mimics the functionality of the Bluetooth microphone that connects with XG-25P, XG-25M, and Unity Radios.

PROJECTS

Autonomous Rover*, Embedded System Design*, Spring 2014

• Implemented a navigation algorithm for the rover to traverse a room containing obstacles.

• Designed and implemented a webserver that controlled the rover and mapped the path traversed by the rover.

• Implemented a message passing workflow between multiple tasks in free RTOS on the ARM board to process all the data wirelessly from the rover.

Geographic Information System, *Software Design and Data Structures*, Spring 2013

• Designed a system to organize information pertaining to geographic features.

• Implemented various data structures to provide different kinds of access to the information.

The Cerebot Oscilloscope*, Micro Controller Interfacing*, Fall 2012

• Created an oscilloscope using Analog to Digital converter and timer peripherals.

• Processed interrupts generated by the ADC and the timer module.

• Designed and implemented an algorithm to process and display the graph of the input voltage applied by the user.

Arithmetic Logic Unit, Digital Design, Fall 2013

• Designed and implemented an arithmetic logic unit in Verilog to be deployed on DE0 nano.

• ALU performed functions like subtraction, addition, multiplication, decrement, and increment.

ACTIVITIES

- Virginia Tech Linux and Unix Users Group, Fall 2012 – Present
- Virginia Tech Cyber Security Club, Fall 2012 – Present