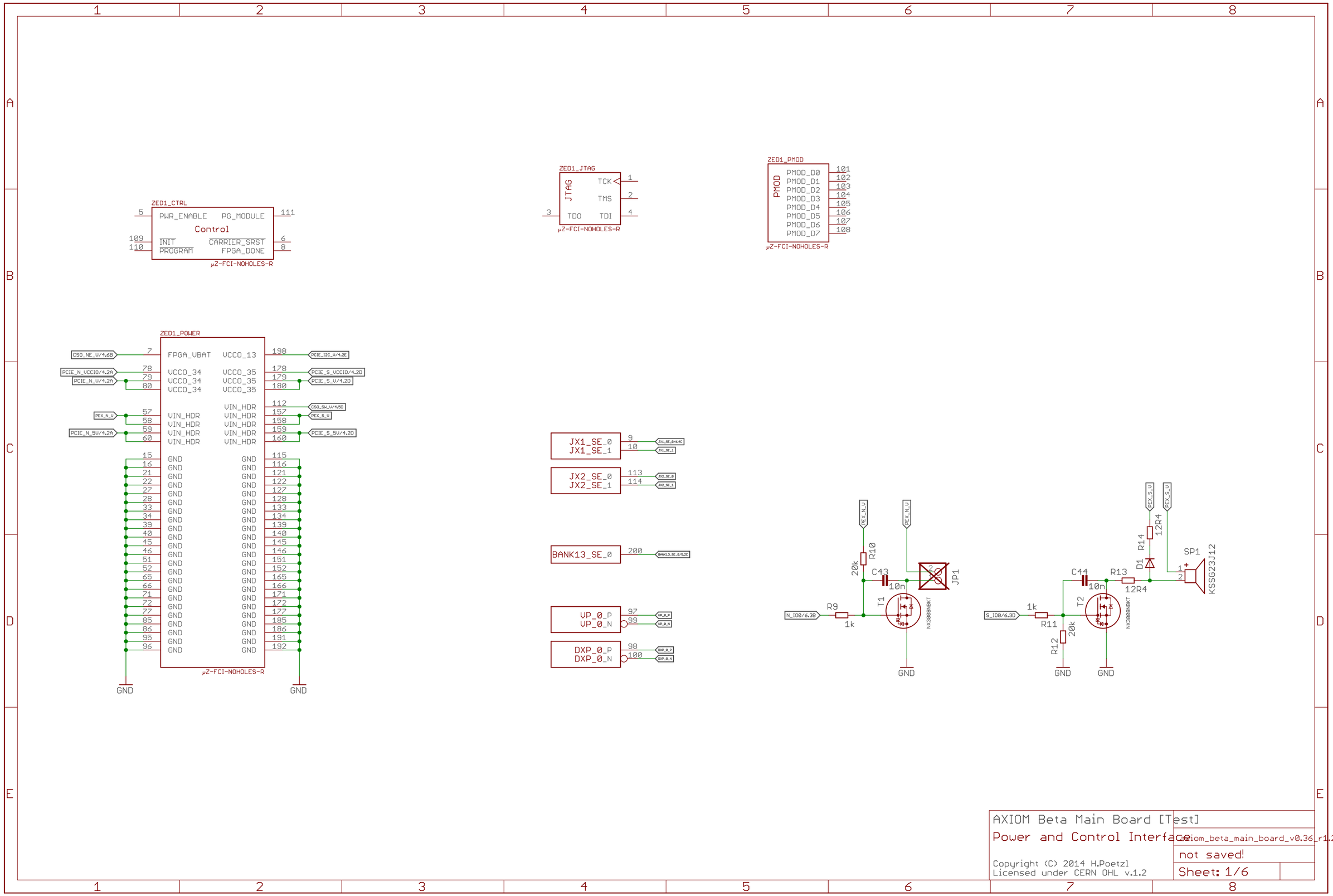

AXIOM Beta Main Board [Test]

Power and Control Interface

axiom_beta_main_board_v0.36_r1.2_offset

not saved!

Sheet: 1/6

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AXIOM Beta Main Board [Test]

ZYNQ LUDS Interface

axiom_beta_main_board_v0.36_r1.2_offset

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Sheet: 2/6

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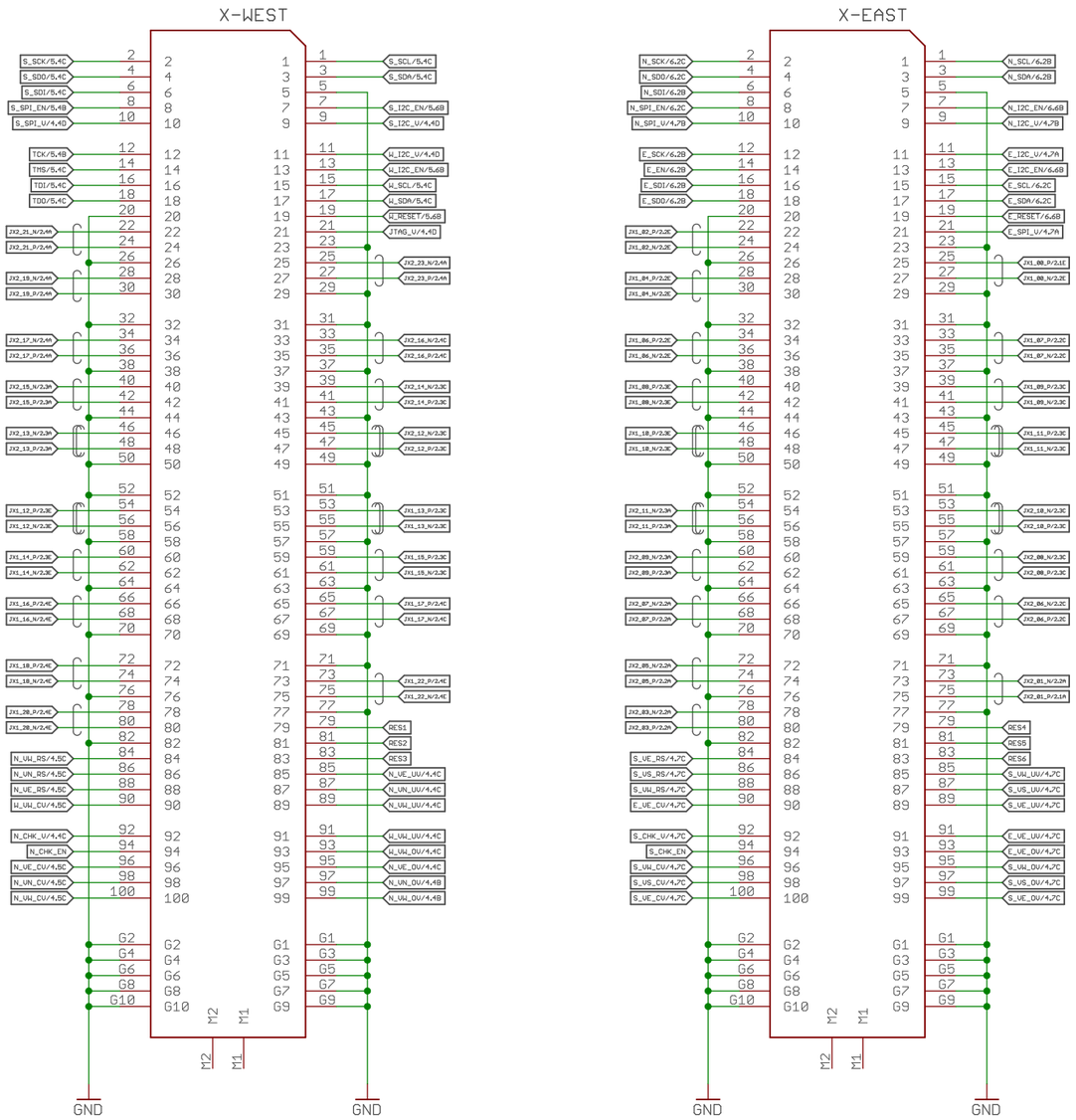

AXIOM Beta Main Board [Test]

Interface Board Connection

axiom_beta_main_board_v0.36_r1.2_offset

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Sheet: 3/6

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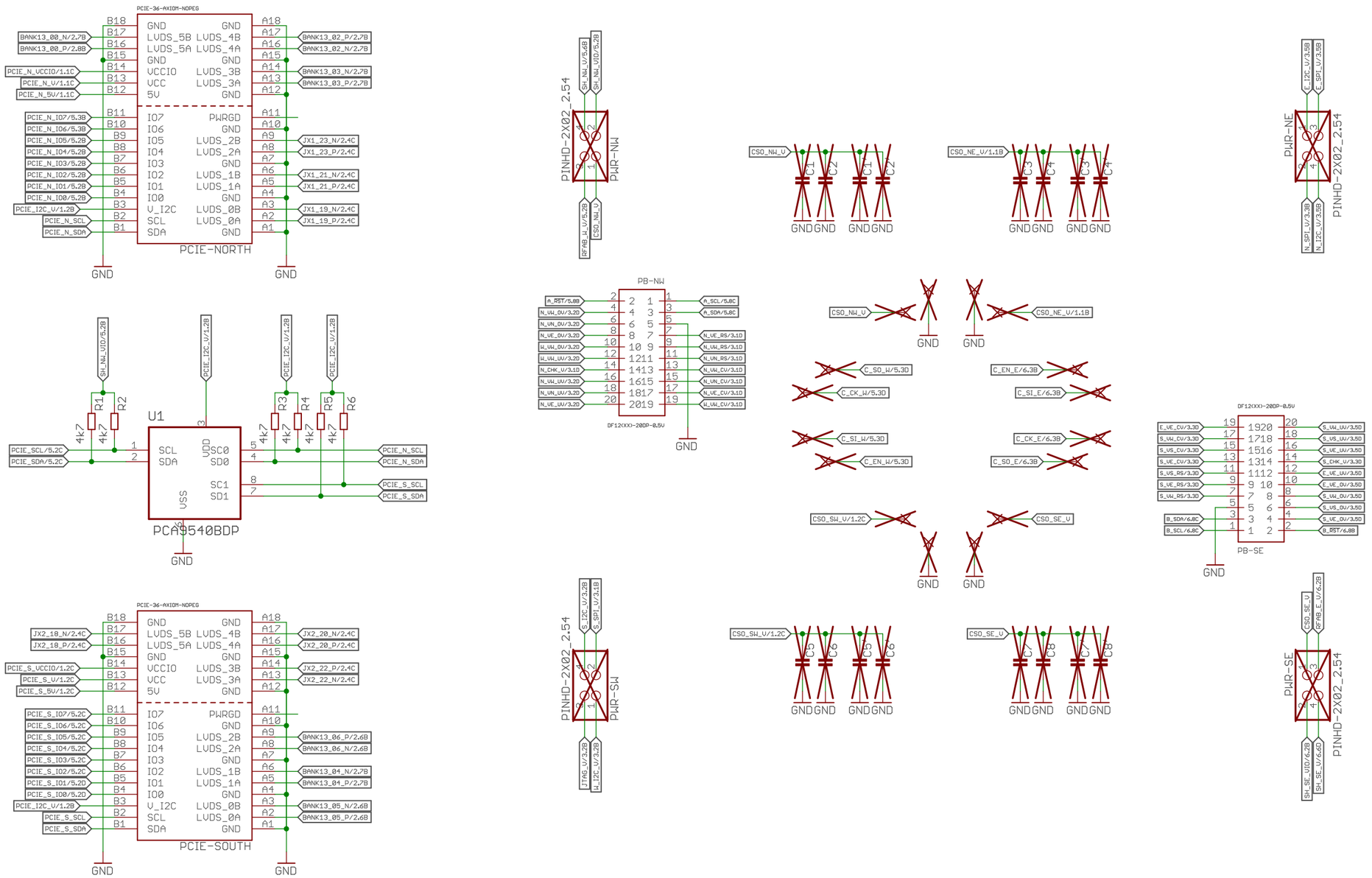

AXIOM Beta Main Board [Test]

PCIE, Power, Center Area

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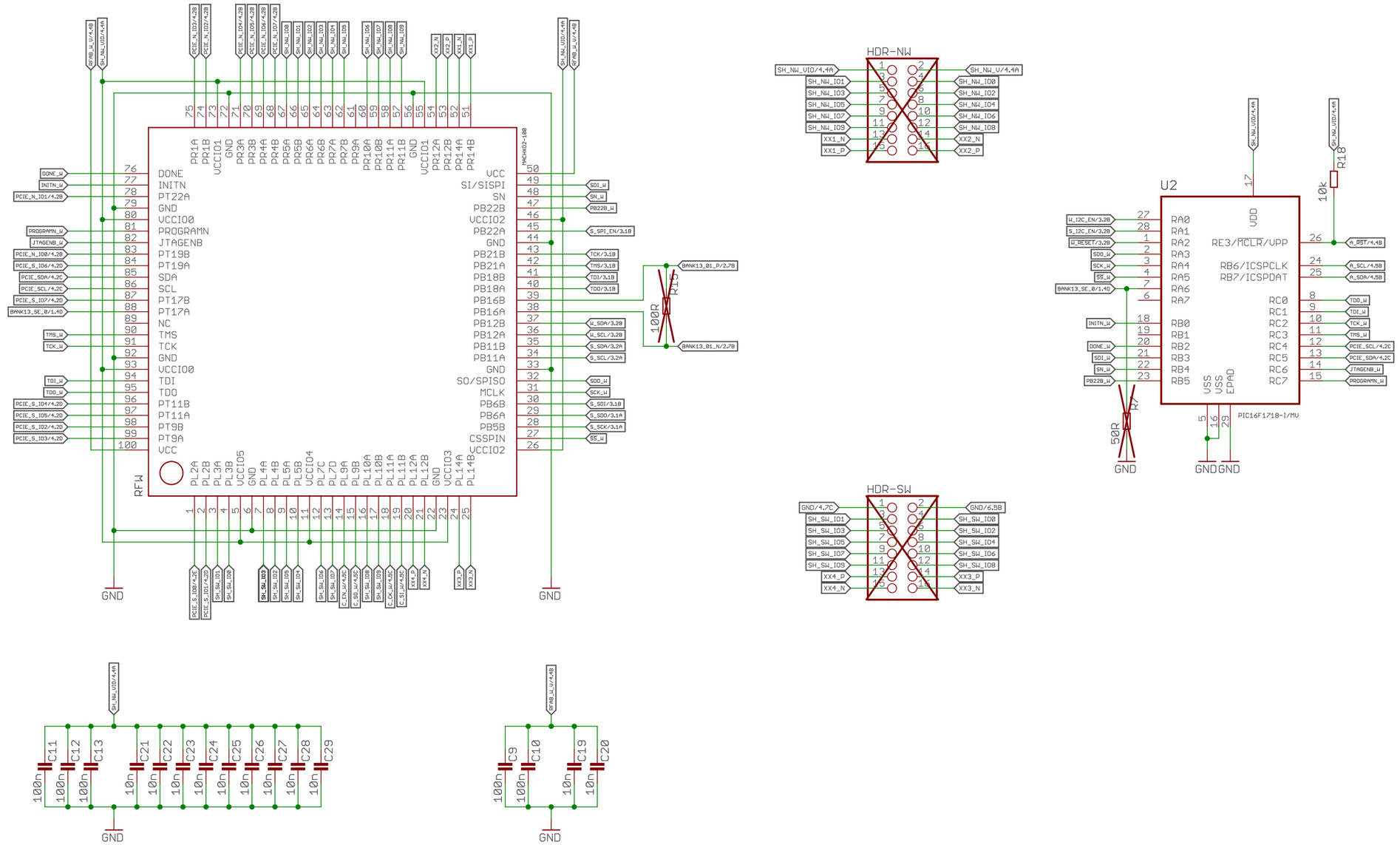

AXIOM Beta Main Board [Test]

Interface Fabric West

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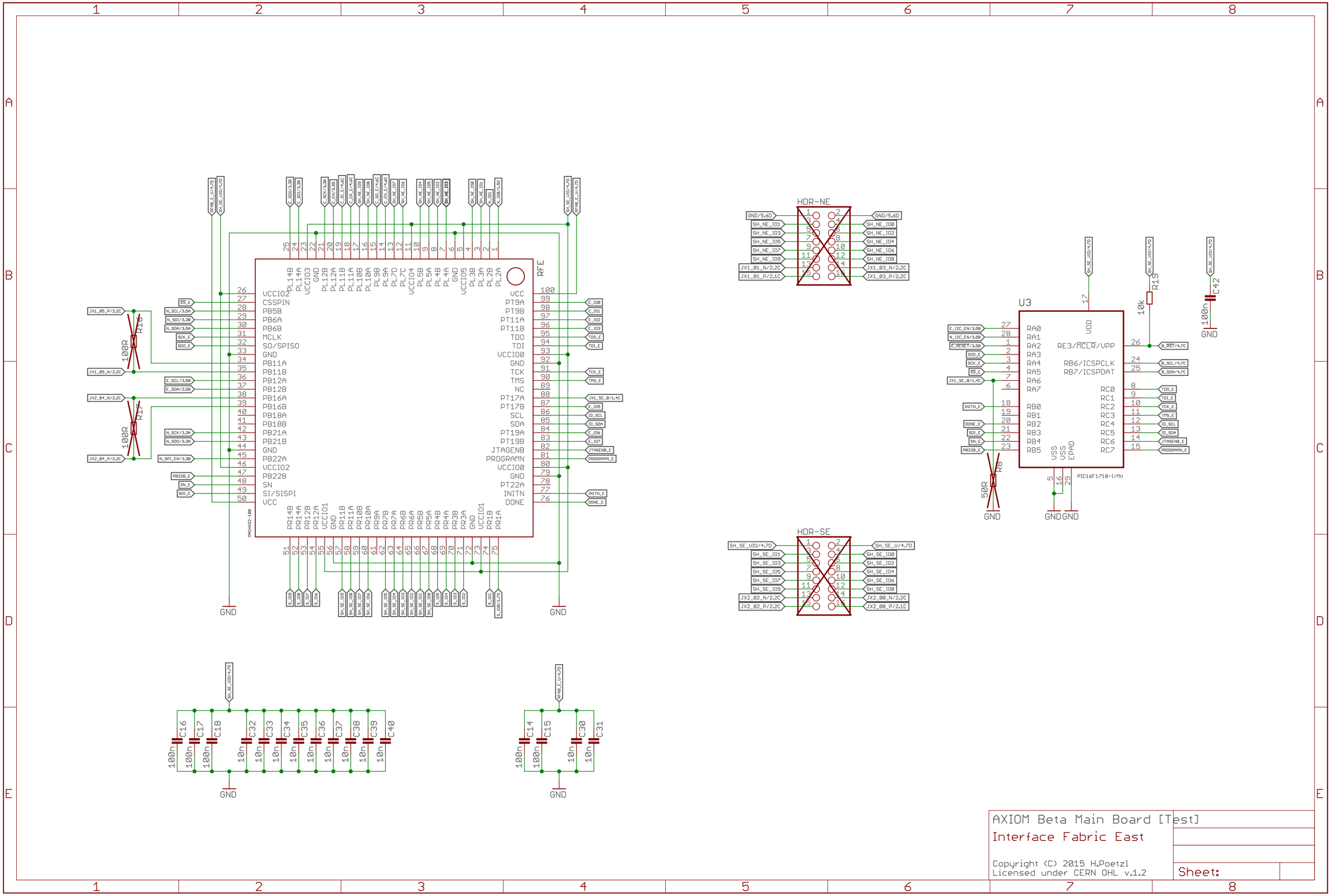

AXIOM Beta Main Board [Test]

Interface Fabric East

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Sheet: