

AXIOM Beta Power Board

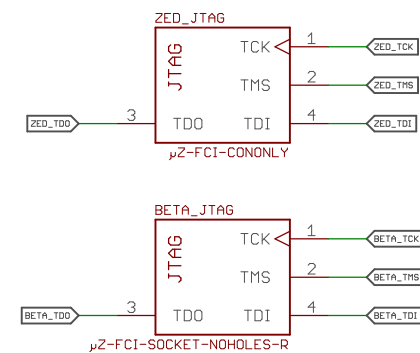
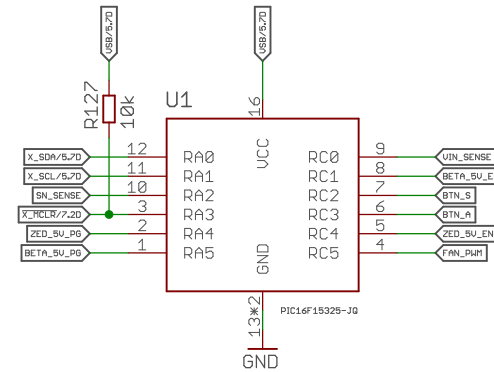
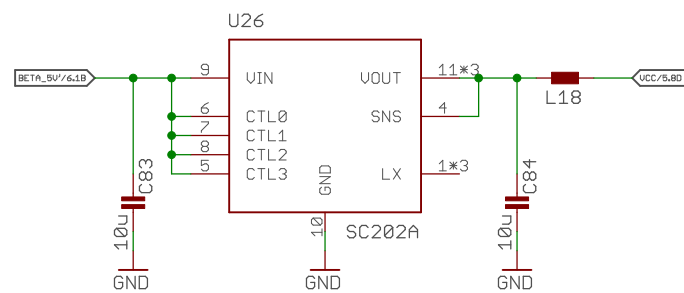
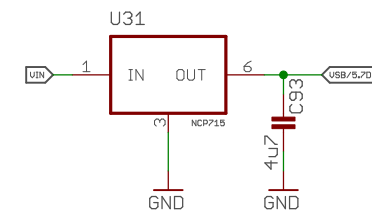
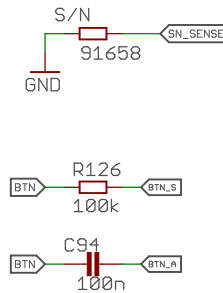
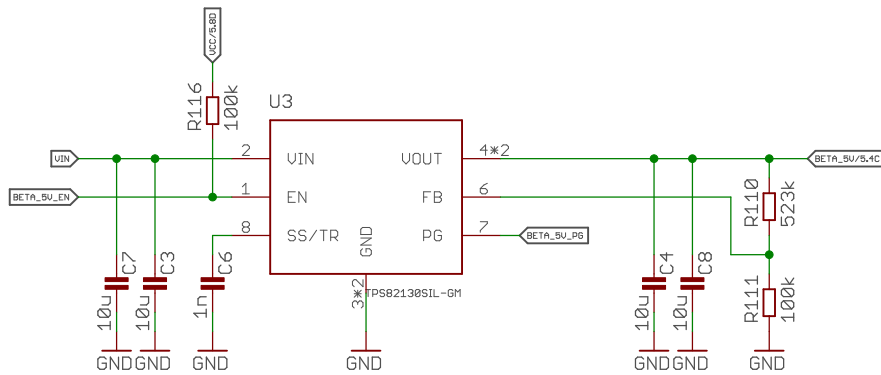
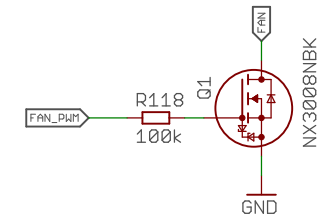
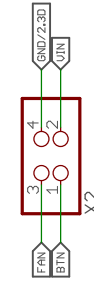
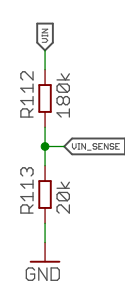
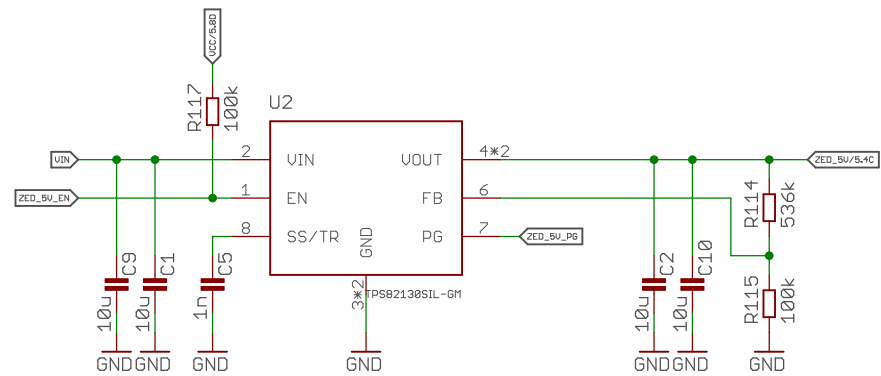
Beta Main Board Power

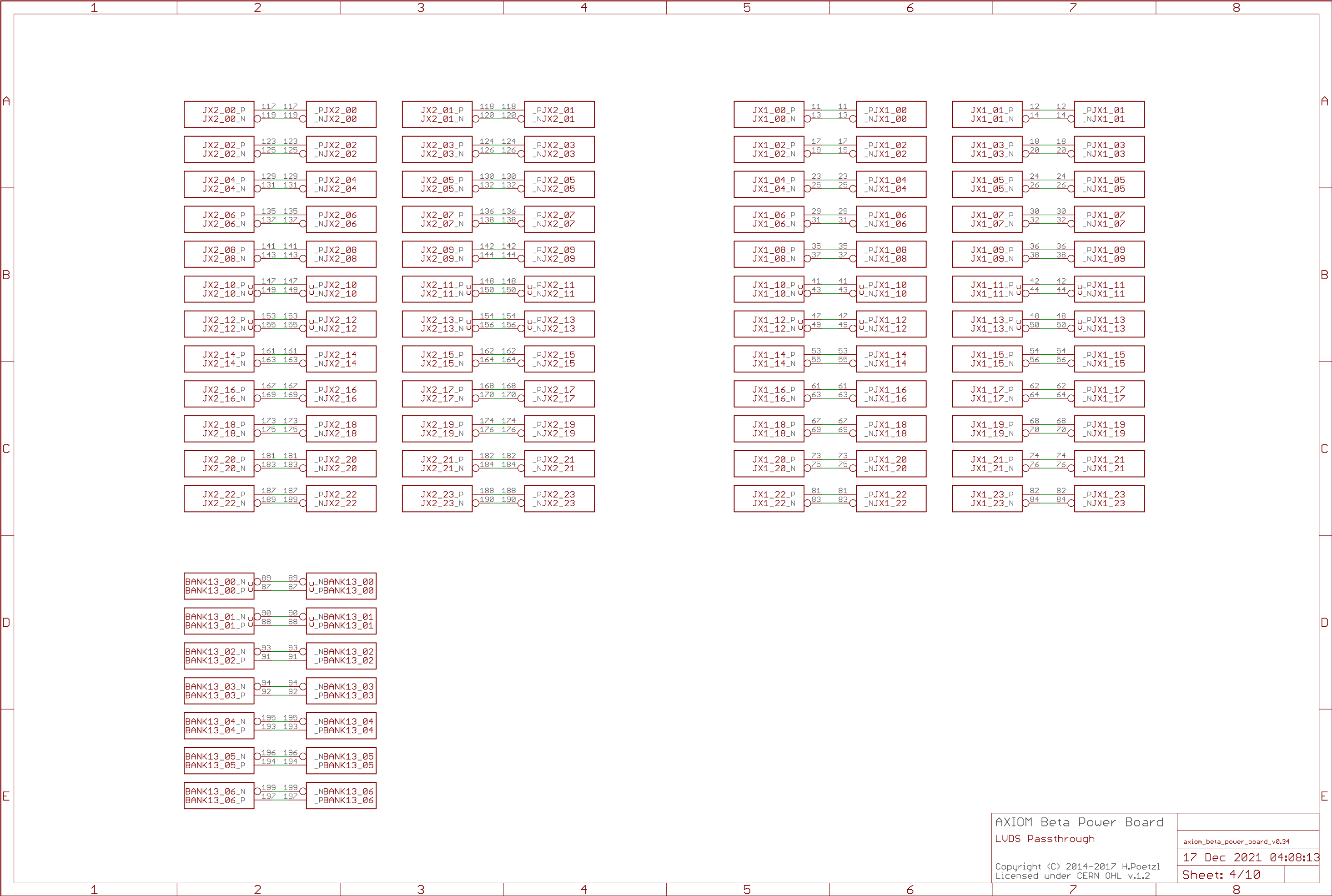
Copyright (C) 2014-2017 H.Poetzl
Licensed under CERN OHL v1.2

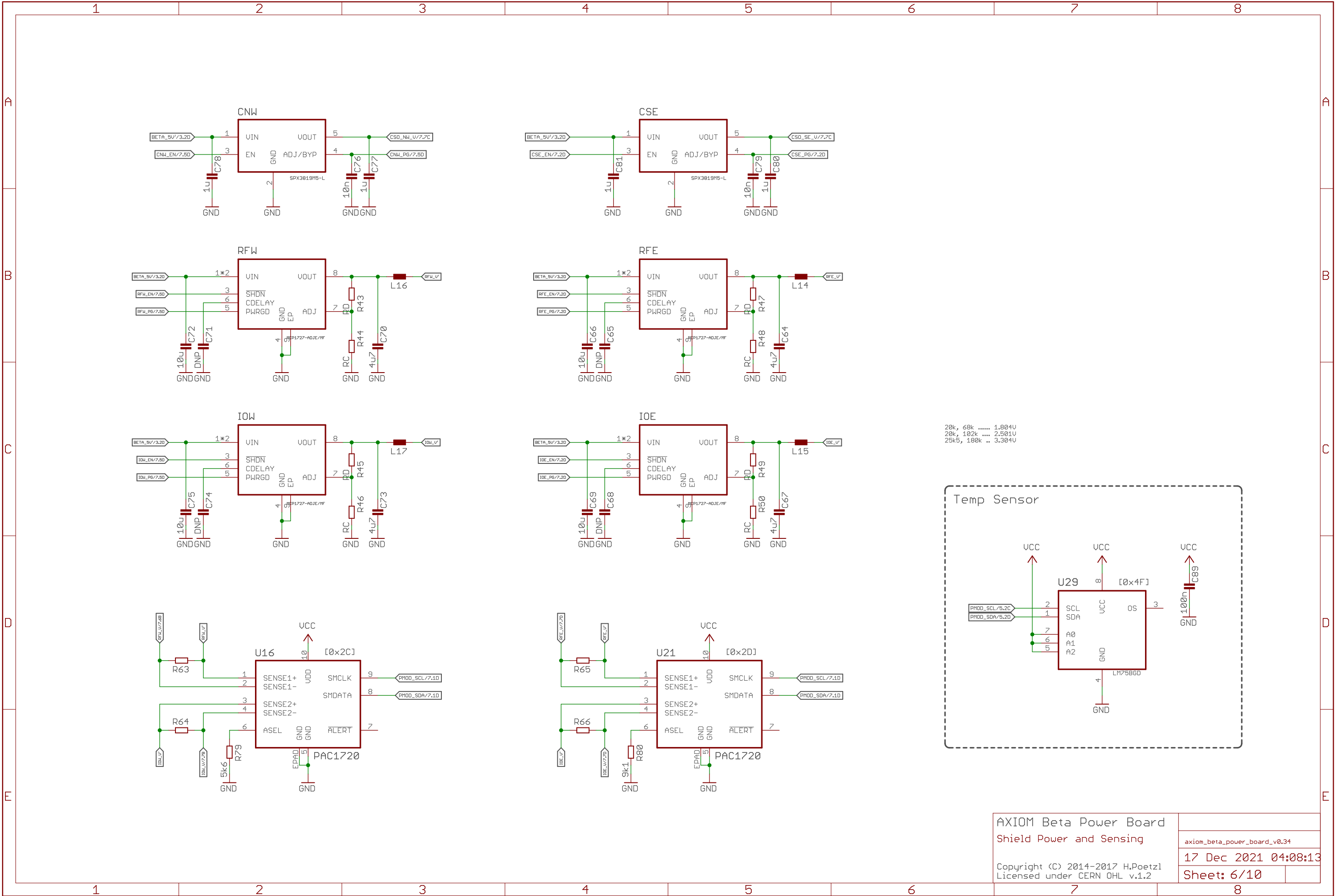
axiom_beta_power_board_v0.34

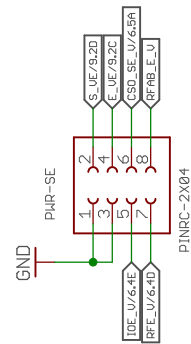
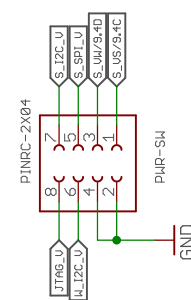
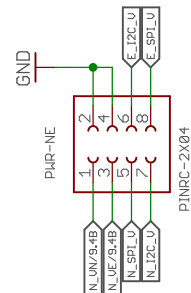
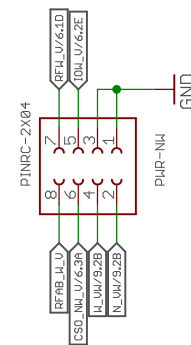
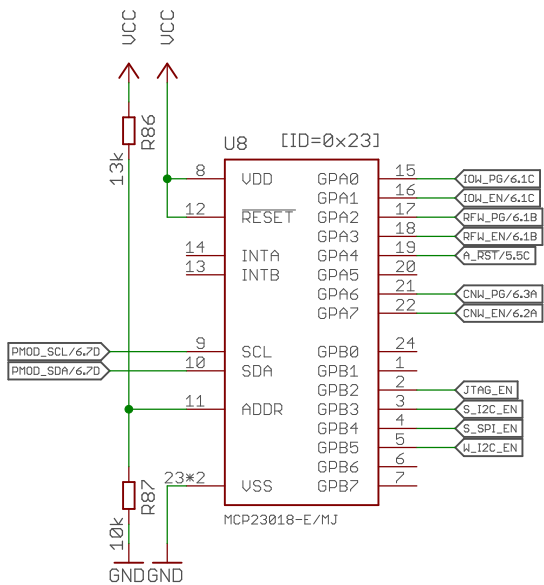
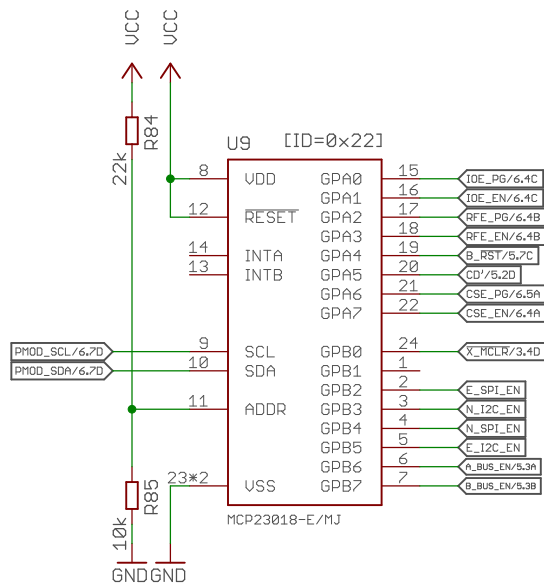
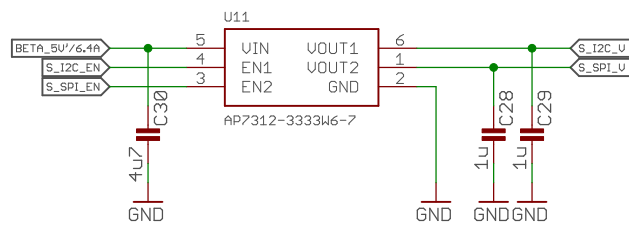
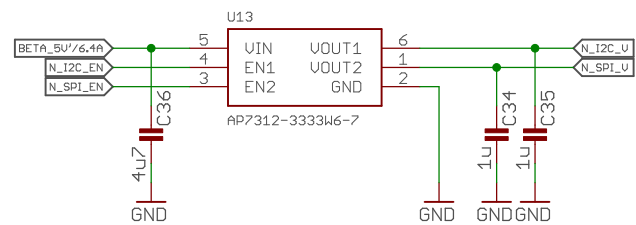
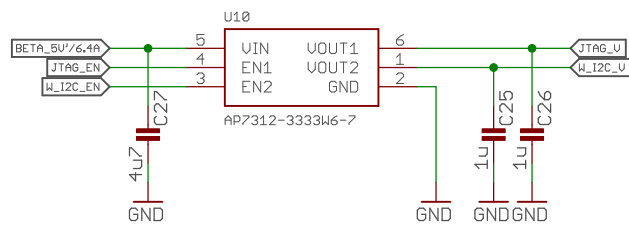
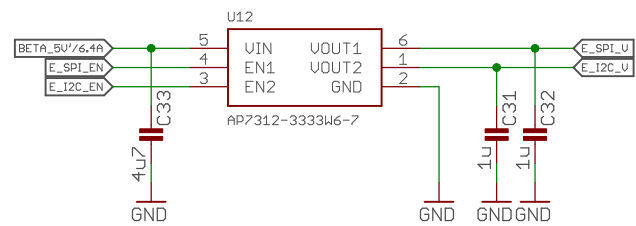
17 Dec 2021 04:08:13

Sheet: 2/10









AXIOM Beta Power Board
Interface Power and Control

Copyright (C) 2014-2017 H.Poetzl
Licensed under CERN OHL v.1.2

axiom_beta_power_board_v0.34

17 Dec 2021 04:08:13

Sheet: 7/10

