

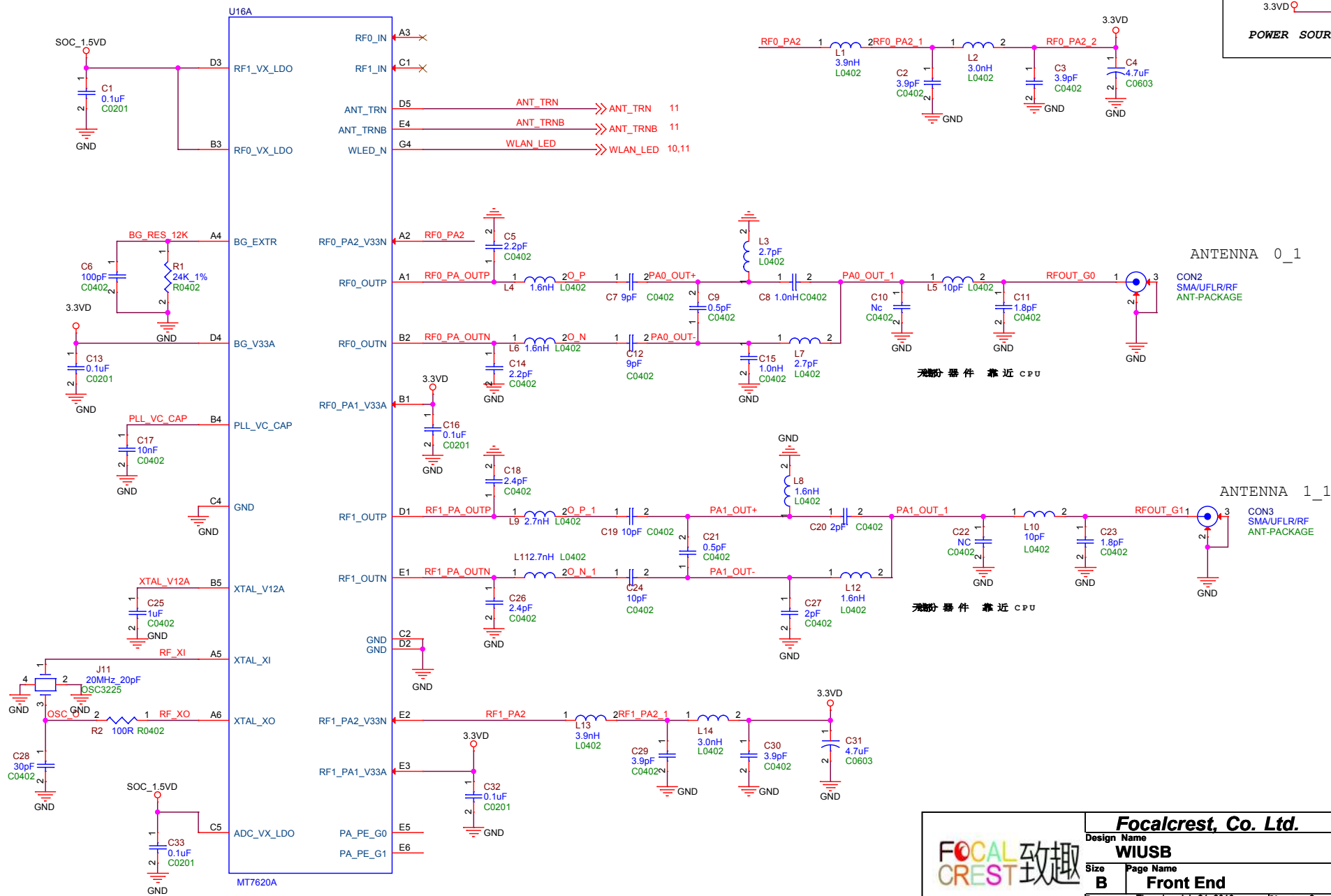


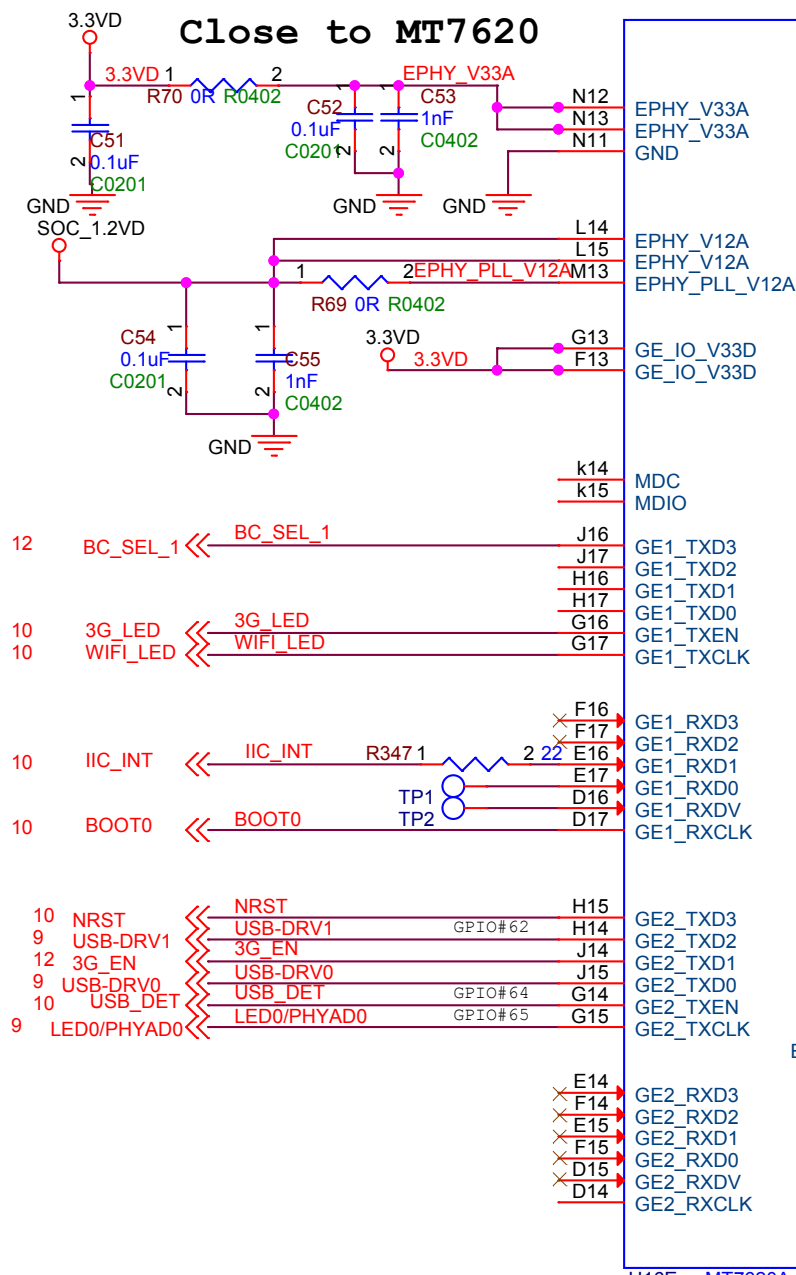
- V1.4 History:
- 1、修改了WIFI部分电容与磁珠的值
 - 2、增加了5V的只读网络
 - 3、GL852G采用Power self
 - 4、两个座子的固定脚接地了
 - 5、新了RGB驱动芯片为A00
 - 6、更新了DCV升压部分的电感及二极管装
 - 7、USB供电电路增加限流开关电路

- V1.5 History:
- 1、充电芯片改为TP5000
 - 2、单片机改为STM32F030
 - 3、去掉专用加密芯片
 - 4、去掉TF卡座
 - 5、增加关机USB切换功能
 - 6、更改wan连接器为连接
 - 7、去掉USB接口快充切换功能

- V1.51 History:
- 1、去掉MCU Crystal 激励

SOC_1.5VD  SOC_1.5VD
3.3VD  3.3VD
POWER SOURCE





MDI_TN_P4
MDI_TP_P4
MDI_RN_P4
MDI_RP_P4
MDI_TN_P3
MDI_TP_P3
MDI_RN_P3
MDI_RP_P3
MDI_TN_P2
MDI_TP_P2
MDI_RN_P2
MDI_RP_P2
MDI_TN_P1
MDI_TP_P1
MDI_RN_P1
MDI_RP_P1
MDI_TN_P0
MDI_TP_P0
MDI_RN_P0
MDI_RP_P0
EPHY_LED0_N_JTDO
EPHY_LED1_N_JTDI
EPHY_LED2_N_JTMS
EPHY_LED3_N_JTCLK
EPHY_LED4_N_JTRST_N
EPHY_RES_VBG

M17 TD- 9
M16 TD+ 9
N17 RD+ 9
N16 RD- 9

N14
N15
M14
M15

P15
P14
P17
P16

T17
T16
R17
R16

U17
U16
T15
U15

H4
H3
H2
H1
J4
LED4/PHYAD4 >>> LED4/PHYAD4 9

Close to MT7620

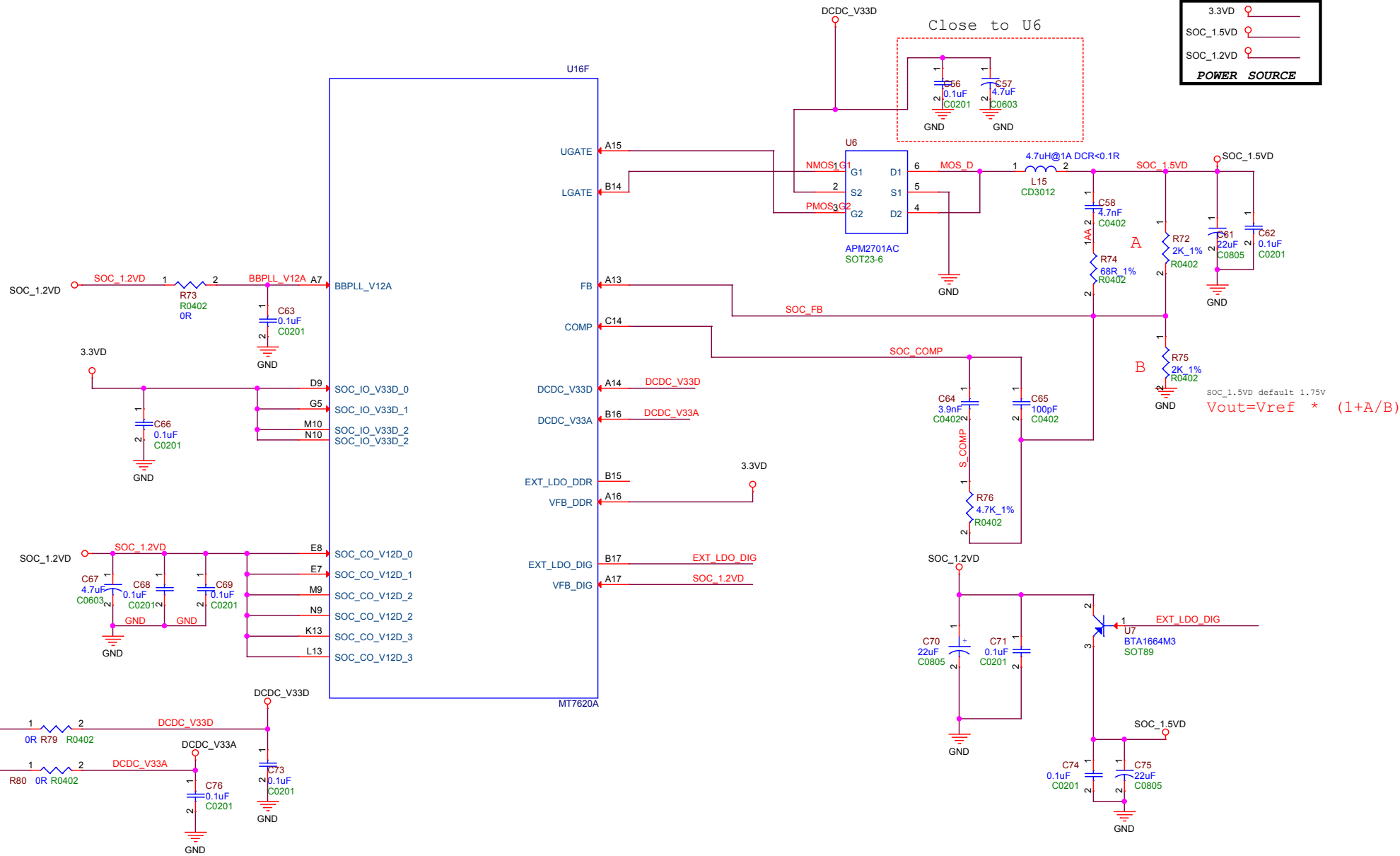
R15 2 1
R71 6.19K 1% R0402
GND

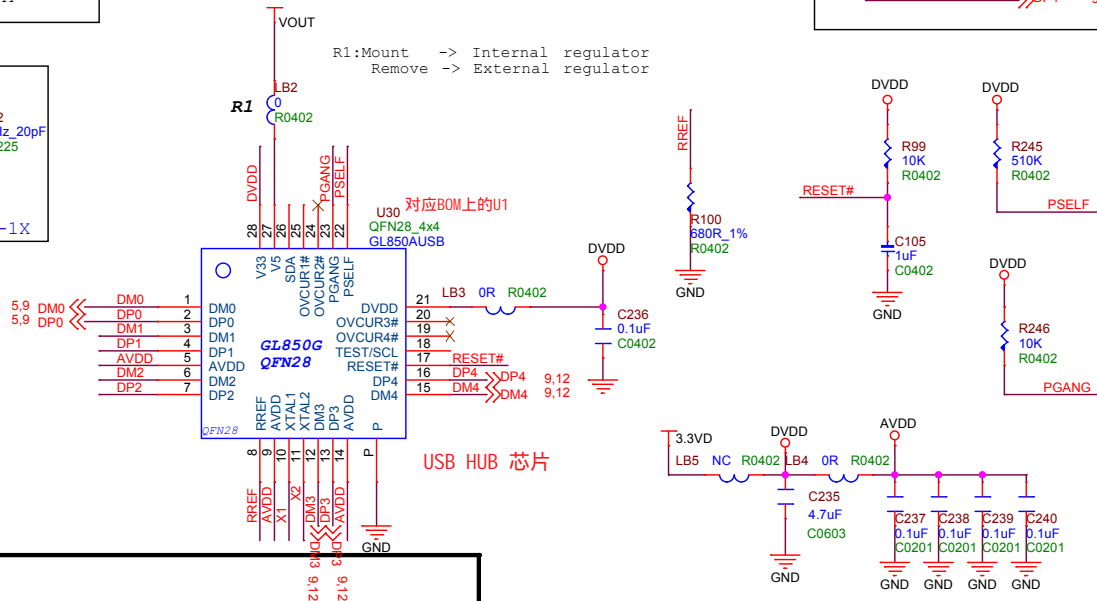
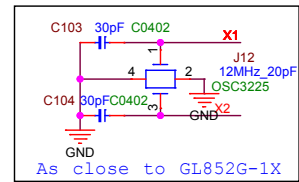
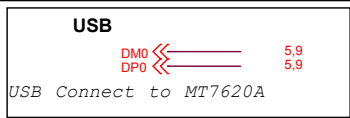
3.3VD
SOC_1.2VD
POWER SOURCE

FOCAL CREST 致趣

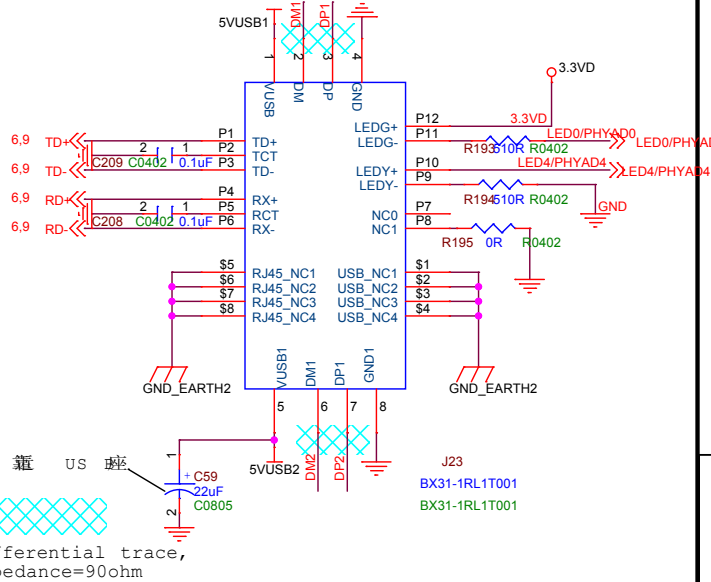
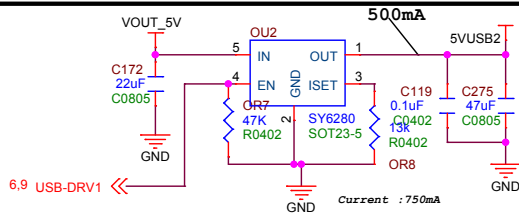
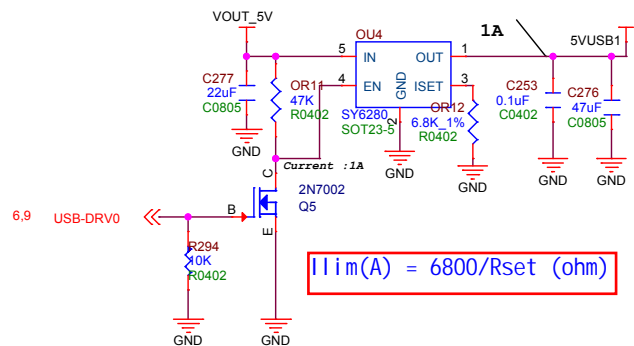
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Design Name		WIUSB	
Size	Page Name	Rev	
A	Ethernet	v1.5	
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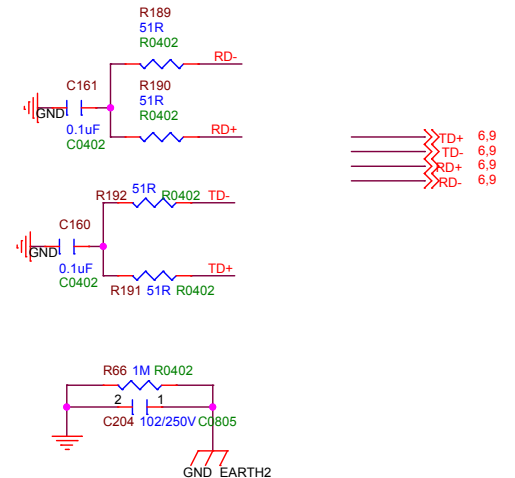
Set Current



USB SWITCH

6.9 USB-DRV0 <<<
6.9 USB-DRV1 <<<

USB Enable



FOCAL CREST 致趣

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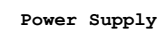
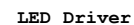
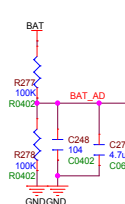
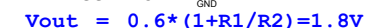
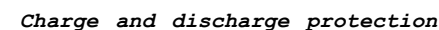
Design Name
WIUSB

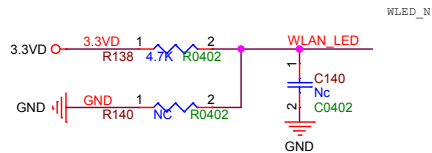
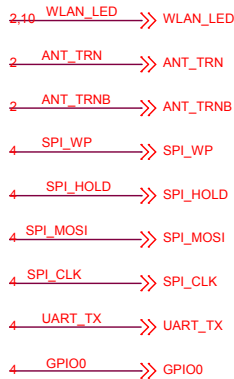
Size B Page Name
USB HUB

Date: Thursday, July 21, 2016

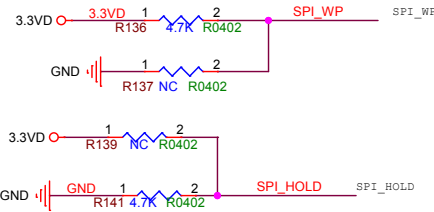
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Rev
v1.5

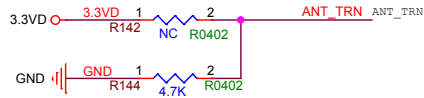




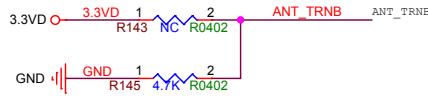
DRAM configuration



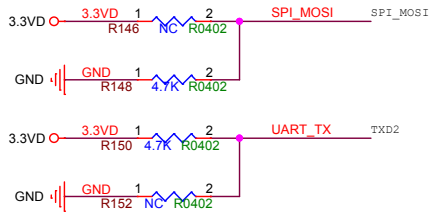
DRAM_TYPE



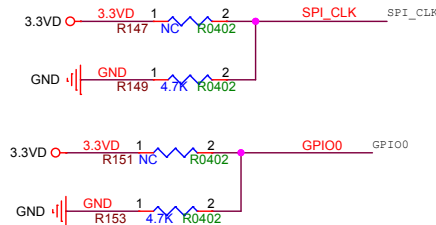
DBG_JTAG_MODE



XTAL_FREQ_SEL




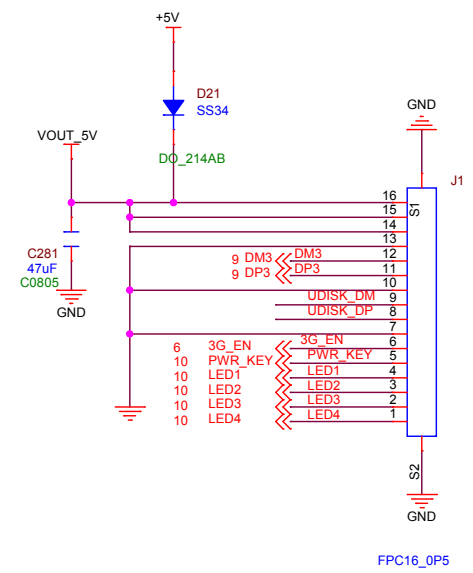
CHIP_MODE[3:0] Boot from



MT7620 Boot Up Strapping

Pin Name	Description	Value
WLED_N	DRAM_FROM_EE For non scan mode:	0: DRAM configuration from EEPROM 1: DRAM configuration from Auto Detect
ANT_TRN	DBG_JTAG_MODE	0: EPHY_LED 1: JTAG MODE
ANT_TRNB	XTAL_FREQ_SEL	0: 20MHz
{SPI_WP, SPI_HOLD}	DRAM_TYPE	01: DDR1 (CPU/3) TSOP 10: DDR2 (CPU/3) FBGA 11: SDRAM (CPU/5) (LVTTL 3.3 V) TSOP Package
{SPI_MOSI, SPI_CLK, TXD2, GPIO0 }	CHIP_MODE[3:0]	A vector to set chip function/test/debug modes. In non-test/debug operation, 0001: Normal mode (boot from ROM+NAND flash 4 cycle address) 0010: Normal mode (boot from SPI 3Byte Addr) 0011: Normal mode (boot from SPI 4Byte Addr) 0100 :iNIC RGMII mode(boot from ROM) 0101: iNIC MII mode(boot from ROM) 0110: iNIC RVIII mode(boot from ROM) 0111: iNIC PHY mode(boot from ROM) 1000: iNIC USB mode(boot from ROM) 1001: iNIC PCIe mode(boot from ROM)

		Focalcrest, Co. Ltd.					
		Design Name					
		WIUSB					
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OEb	Sel	Function
1	X	Disconnect
0	0	Dp, Dn=Dp1, Dn1
0	1	Dp, Dn=Dp2, Dn2