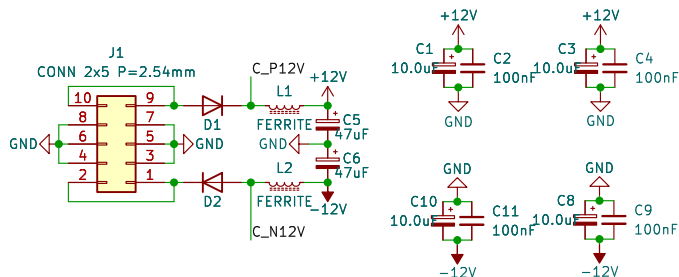
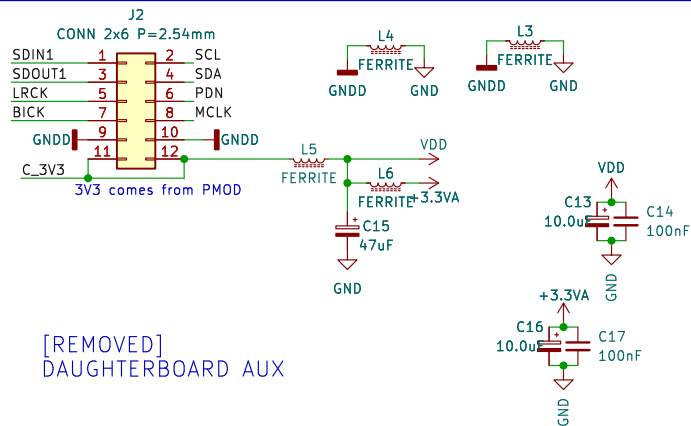


## +/- 12V IN



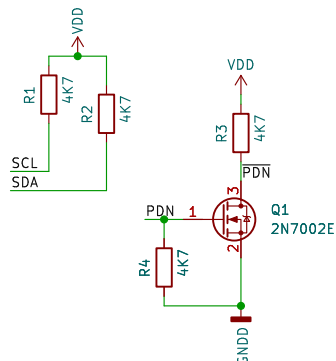
## PMOD, +3.3V IN



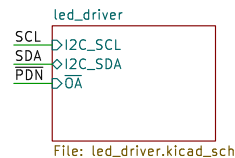
[REMOVED]  
DAUGHTERBOARD AUX

## INPUT CONDITIONING

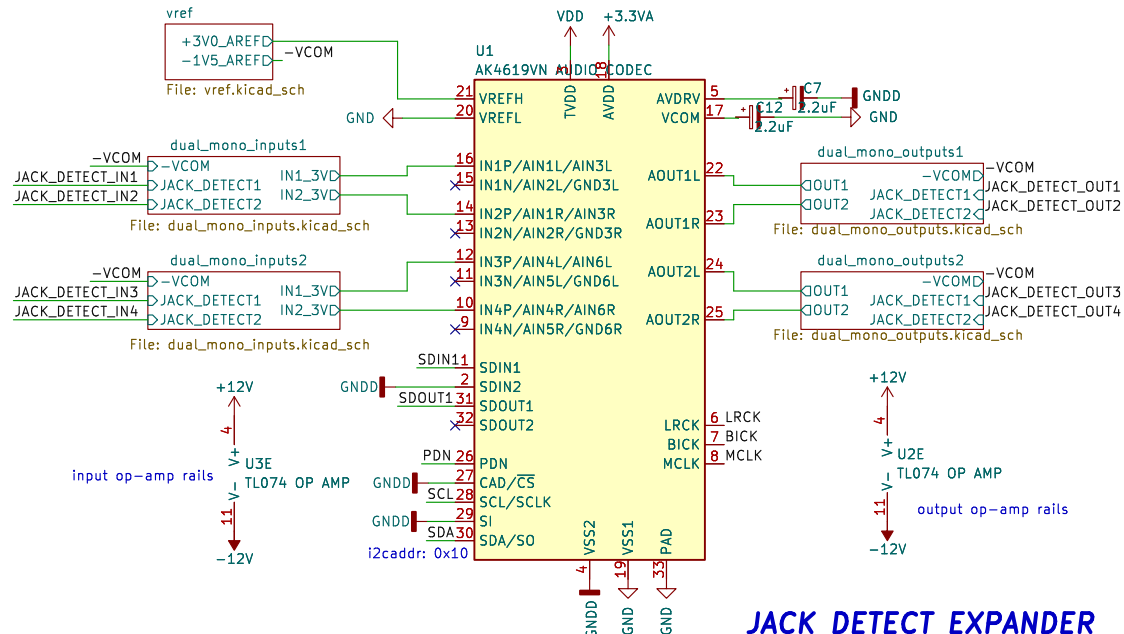
[REMOVED]  
INPUT ESD CLAMPS



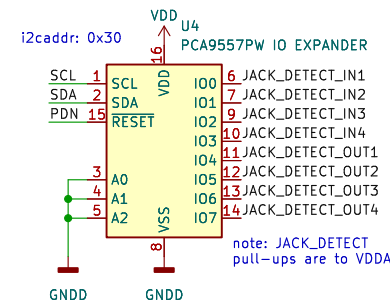
## LED DRIVER



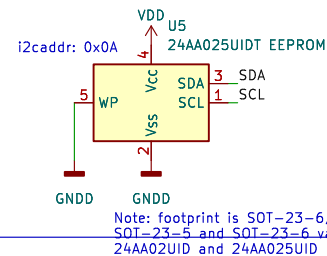
## CODEC, IN+OUT



## JACK DETECT EXPANDER



## CAL MEMORY



Sheet: /  
File: eurorack-pmod-pcb.kicad\_sch

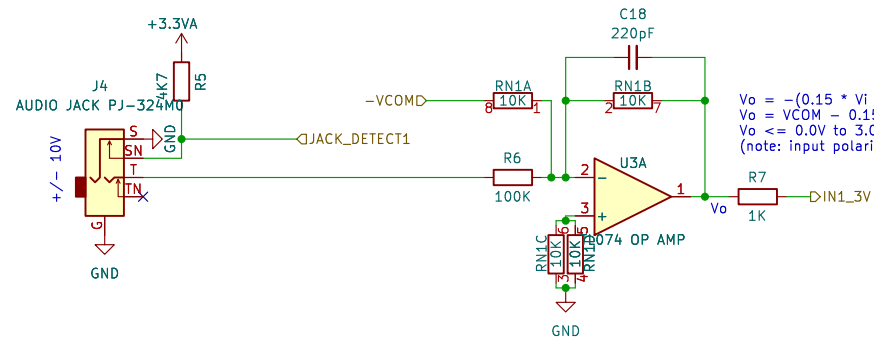
Title:

Size: A4  
KiCad E.D.A. kicad 7.0.9

Date:

Rev:

Id: 1/7



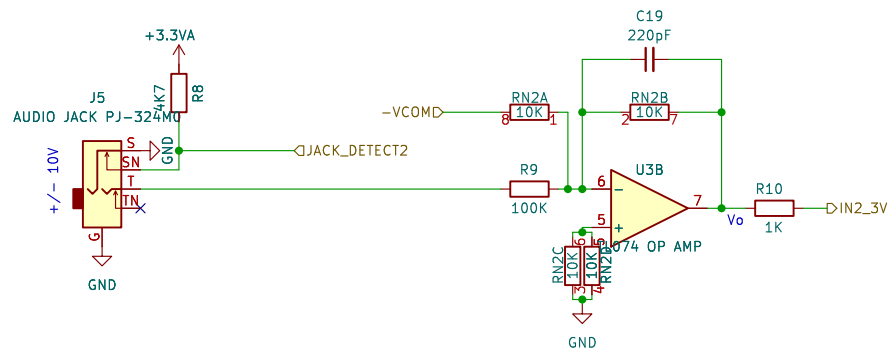
$$V_o = -(0.15 * V_i + -V_{COM})$$

$$V_o = V_{COM} - 0.15 * V_{in}$$

$$V_o \leq 0.0V \text{ to } 3.0V, V_{in} \text{ inverted}$$

(note: input polarity inverted here)

VINA (ADC absolute max.) = -0.3V to 3.6V  
 $V_o = VINA = V_{COM} - 0.15 * V_{in}$   
 $V_{in} \text{ (max. tolerated) } = -13V \text{ to } +13V$   
 Outside +/- 12V is impossible, no need for diode clamps before ADC



Sheet: /dual\_mono\_inputs1/  
 File: dual\_mono\_inputs.kicad\_sch

# **Title:**

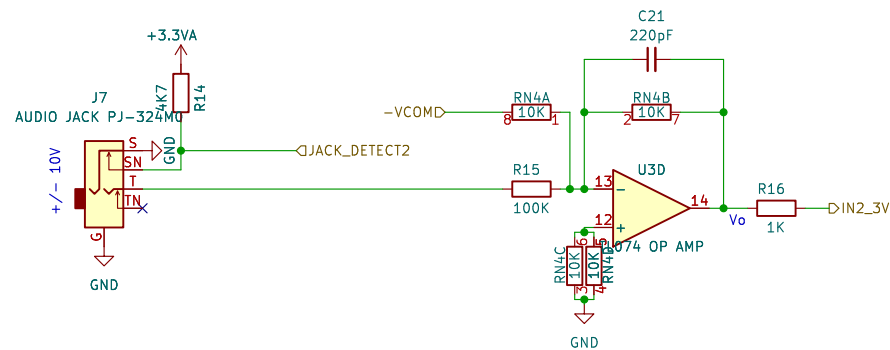
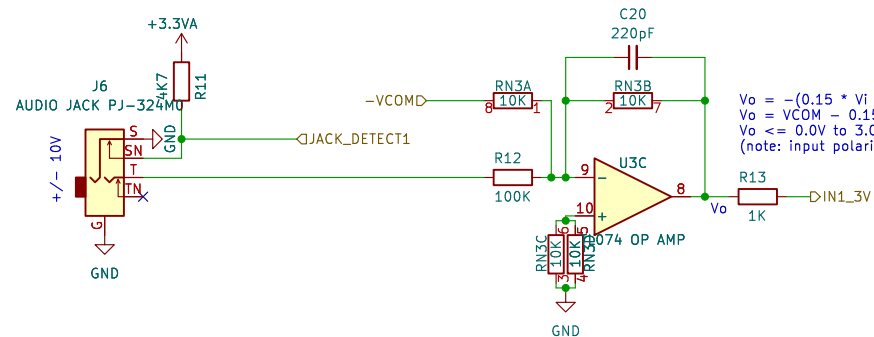
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Sheet: /dual\_mono\_inputs2/  
File: dual\_mono\_inputs.kicad\_sch

**Title:**

Size: A4

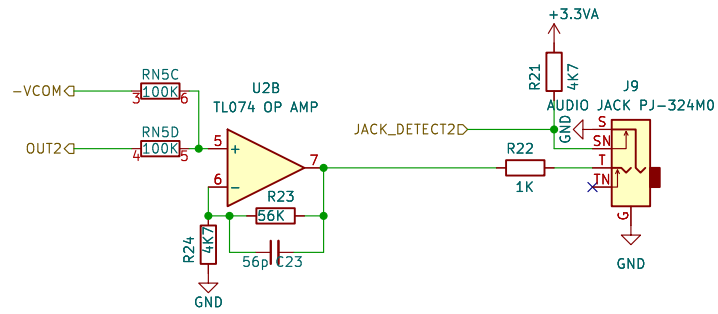
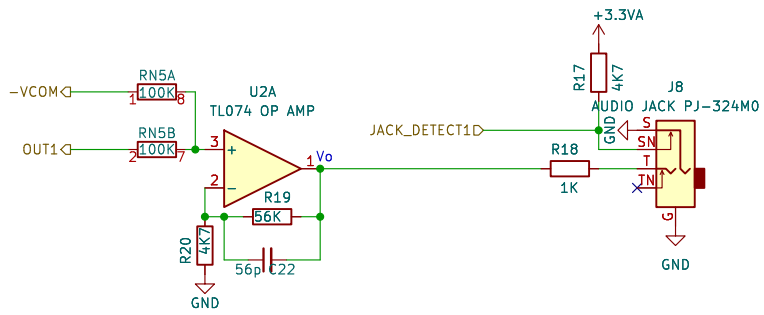
Date:

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**Rev:**

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$V_o = (1 + 56/4.7)(AOUT - VCOM)/2$   
 $V_o = 6.46 * (AOUT - 1.65V)$   
 0.1V in => -10V out, 3.2V in => +10V out



Sheet: /dual\_mono\_outputs1/  
 File: dual\_mono\_outputs.kicad\_sch

**Title:**

Size: A4

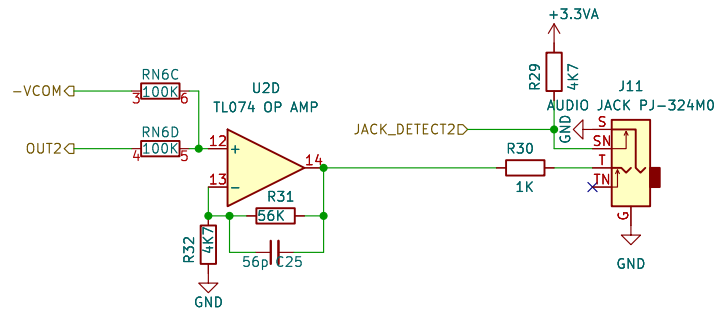
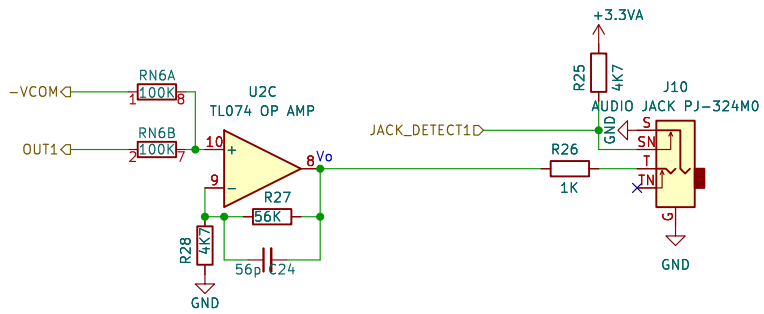
Date:

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**Rev:**

Id: 4/7

$V_o = (1 + 56/4.7)(AOUT - VCOM)/2$   
 $V_o = 6.46 * (AOUT - 1.65V)$   
 0.1V in => -10V out, 3.2V in => +10V out



Sheet: /dual\_mono\_outputs2/  
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**Title:**

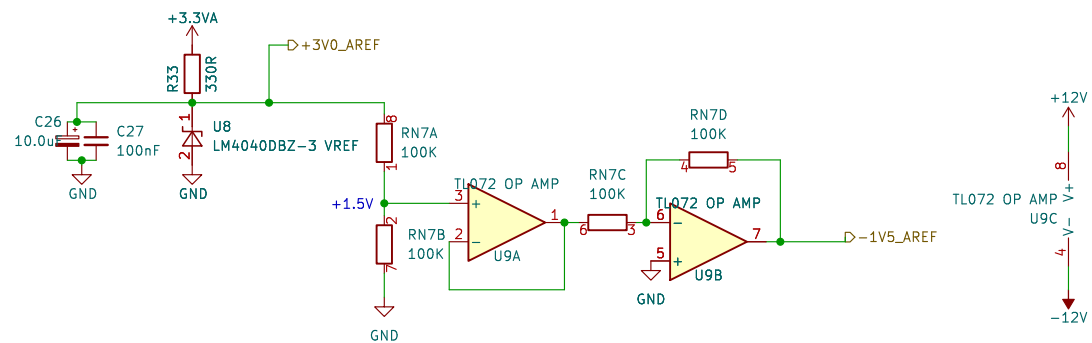
Size: A4

Date:

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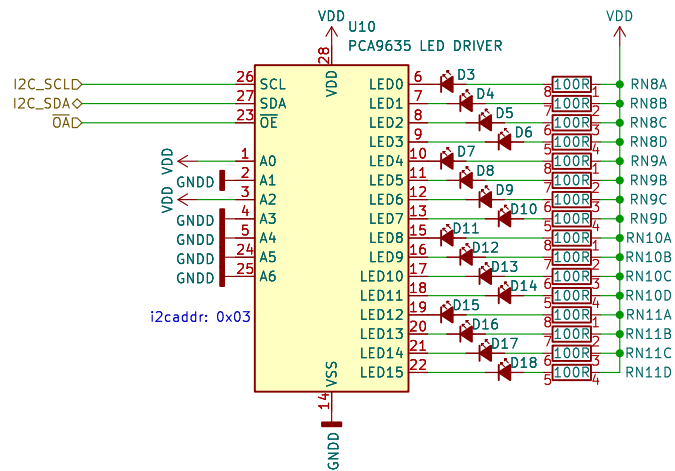
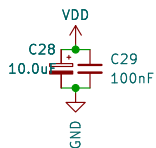
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**Title:**

Size: A4  
KiCad E.D.A. kicad 7.0.9

Date:

Rev:  
Id: 6/7



Sheet: /led\_driver/  
File: led\_driver.kicad\_sch

**Title:**

Size: A4

Date:

KiCad E.D.A. kicad 7.0.9

**Rev:**

Id: 7/7