

# CS122A: Intermediate Embedded and Real Time Operating Systems

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# Using sleep to reduce power consumption

- ▶ Microcontrollers consume power while running programs
- ▶ Instructions such as:

```
while (!timerFlag) { ... }
```

- ▶ These loops are wasteful because nothing can happen until the *ISR* is called
- ▶ Microcontrollers have a **sleep** mode which consumes less power
- ▶ The execution of the program is stopped in **sleep** mode, but the hardware continues to function.

# Using sleep to reduce power consumption

- ▶ In general it is challenging to know when to call sleep
- ▶ The disciplined SynchSM approach simplifies the issue.
- ▶ **Sleep** can be called while the user waits for *TimerISR* to be called

```
while(1) {  
    Sleep(); // Put processor in low-power mode  
    while(!TimerFlag); // Once ISR is called  
    // processor wakes up and continues executing  
}
```

## Using sleep to reduce power consumption

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$$Lifetime = 1,000J * (1s/0.001J)$$

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$$Lifetime = 1,000,000s$$

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$$\textit{Lifetime} = 277\text{hours}$$



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- ▶ If the microcontroller is always running:

$$Lifetime = 11days$$

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- ▶ A small battery stores 1,000 Joules (J)
- ▶ If the microcontroller is always running:

$$Lifetime = 11days$$

- ▶ If the microcontroller is asleep 99% of the time:

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- ▶ and  $1\mu\text{W}$  when asleep
- ▶ A small battery stores 1,000 Joules (J)
- ▶ If the microcontroller is always running:

$$\textit{Lifetime} = 11\text{days}$$

- ▶ If the microcontroller is asleep 99% of the time:

$$\textit{Lifetime} = 1,000\text{J} * (1\% * 1/0.001) + (99\% * (1/0.000001))$$

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- ▶ Assume a microcontroller consumes  $1mW$  while running
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- ▶ A small battery stores 1,000 Joules (J)
- ▶ If the microcontroller is always running:

$$Lifetime = 11days$$

- ▶ If the microcontroller is asleep 99% of the time:

$$Lifetime = 990,010,000s$$

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- ▶ A small battery stores 1,000 Joules (J)
- ▶ If the microcontroller is always running:

$$Lifetime = 11days$$

- ▶ If the microcontroller is asleep 99% of the time:

$$Lifetime = 275,002hours$$

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- ▶ Assume a microcontroller consumes  $1mW$  while running
- ▶ and  $1\mu W$  when asleep
- ▶ A small battery stores 1,000 Joules (J)
- ▶ If the microcontroller is always running:

$$Lifetime = 11days$$

- ▶ If the microcontroller is asleep 99% of the time:

$$Lifetime = 11,458days$$

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- ▶ Assume a microcontroller consumes  $1mW$  while running
- ▶ and  $1\mu W$  when asleep
- ▶ A small battery stores 1,000 Joules (J)
- ▶ If the microcontroller is always running:

$$Lifetime = 11days$$

- ▶ If the microcontroller is asleep 99% of the time:

$$Lifetime = 11,458days$$

- ▶ 1,041.65x as long when asleep 99% of the time

# Using sleep to reduce power consumption

- ▶ Aperiodic tasks allow for more optimization
- ▶ Dynamically change the Timer's tick rate to GCD of *active* tasks

## Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms



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- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 100ms

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Task 3: SynchSM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 100ms

$$Utilization = ((10 * 2) + (50 * 1) + (5 * 10))/1000$$

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- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 100ms

*Utilization* = 120/1000

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Task 1: SynchSM

- ▶ WCET 10 ms
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Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: SynchSM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 100ms

*Utilization* = 12%

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Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: SynchSM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 100ms

*Utilization* = 12%

*Lifetime* =  $1,000J * (12\% * 1/0.001) + (88\% * (1/0.000001))$

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Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: SynchSM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 100ms

*Utilization* = 12%

*Lifetime* = 880,120,000s



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- ▶ Aperiodic tasks allow for more optimization
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Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: SynchSM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 100ms

*Utilization* = 12%

*Lifetime* = 244,478hours

## Using sleep to reduce power consumption

- ▶ Aperiodic tasks allow for more optimization
- ▶ Dynamically change the Timer's tick rate to GCD of *active* tasks

Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: SynchSM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 100ms

*Utilization* = 12%

*Lifetime* = 10,187 days

# Using sleep to reduce power consumption

- ▶ Aperiodic tasks allow for more optimization
- ▶ Dynamically change the Timer's tick rate to GCD of *active* tasks

Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

## Using sleep to reduce power consumption

- ▶ Aperiodic tasks allow for more optimization
- ▶ Dynamically change the Timer's tick rate to GCD of *active* tasks

Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

$$Utilization = ((10 * 2) + (50 * 1))/1000$$

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- ▶ Aperiodic tasks allow for more optimization
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Task 1: SynchSM

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Task 2: SynchSM

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- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

*Utilization* = 70/1000

# Using sleep to reduce power consumption

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Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

*Utilization = 7%*

## Using sleep to reduce power consumption

- ▶ Aperiodic tasks allow for more optimization
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Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

*Utilization* = 7%

*Lifetime* =  $1,000J * (7\% * 1/0.001) + (93\% * (1/0.000001))$

# Using sleep to reduce power consumption

- ▶ Aperiodic tasks allow for more optimization
- ▶ Dynamically change the Timer's tick rate to GCD of *active* tasks

Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

*Utilization* = 7%

*Lifetime* = 930,070,000s



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- ▶ Aperiodic tasks allow for more optimization
- ▶ Dynamically change the Timer's tick rate to GCD of *active* tasks

Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

*Utilization = 7%*

*Lifetime = 258,353hours*

# Using sleep to reduce power consumption

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Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

*Utilization = 7%*

*Lifetime = 10,764days*

# Using sleep to reduce power consumption

- ▶ Aperiodic tasks allow for more optimization
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Task 1: SynchSM

- ▶ WCET 10 ms
- ▶ Period 500 ms

Task 2: SynchSM

- ▶ WCET 50 ms
- ▶ Period 1000 ms

Task 3: Triggered SM

- ▶ WCET 5 ms
- ▶ Period 100 ms

**System Period:** 500ms

*Utilization* = 7%

*Lifetime* = 10,764days

1.06x as long