1. Description

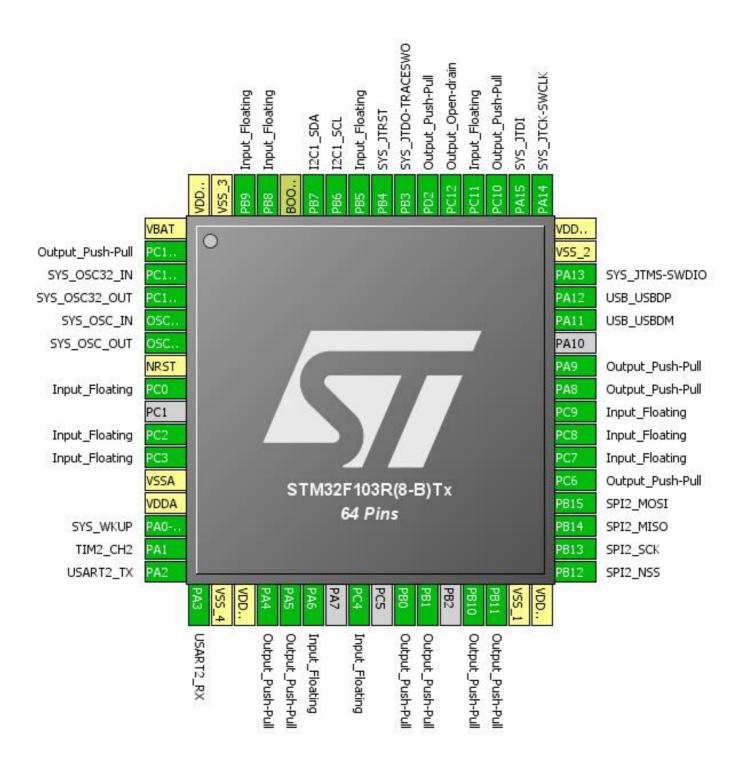
1.1. Project

Project Name	trecker
Generated with:	MicroXplorer V2.0
Date	03/14/2012

1.2. MCU

MCU family	STM32
MCU name	STM32F103R(8-B)Tx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



3. Peripherals Configuration

Peripheral	Mode	Remap	Fonction	Pin
SPI2 Full-Duplex-Master_With-NSS			SPI2_MISO	PB14
	5 11 B . I. M MEI NOO		SPI2_MOSI	PB15
	0	SPI2_NSS	PB12	
			SPI2_SCK	PB13
		I2C1_SCL	PB6	
I2C1	I2C	0	I2C1_SDA	PB7
LIGARTO	USART2 Asynchronous	0	USART2_RX	PA3
USAR12			USART2_TX	PA2
1100	1100 50		USB_USBDM	PA11
USB	USB_FS	0	USB_USBDP	PA12
			SYS_JTCK-SWCLK	PA14
			SYS_JTDI	PA15
	JTAG(5-pin)	0	SYS_JTDO-TRACESWO	PB3
		SYS_JTMS-SWDIO	PA13	
		SYS_JTRST	PB4	
SYS SYS-OSC SYS-OSC32	0	SYS_OSC_IN	OSC_IN-PD0	
		SYS_OSC_OUT	OSC_OUT-PD1	
	000000		SYS_OSC32_IN	PC14-OSC32_IN
	0	SYS_OSC32_OUT	PC15-OSC32_OUT	
	SYS-WKUP	0	SYS_WKUP	PA0-WKUP

4. Pins configuration

Pins	Pos	Functions	
PC13-ANTI_TAMP *	2	Output_Push-Pull	
PC14-OSC32_IN	3	SYS_OSC32_IN	
PC15-OSC32_OUT	4	SYS_OSC32_OUT	
OSC_IN-PD0	5	SYS_OSC_IN	
OSC_OUT-PD1	6	SYS_OSC_OUT	
PC0 *	8	Input_Floating	
PC2 *	10	Input_Floating	
PC3 *	11	Input_Floating	
PA0-WKUP	14	SYS_WKUP	
PA1 **	15	TIM2_CH2	
PA2	16	USART2_TX	
PA3	17	USART2_RX	
PA4 *	20	Output_Push-Pull	
PA5 *	21	Output_Push-Pull	
PA6 *	22	Input_Floating	
PC4 *	24	Input_Floating	
PB0 *	26	Output_Push-Pull	
PB1 *	27	Output_Push-Pull	
PB10 *	29	Output_Push-Pull	
PB11 *	30	Output_Push-Pull	
PB12	33	SPI2_NSS	
PB13	34	SPI2_SCK	
PB14	35	SPI2_MISO	
PB15	36	SPI2_MOSI	
PC6 *	37	Output_Push-Pull	
PC7 *	38	Input_Floating	
PC8 *	39	Input_Floating	
PC9 *	40	Input_Floating	
PA8 *	41	Output_Push-Pull	
PA9 *	42	Output_Push-Pull	
PA11	44	USB_USBDM	
PA12	45	USB_USBDP	
PA13	46	SYS_JTMS-SWDIO	
PA14	49	SYS_JTCK-SWCLK	
PA15	50	SYS_JTDI	
PC10 *	51	Output_Push-Pull	
PC11 *	52	Input_Floating	
PC12 *	53	Output_Open-drain	
PD2 *	54	Output_Push-Pull	

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Pins	Pos	Functions	
PB3	55	SYS_JTDO-TRACESWO	
PB4	56	SYS_JTRST	
PB5 *	57	Input_Floating	
PB6	58	I2C1_SCL	
PB7	59	I2C1_SDA	
PB8 *	61	Input_Floating	
PB9 *	62	Input_Floating	

^{*} The pin is affected with an I/O function

^{**} The pin is affected with a peripheral function but no peripheral mode is activated