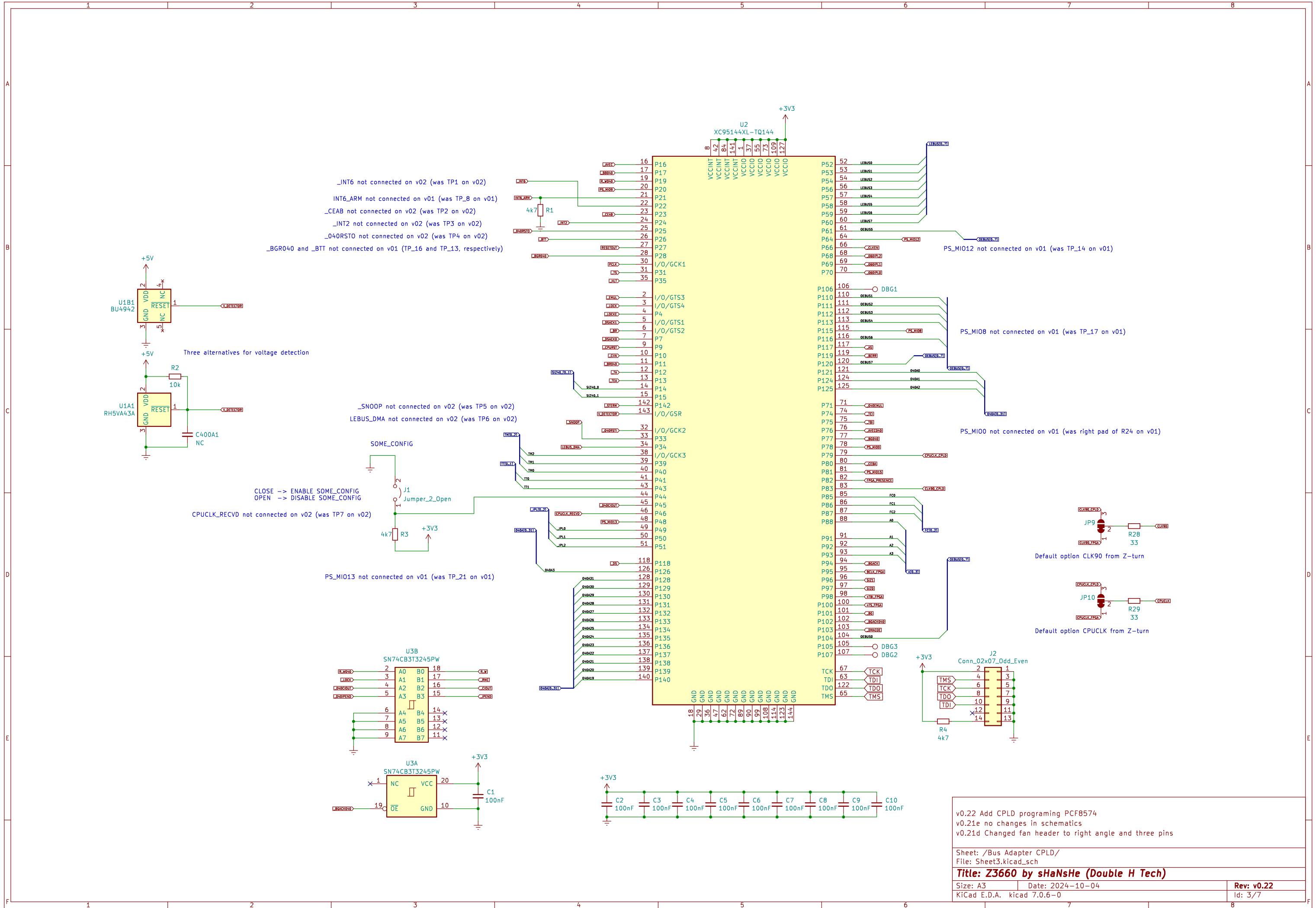
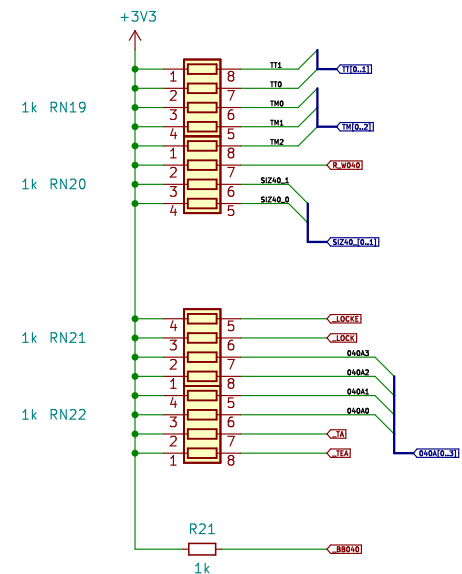
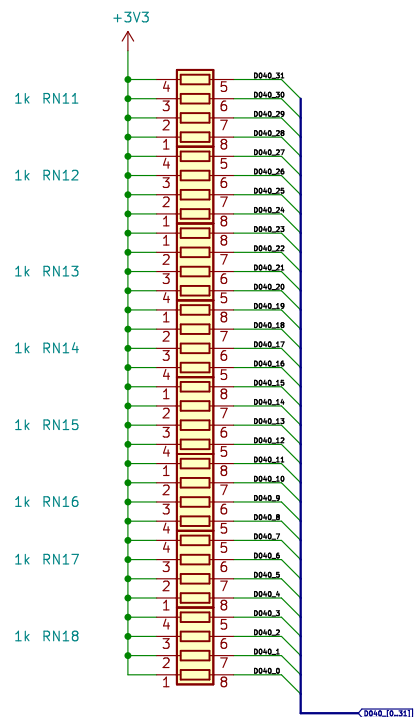


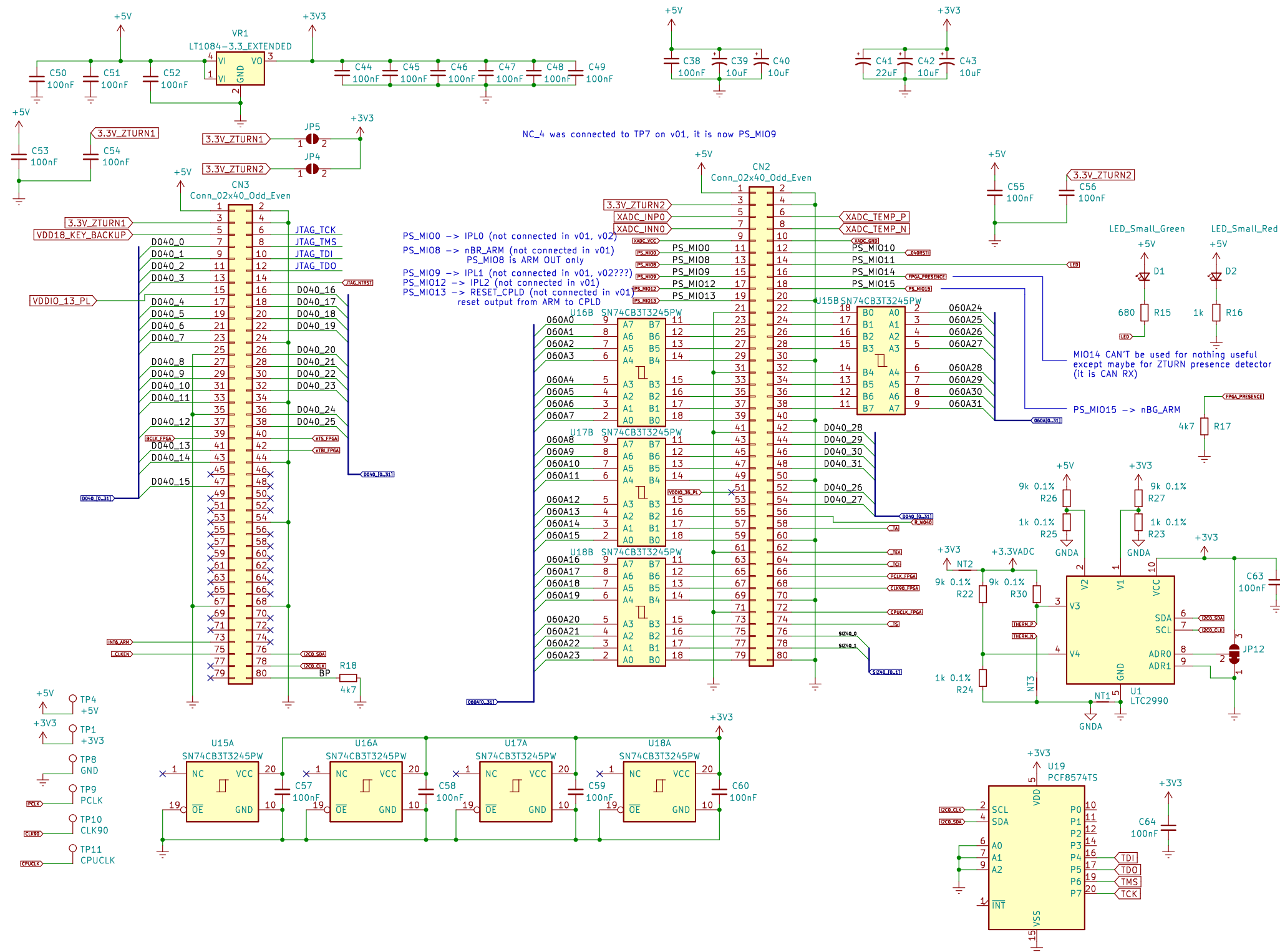
v0.22 Add CPLD programing PCF8574  
v0.21e no changes in schematics  
v0.21d Changed fan header to right angle and three pins

Sheet: /Data Bus Buffer/ File: Sheet2.kicad_sch		
Title: Z3660 by sHaNsHe (Double H Tech)		
Size: A3	Date: 2024-10-04	Rev: v0.22
KiCad E.D.A. kicad 7.0.6-0		Id: 2/7





Id: 4/7



v0.22 Add CPLD programing PCF8574  
v0.21e no changes in schematics  
v0.21d Changed fan header to right angle and three pins

## CPU and CLK



File: Sheet1.kicad\_sch

ZTURN and 3.3V



File: Sheet5.kicad\_sch

## Torsti76's V Regulator



File: Sheet6.kicad\_sch

## Bus Adapter CPLD



File: Sheet3.kicad\_sch

## Data Bus Buffer



File: Sheet2.kicad\_sch

## CPU Connector



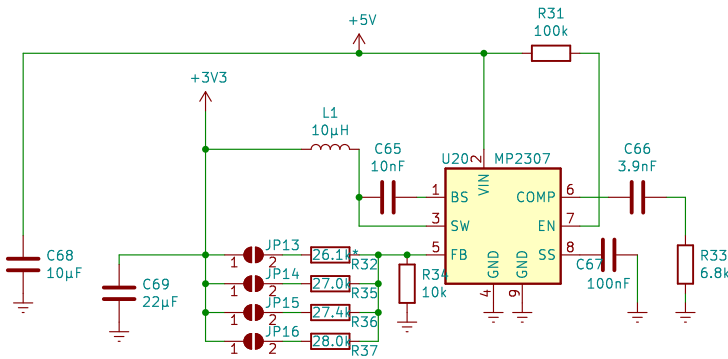
File: Sheet4.kicad\_sch

- v0.22 Add CPLD programing PCF8574
- v0.21e no changes in schematics
- v0.21d Changed fan header to right angle and three pins

Sheet: /  
File: Z3660\_v022.kicad\_sch

**Title: Z3660 by sHaNsHe (Double H Tech)**

Size: A3	Date: 2024-10-04	Rev: v0.22
KiCad E.D.A.	kiCad 7.0.6-0	Id: 6/7



\* R32 value vs. VOUT  
26.1k - 3.34 V  
27.0k - 3.42 V  
27.4k - 3.46 V  
28.0k - 3.52 V  
  
 $R32 = 10.81 \cdot (VOUT - 0.925) \text{ k}\Omega$   
 $VOUT = 0.0925 \cdot (R32 + 10) \text{ k}\Omega$

v0.22 Add CPLD programing PCF8574  
v0.21e no changes in schematics  
v0.21d Changed fan header to right angle and three pins

Sheet: /Torsti76's V Regulator/  
File: Sheet6.kicad\_sch

**Title: Z3660 by sHaNsHe (Double H Tech)**

Size: A3	Date: 2024-10-04	Rev: v0.22
KiCad E.D.A. kicad 7.0.6-0		Id: 7/7