

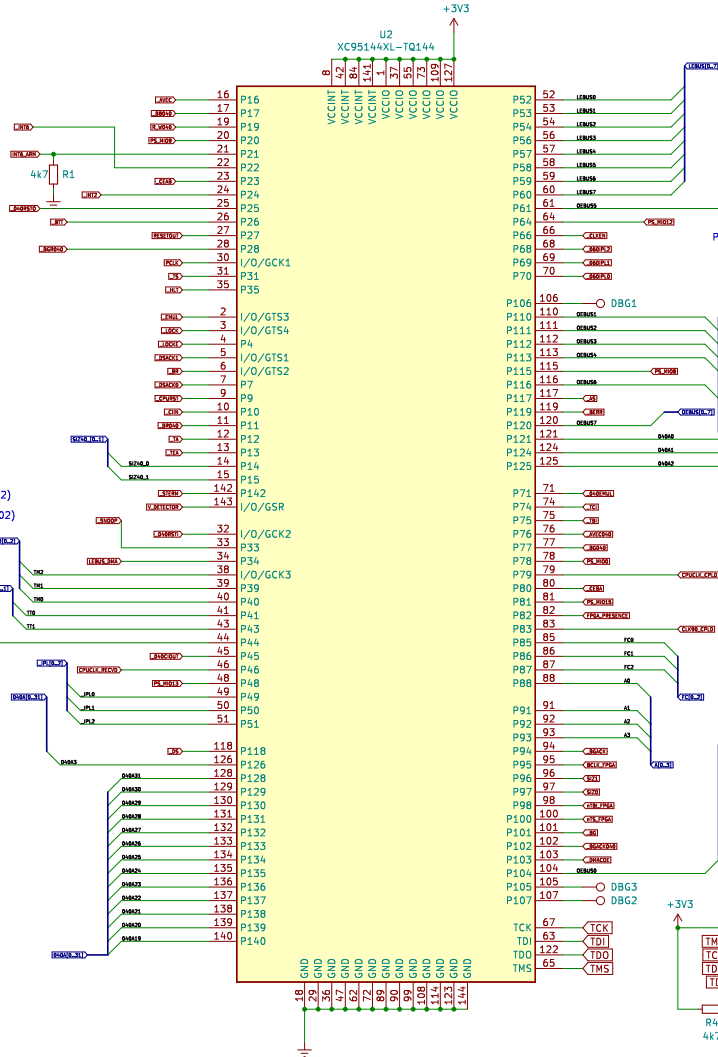
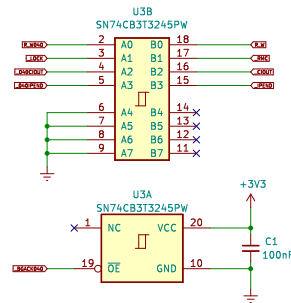
_INT5 not connected on v02 (was TP1 on v02)
 INT6_ARM not connected on v01 (was TP_8 on v01)
 _CEAB not connected on v02 (was TP2 on v02)
 _INT2 not connected on v02 (was TP3 on v02)
 _04ORST0 not connected on v02 (was TP4 on v02)
 _BGR040 and _BTI not connected on v01 (TP_16 and TP_13, respectively)

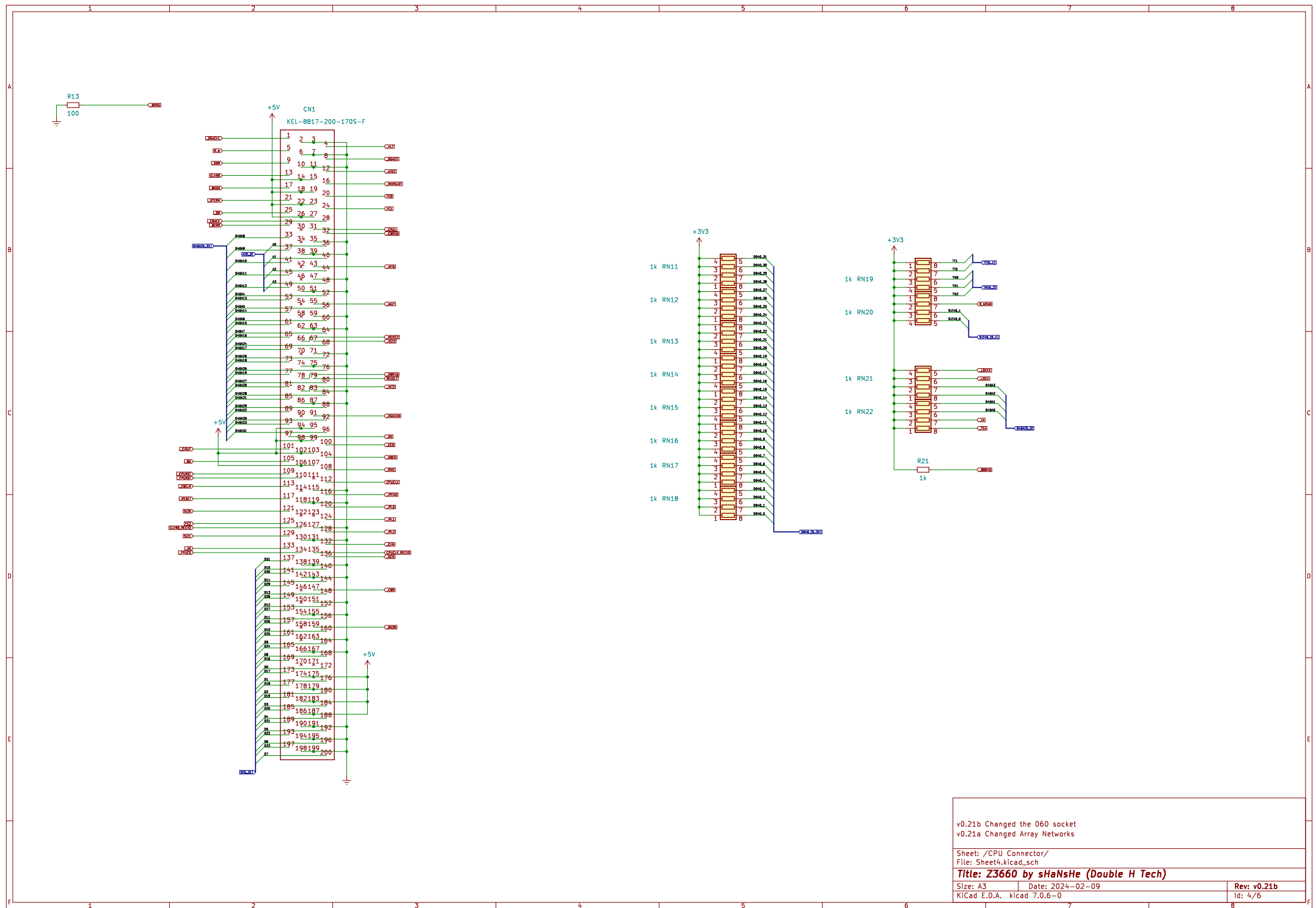
_SNOOP not connected on v02 (was TP5 on v02)
 LEBUS_DMA not connected on v02 (was TP6 on v02)

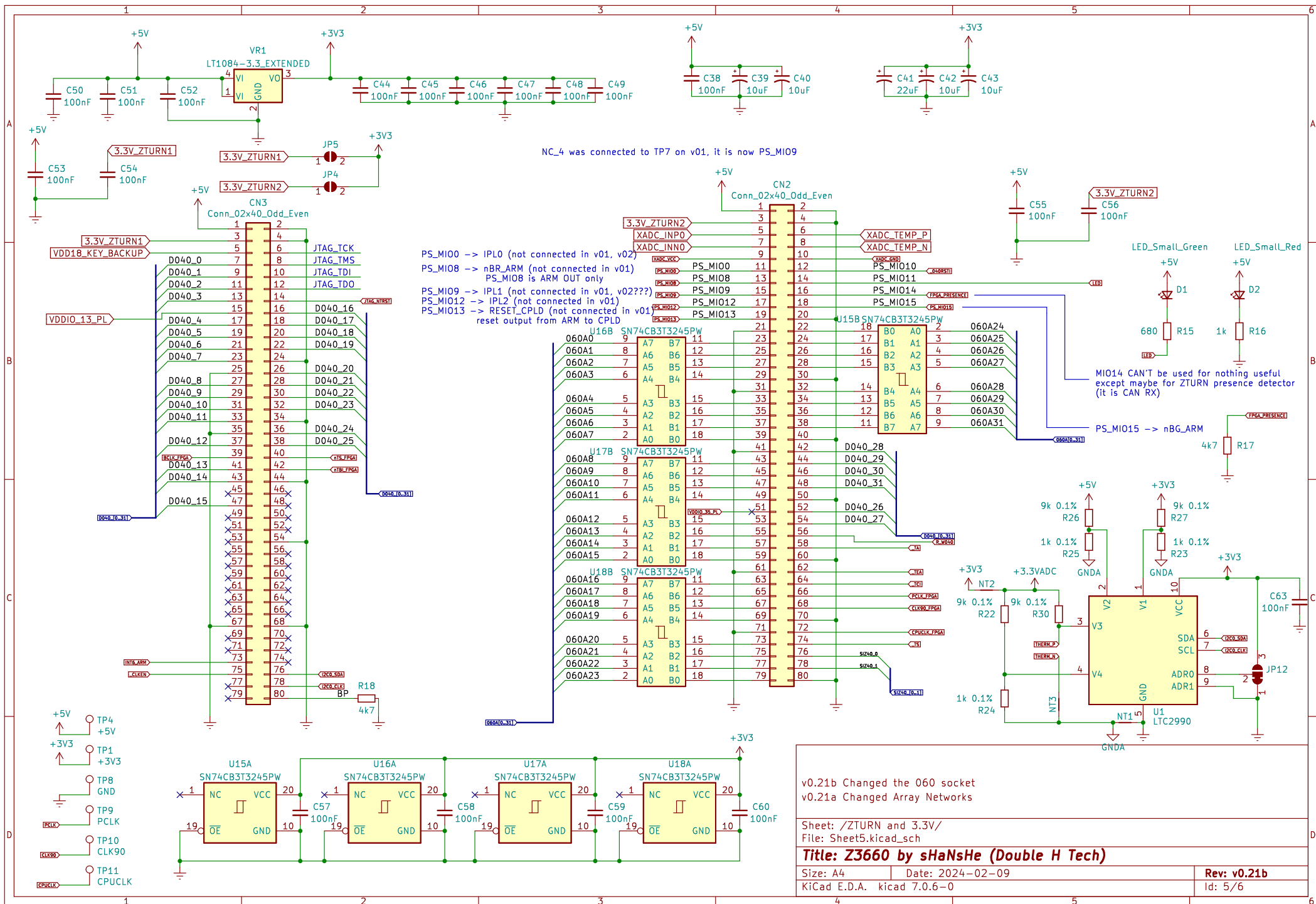
CLOSE -> ENABLE SOME_CONFIG
 OPEN -> DISABLE SOME_CONFIG






CPUCLK_RECVD not connected on v02 (was TP7 on v02)

PS_MIO13 not connected on v01 (was TP_21 on v01)







1	2	3	4	5	6
A	CPU and CLK		ZTURN and 3.3V		A
					
	File: Sheet1.kicad_sch		File: Sheet5.kicad_sch		
B	Bus Adapter CPLD		Data Bus Buffer	CPU Connector	B
					
	File: Sheet3.kicad_sch		File: Sheet2.kicad_sch	File: Sheet4.kicad_sch	
C					C
D					D
1	2	3	4	5	6

v0.21b Changed the 060 socket
v0.21a Changed Array Networks

Sheet: /
File: Z3660_v021b.kicad_sch

Title: Z3660 by sHaNsHe (Double H Tech)

Size: A4	Date: 2024-02-09	Rev: v0.21b
KiCad E.D.A. kicad 7.0.6-0		Id: 6/6