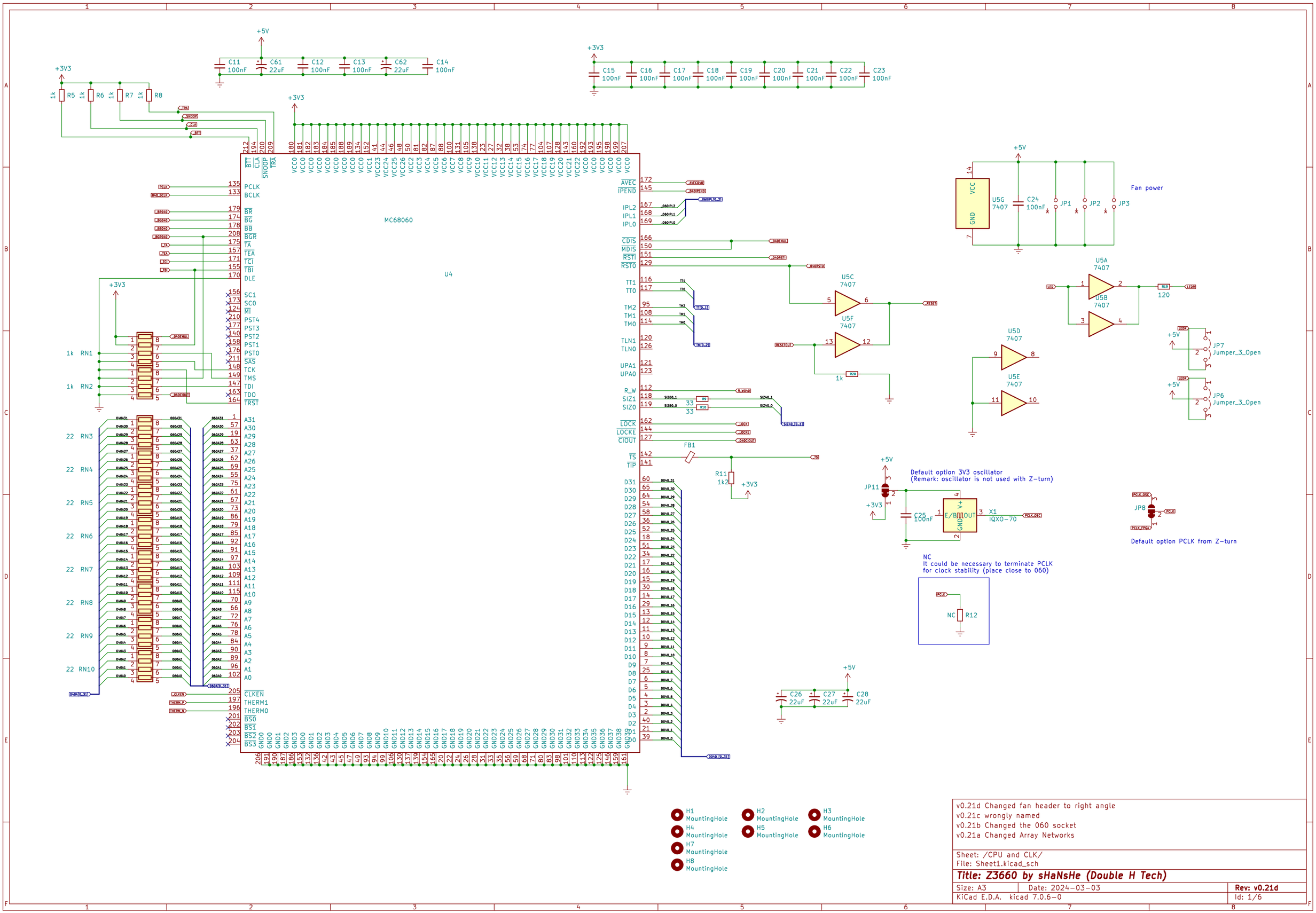
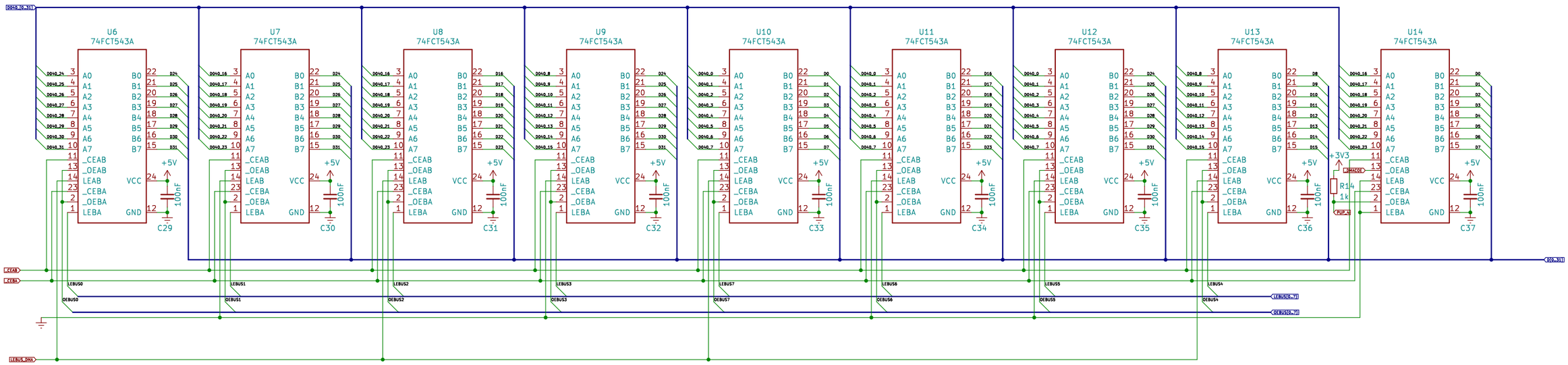


v0.21d Changed fan header to right angle
v0.21c wrongly named
v0.21b Changed the 060 socket
v0.21a Changed Array Networks

Sheet: /CPU and CLK/
File: Sheet1.kicad_sch

Title: Z3660 by sHaNsHe (Double H Tech)

Size: A3	Date: 2024-03-03	**Rev: v0.21d**
KiCad E.D.A. kicad 7.0.6-0		Id: 1/6



v0.21b Changed the 060 socket
v0.21a Changed Array Networks

Sheet: /Data Bus Buffer/		
File: Sheet2.kicad_sch		
Title: Z3660 by sHaNsHe (Double H Tech)		
Size: A3	Date: 2024-03-03	**Rev: v0.21d**
KiCad E.D.A. kicad 7.0.6-0		Id: 2/6

_INT6 not connected on v02 (was TP1 on v02)

INT6_ARM not connected on v01 (was TP_8 on v01)

_CEAB not connected on v02 (was TP2 on v02)

_INT2 not connected on v02 (was TP3 on v02)

_040RSTO not connected on v02 (was TP4 on v02)

_BGR040 and _BTT not connected on v01 (TP_16 and TP_13, respectively)

PS_MIO12 not connected on v01 (was TP_14 on v01)

PS_MIO8 not connected on v01 (was TP_17 on v01)

PS_MIO0 not connected on v01 (was right pad of R24 on v01)

_SNOOP not connected on v02 (was TP5 on v02)

LEBUS_DMA not connected on v02 (was TP6 on v02)

CPUCLK_RECVD not connected on v02 (was TP7 on v02)

PS_MIO13 not connected on v01 (was TP_21 on v01)

Three alternatives for voltage detection

Default option CLK90 from Z-turn

Default option CPUCLK from Z-turn

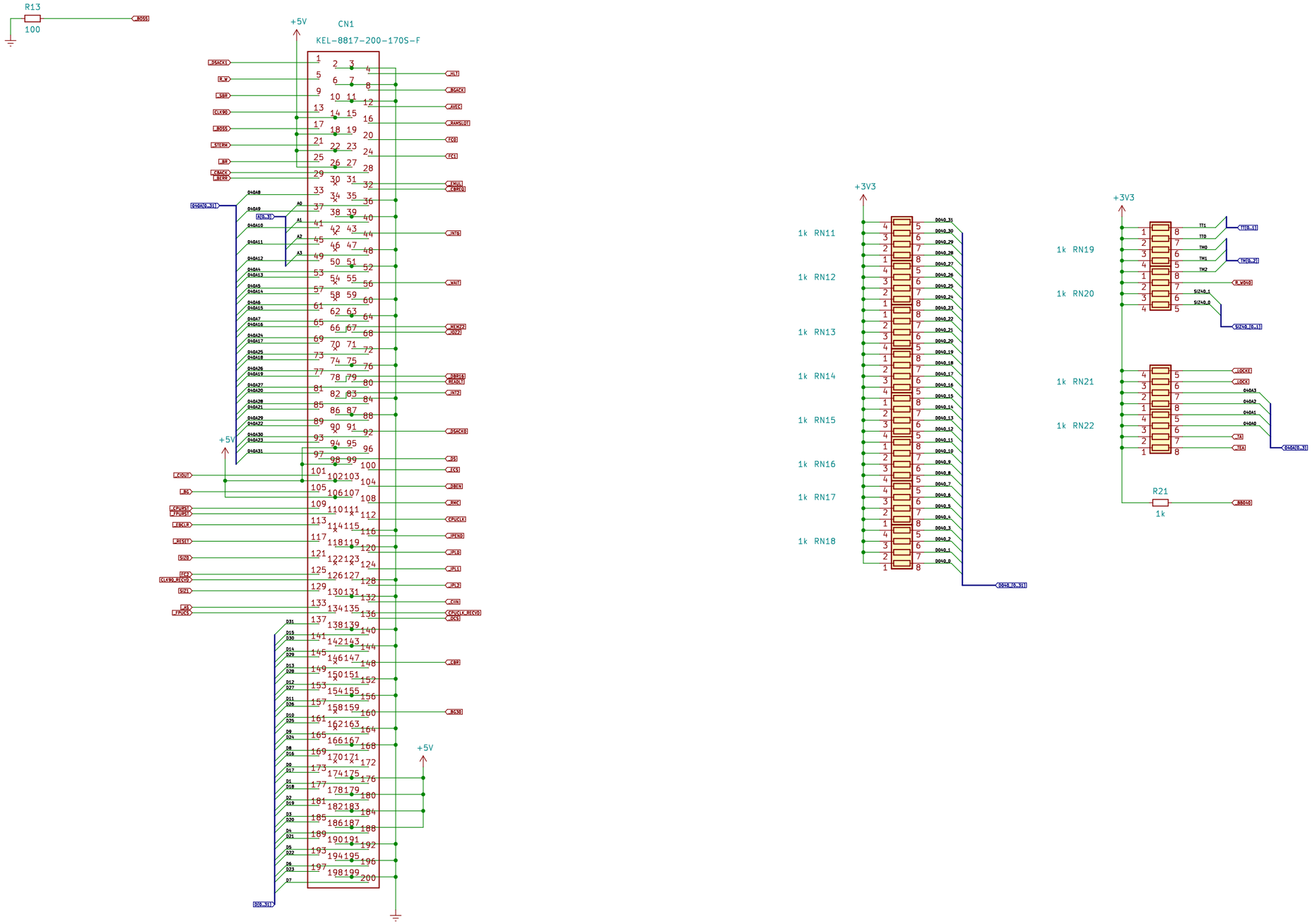
CLOSE -> ENABLE SOME_CONFIG
OPEN -> DISABLE SOME_CONFIG

v0.21b Changed the 060 socket
v0.21a Changed Array Networks

Sheet: /Bus Adapter CPLD/
File: Sheet3.kicad_sch

Title: Z3660 by sHaNsHe (Double H Tech)

Size: A3	Date: 2024-03-03	**Rev: v0.21d**
KiCad E.D.A. kicad 7.0.6-0		Id: 3/6



v0.21b Changed the 060 socket
v0.21a Changed Array Networks

Sheet: /CPU Connector/		
File: Sheet4.kicad_sch		
Title: Z3660 by sHaNsHe (Double H Tech)		
Size: A3	Date: 2024-03-03	**Rev: v0.21d**
KiCad E.D.A. kicad 7.0.6-0		Id: 4/6

NC_4 was connected to TP7 on v01, it is now PS_MIO9

PS_MIO0 –> IPL0 (not connected in v01, v02)
PS_MIO8 –> nBR_ARM (not connected in v01)
PS_MIO8 is ARM OUT only
PS_MIO9 –> IPL1 (not connected in v01, v02???)
PS_MIO12 –> IPL2 (not connected in v01)
PS_MIO13 –> RESET_CPLD (not connected in v01)
reset output from ARM to CPLD

MIO14 CAN'T be used for nothing useful
except maybe for ZTURN presence detector
(it is CAN RX)

PS_MIO15 –> nBG_ARM

v0.21b Changed the 060 socket
v0.21a Changed Array Networks

Sheet: /ZTURN and 3.3V/		
File: Sheet5.kicad_sch		
Title: Z3660 by sHaNsHe (Double H Tech)		
Size: A3	Date: 2024-03-03	**Rev: v0.21d**
KiCad E.D.A. kicad 7.0.6-0		Id: 5/6

CPU and CLK

File: Sheet1.kicad_sch

ZTURN and 3.3V

File: Sheet5.kicad_sch

Bus Adapter CPLD

File: Sheet3.kicad_sch

Data Bus Buffer

File: Sheet2.kicad_sch

CPU Connector

File: Sheet4.kicad_sch

v0.21d Changed fan header to right angle and three pins
v0.21c version wrongly named
v0.21b Changed the 060 socket
v0.21a Changed Array Networks

Sheet: /
File: Z3660_v021d.kicad_sch

Title: Z3660 by sHaNsHe (Double H Tech)

Size: A3	Date: 2024-03-03	**Rev: v0.21d**
KiCad E.D.A. kicad 7.0.6-0		Id: 6/6