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	CPU and CLK	ZTURN and 3.3V					
							A
-	File: Sheet1.kicad_sch	File: Sheet5.kicad_sch					
	Bus Adapter CPLD	Data Bus Buffer	CPU Connec	tor			
3							В
	511 61 1711						
	File: Sheet3.kicad_sch	File: Sheet2.kicad_sch	File: Sheet4.kicad	_scn			
							C
				Sheet: /			
1				Title: 2366	0_v021.kicad_sch 3660 by sHaNsHe	(Double H Tech)	
				Size: A4	Date: 2023-08	-06	Rev: v0.21
				KiCad E.D.	A. kicad 7.0.6-0		ld: 6/6